

Analog cell simulation

Laurent Blanquart

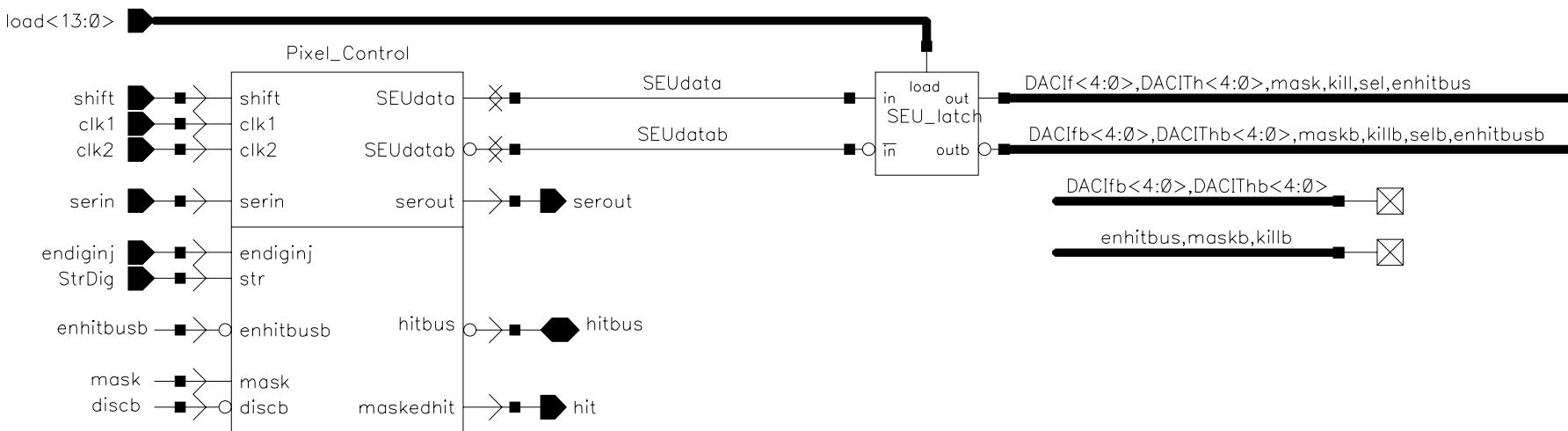
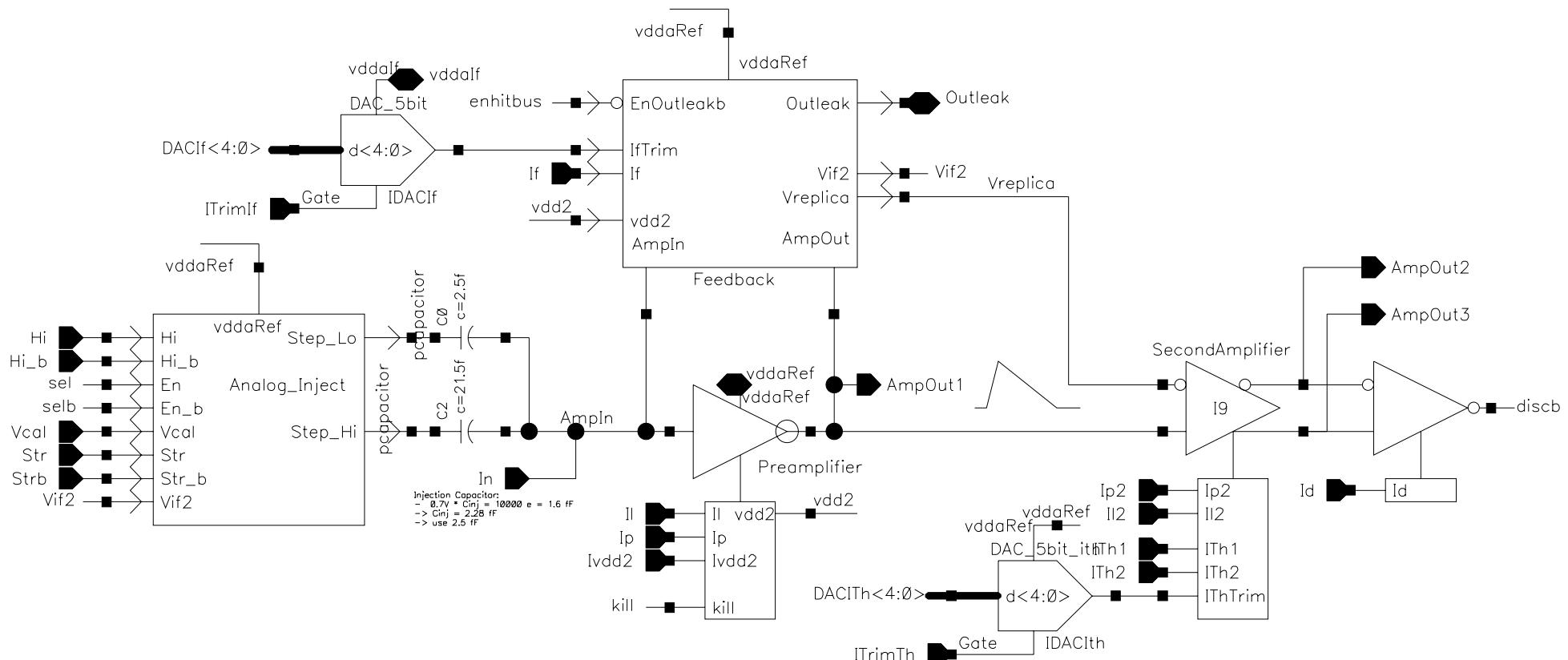
Requirements

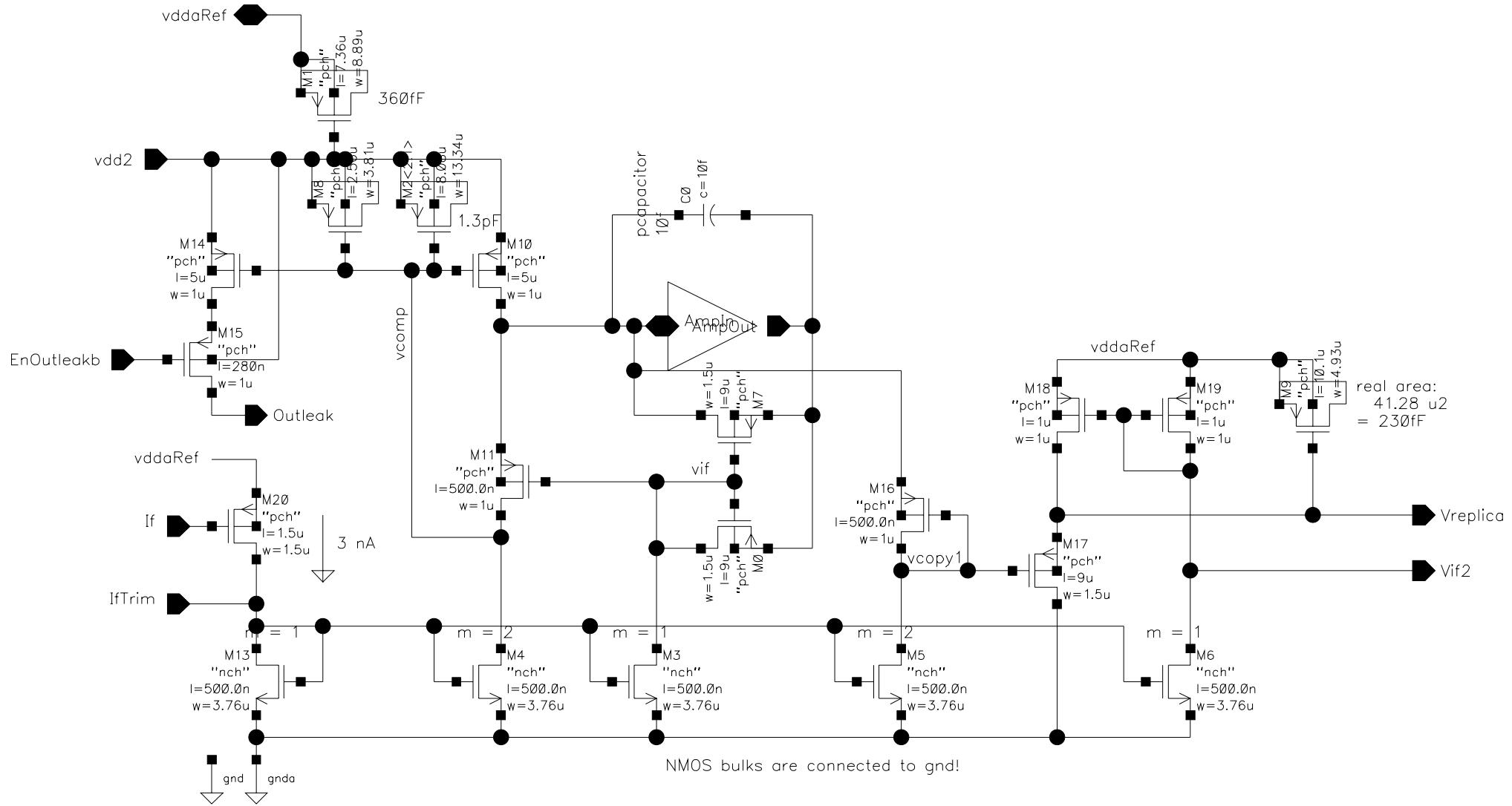
- 1) operate with nominal capacitive load of 400fF, about half parasitic to ground, and half to neighbors
- 2) end-of-lifetime signal is 10Ke after 10^{15} n equiv at 600V bias in oxygenated sensors. This means we want an in-time threshold of about 4Ke for full efficiency.
- 3) threshold dispersion less than about 200e (after tuning)
- 4) noise less than about 300e after irradiation, including series noise from 400fF and parallel noise from leakage current.
- 5) leakage current tolerance of up to 100nA
- 6) timewalk (time slewing from large charge to small charge) of less than 20ns for "in-time" threshold of 4Ke. For example, operate with a threshold of 3Ke and an overdrive for 20ns slewing of 1Ke.
- 7) noise occupancy less than 10-6 hits/crossing/pixel
- 8) double pulse resolution which can be tuned down to 500ns
- 9) modest charge measurement resolution of 4-5 bits
- 10) cross-talk less than 5-10% (pulse height in neighbor channel of 10-20 times threshold required to fire pixel)
- 11) threshold range of up to at least 6Ke

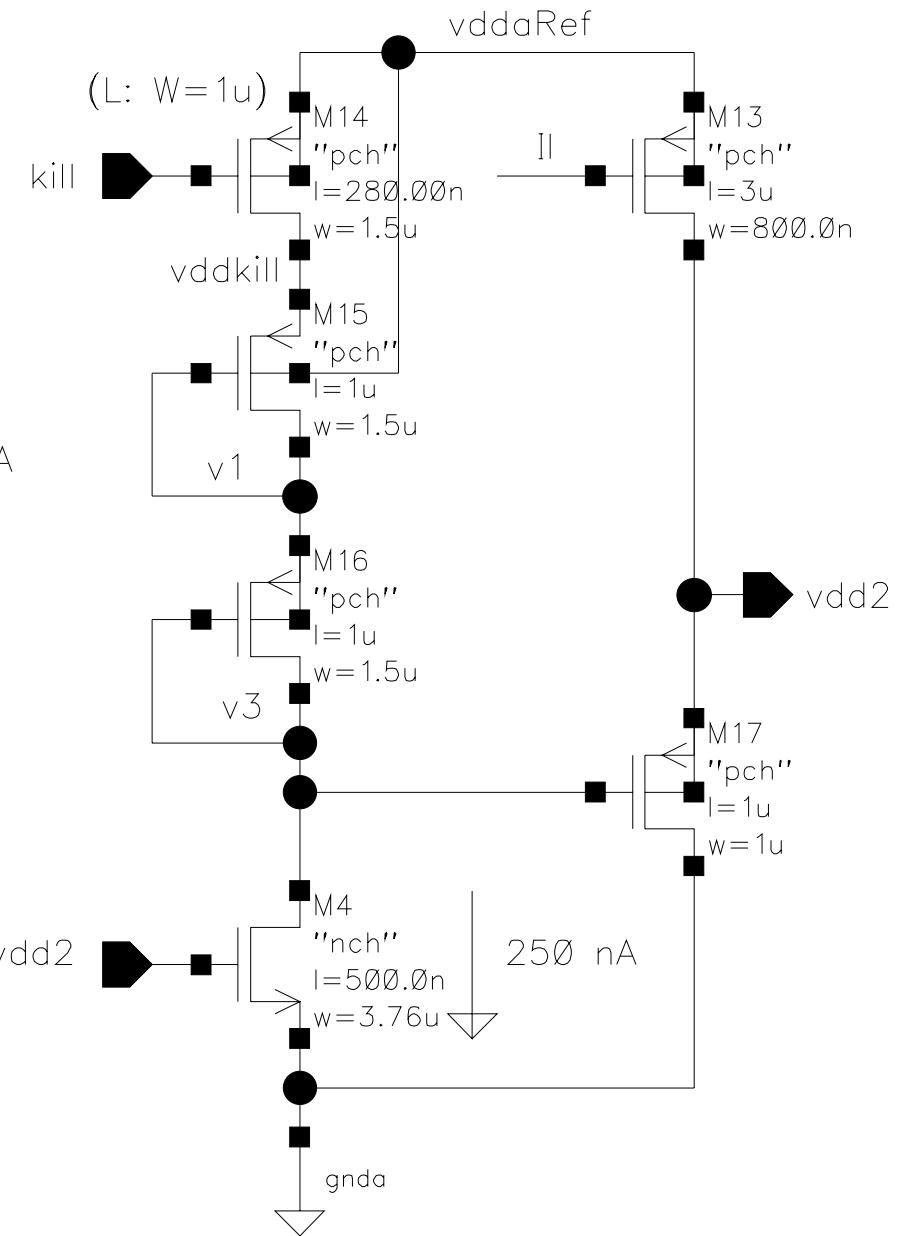
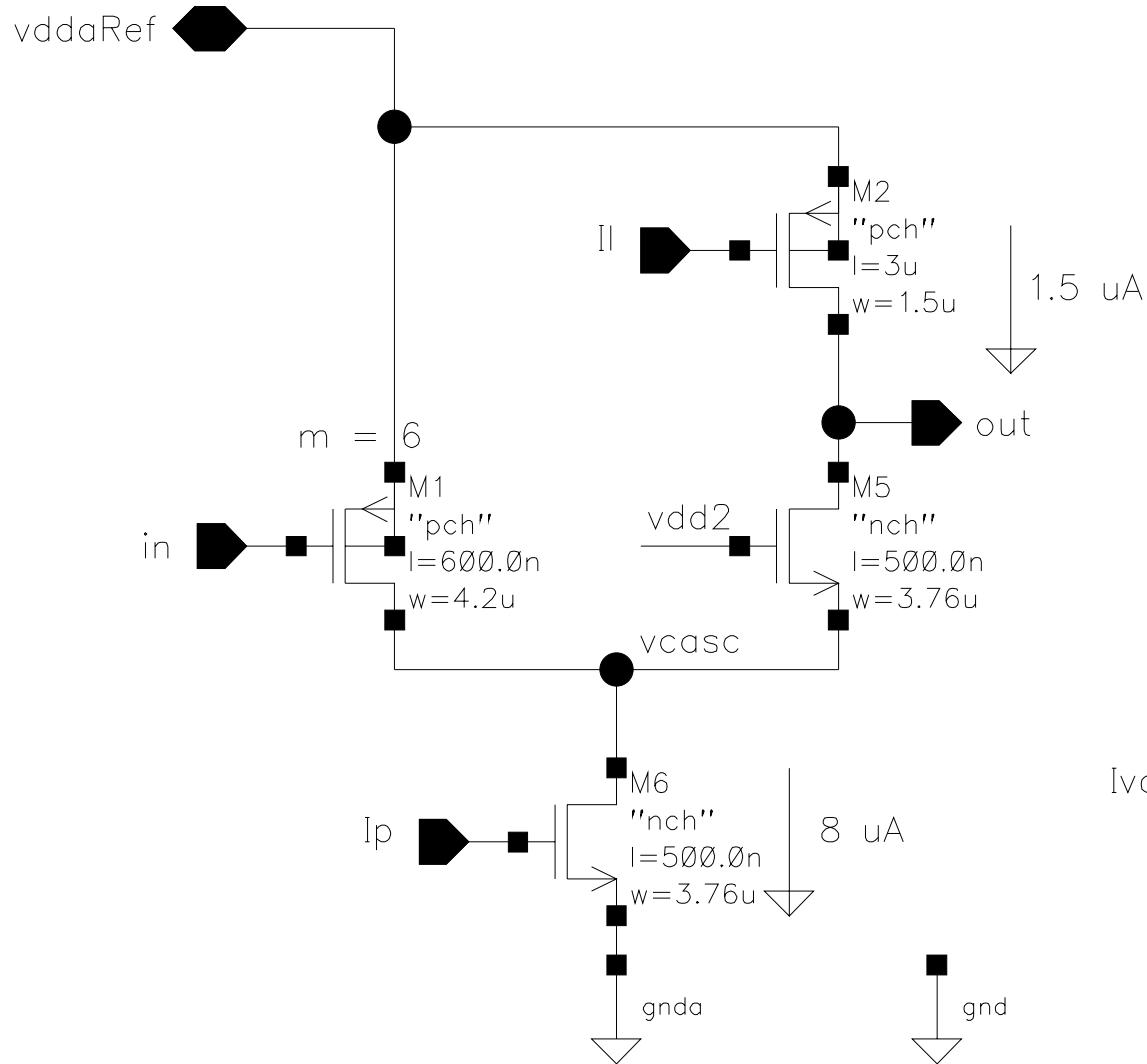
Analog cell

- Main goals:
 - Full operation with a single analog power supply of 1.6V
 - Single ended for this first “secure” cell but with special emphasis on high PSRR.
 - 2 stage based cell
 - First amp with small gain -> low input impedance
 - Fully differential second stage with high gain -> saturation at $12ke^-$
 - Same current as for DMILL & HONEYWELL analog cell
(-> half of the power)
 - New scheme of leakage current compensation (only a 1u/0.5u and a 1u/1u PMOS added to the usual 1u/6u PMOS current mirror)

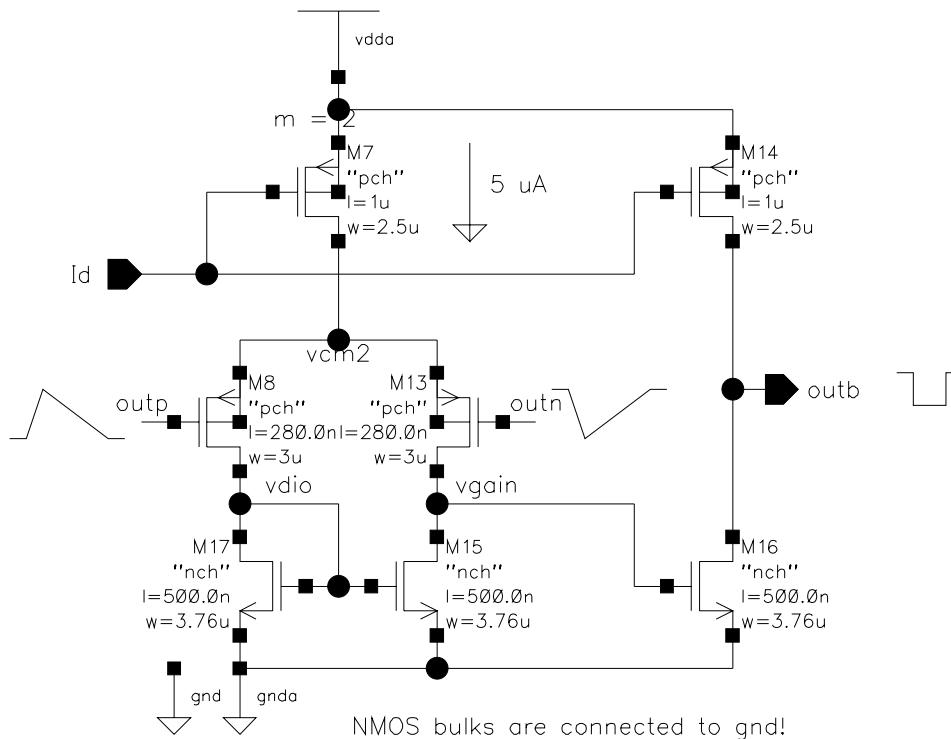
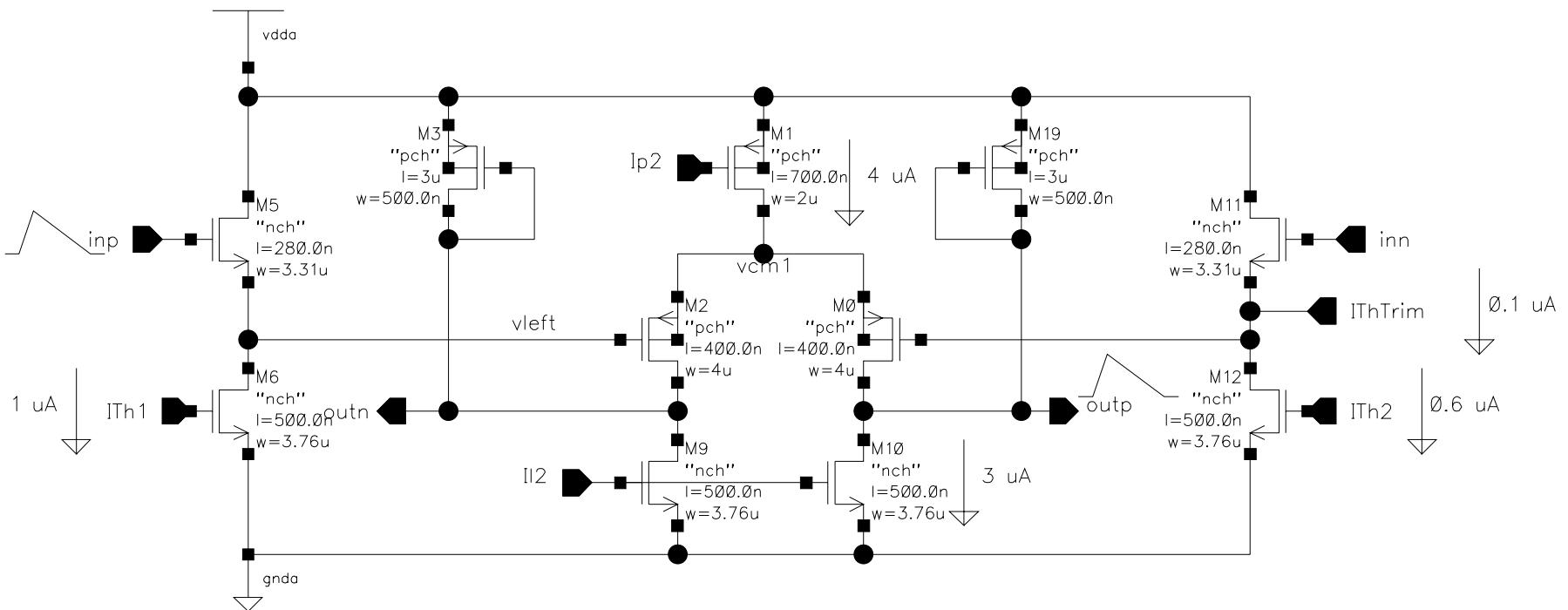
Schematics

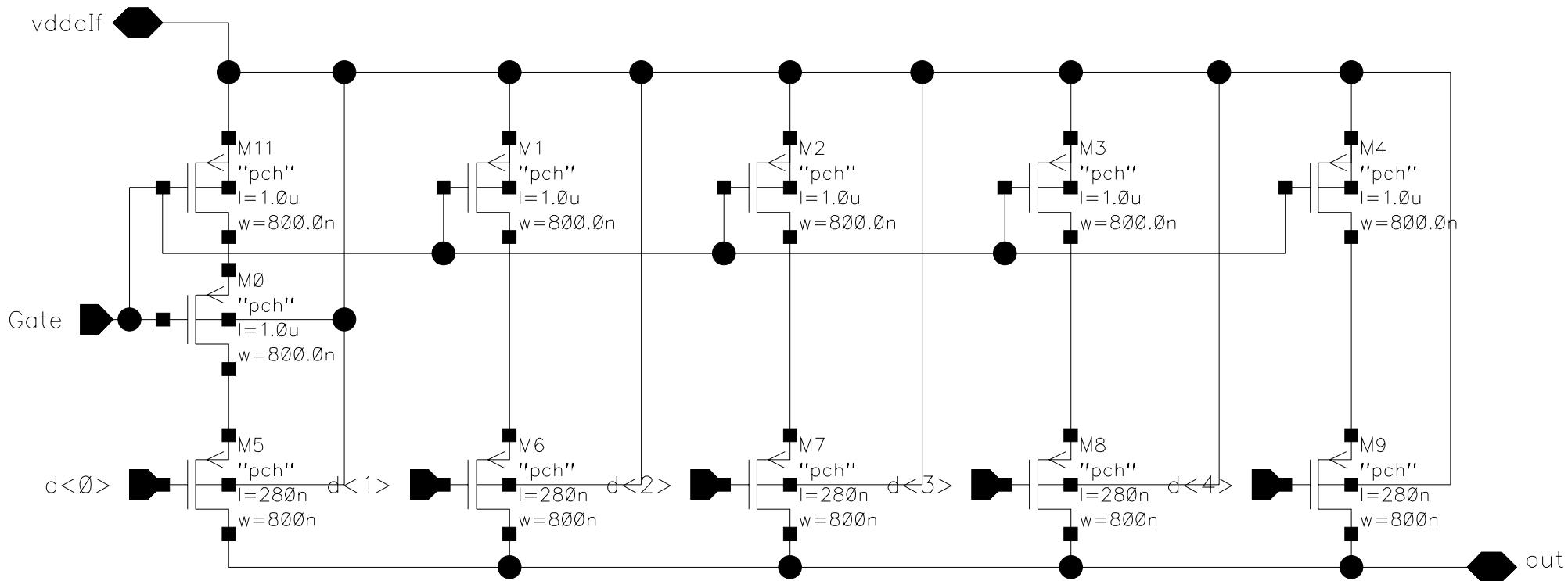


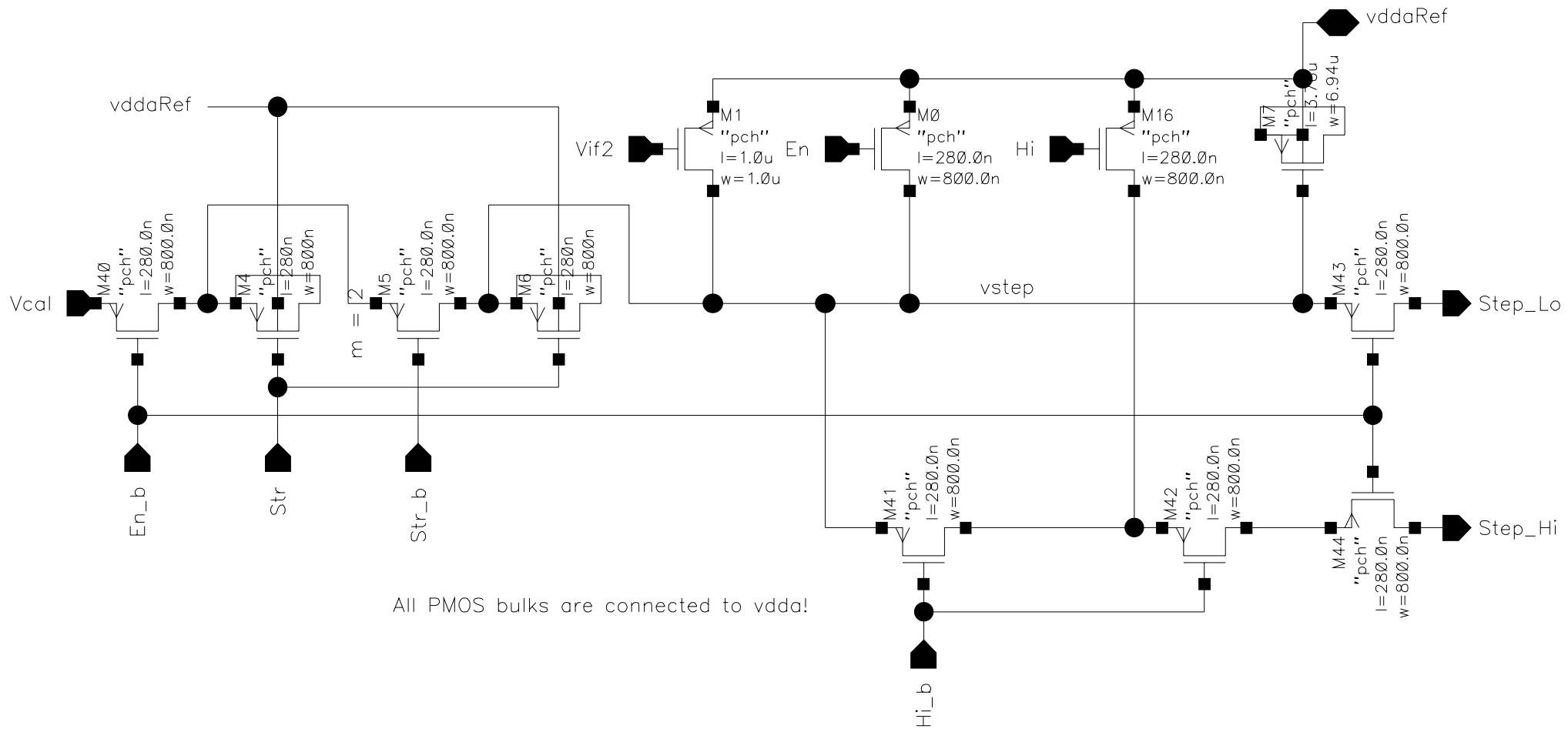


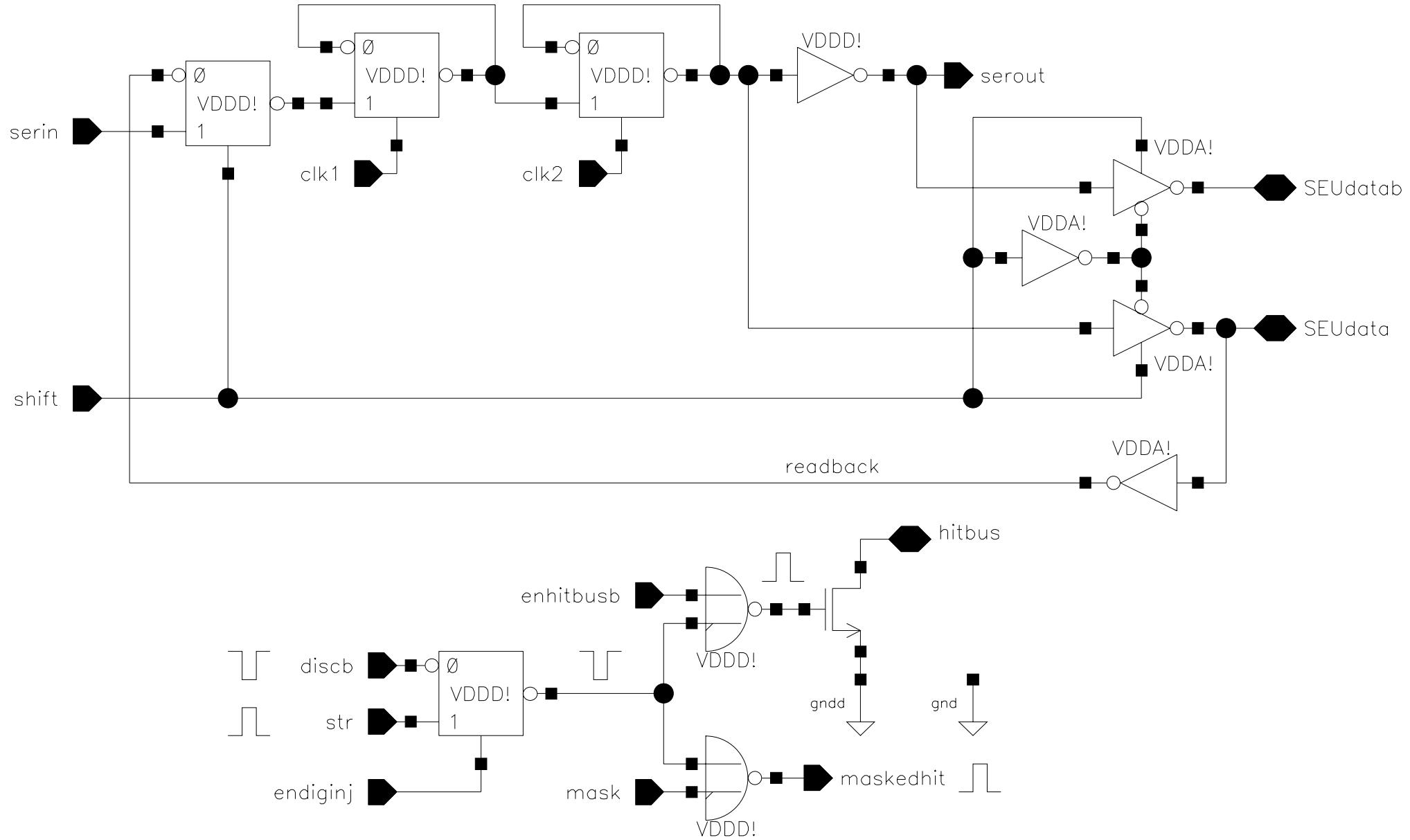


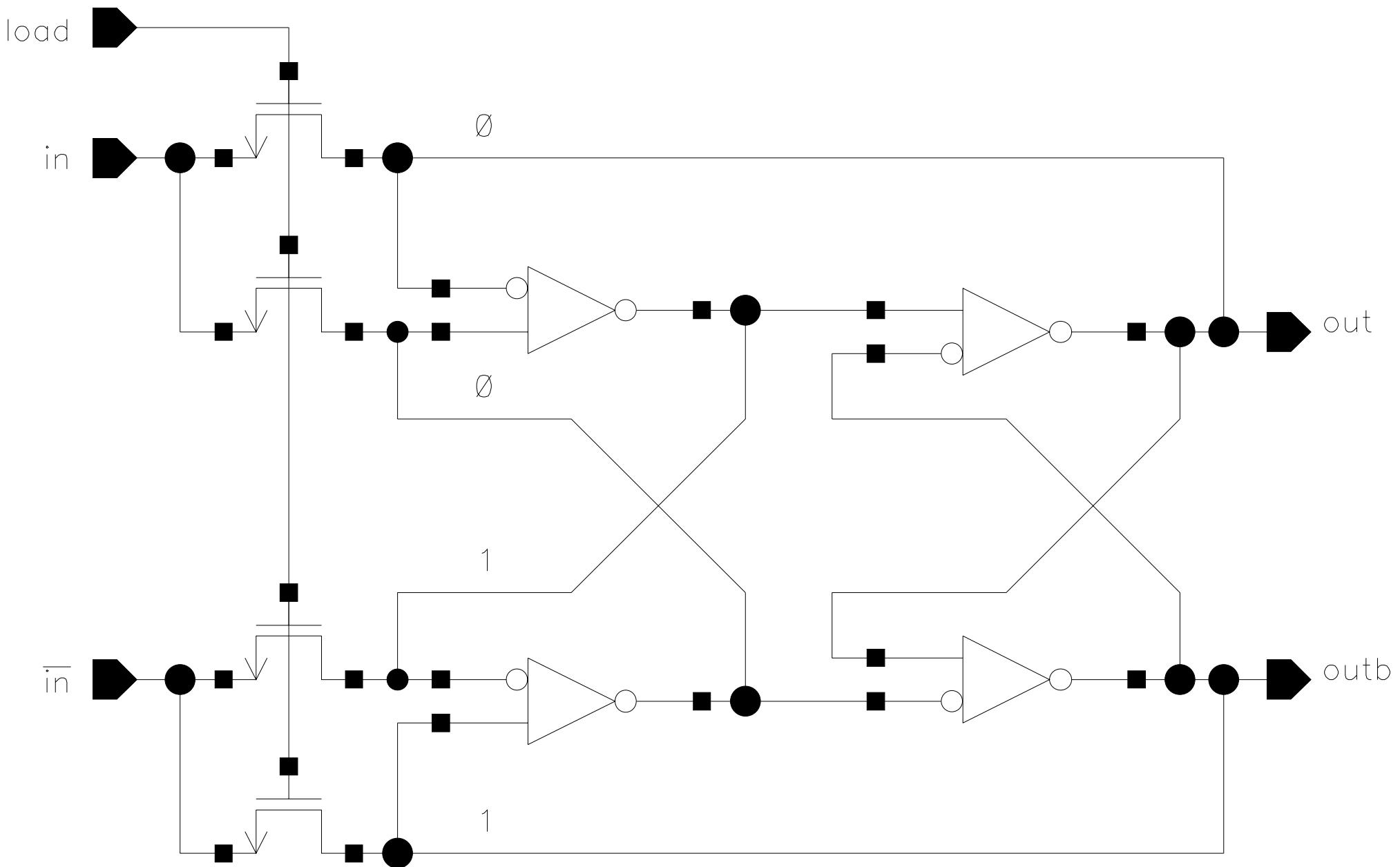
NMOS bulks are connected to gnd!

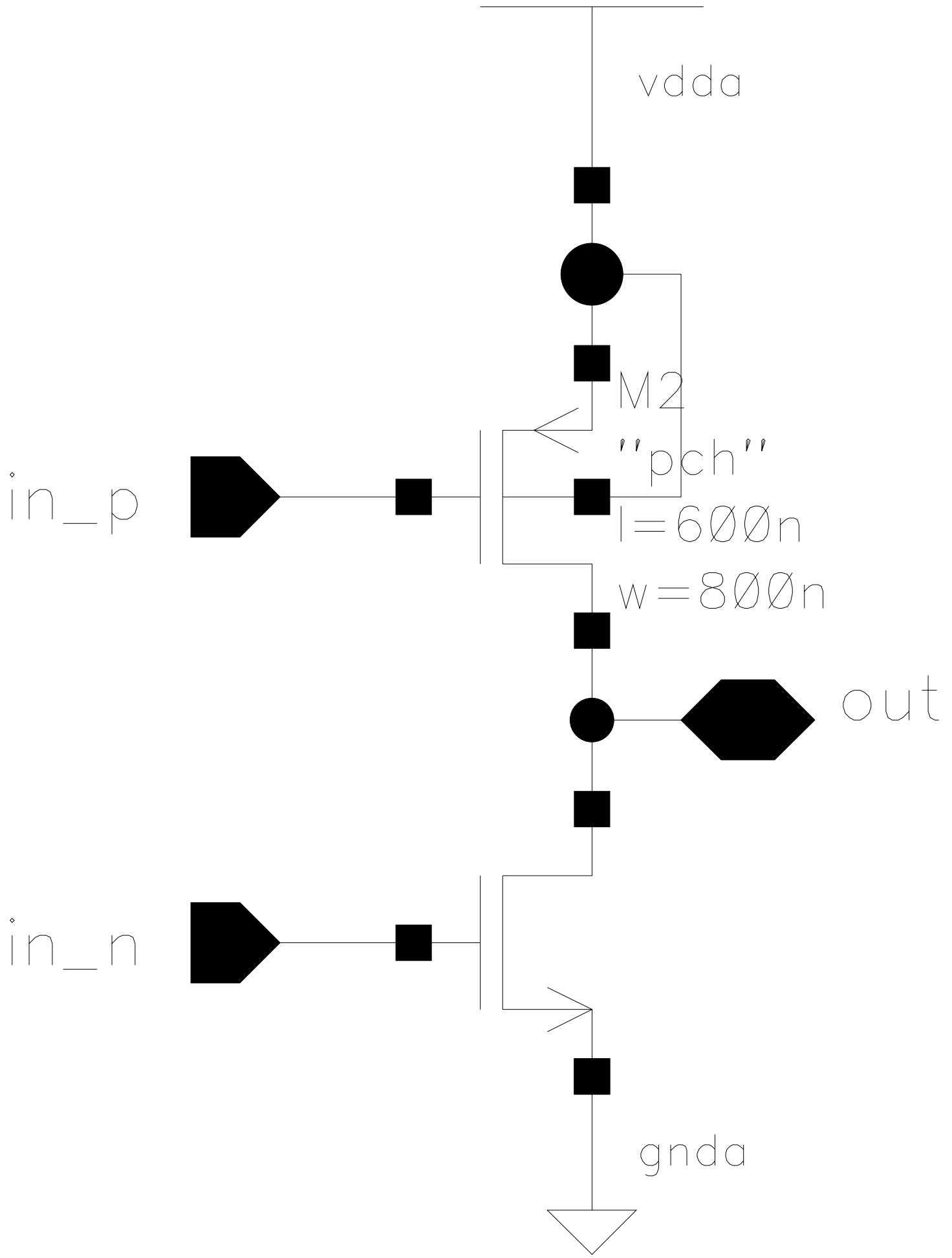


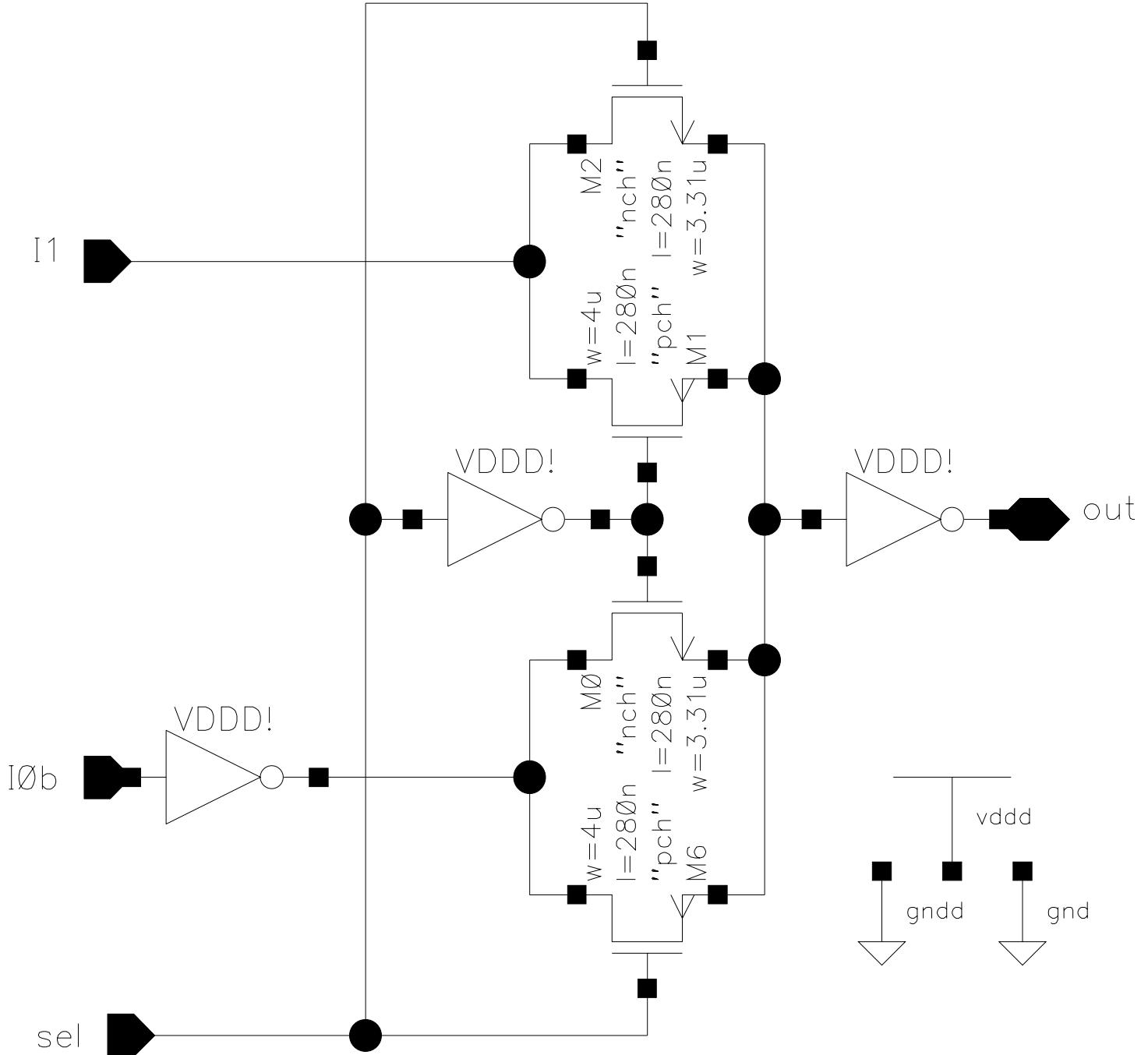




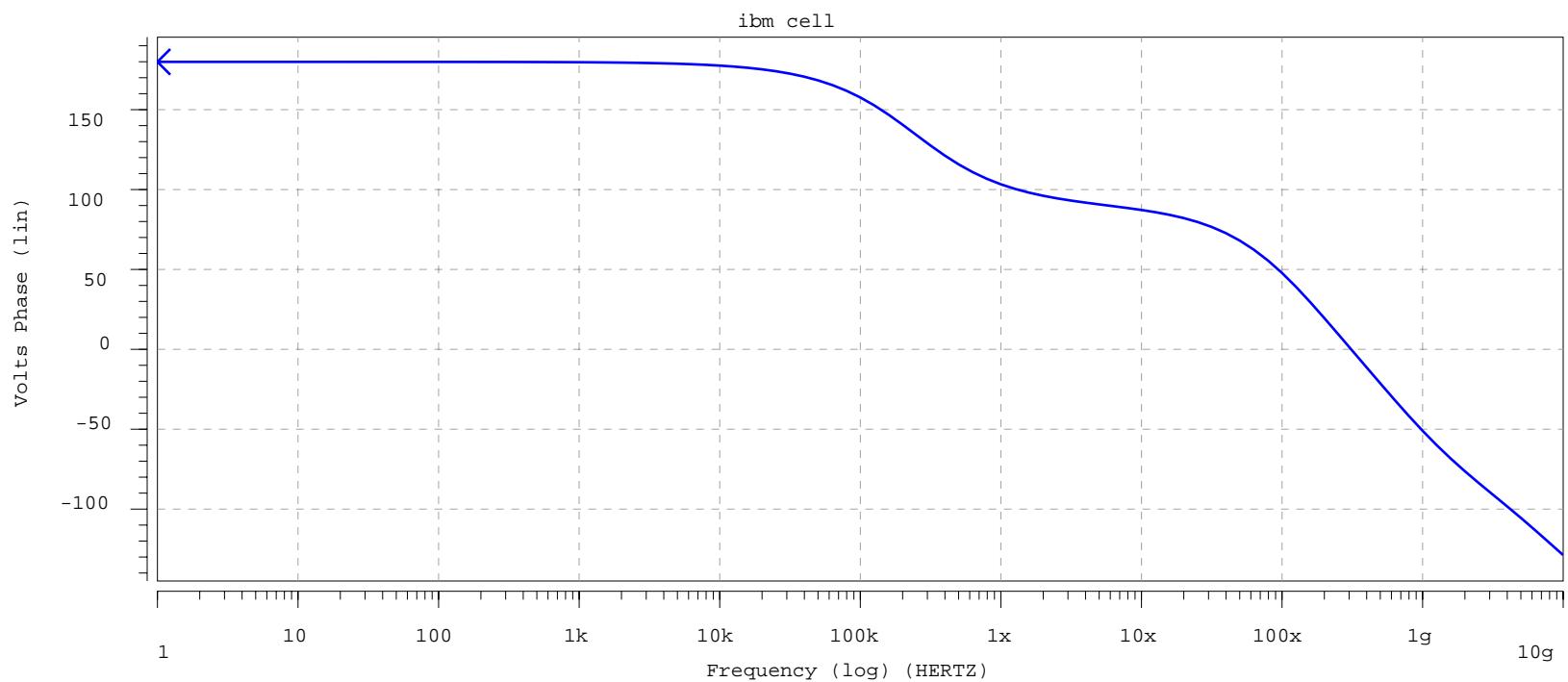
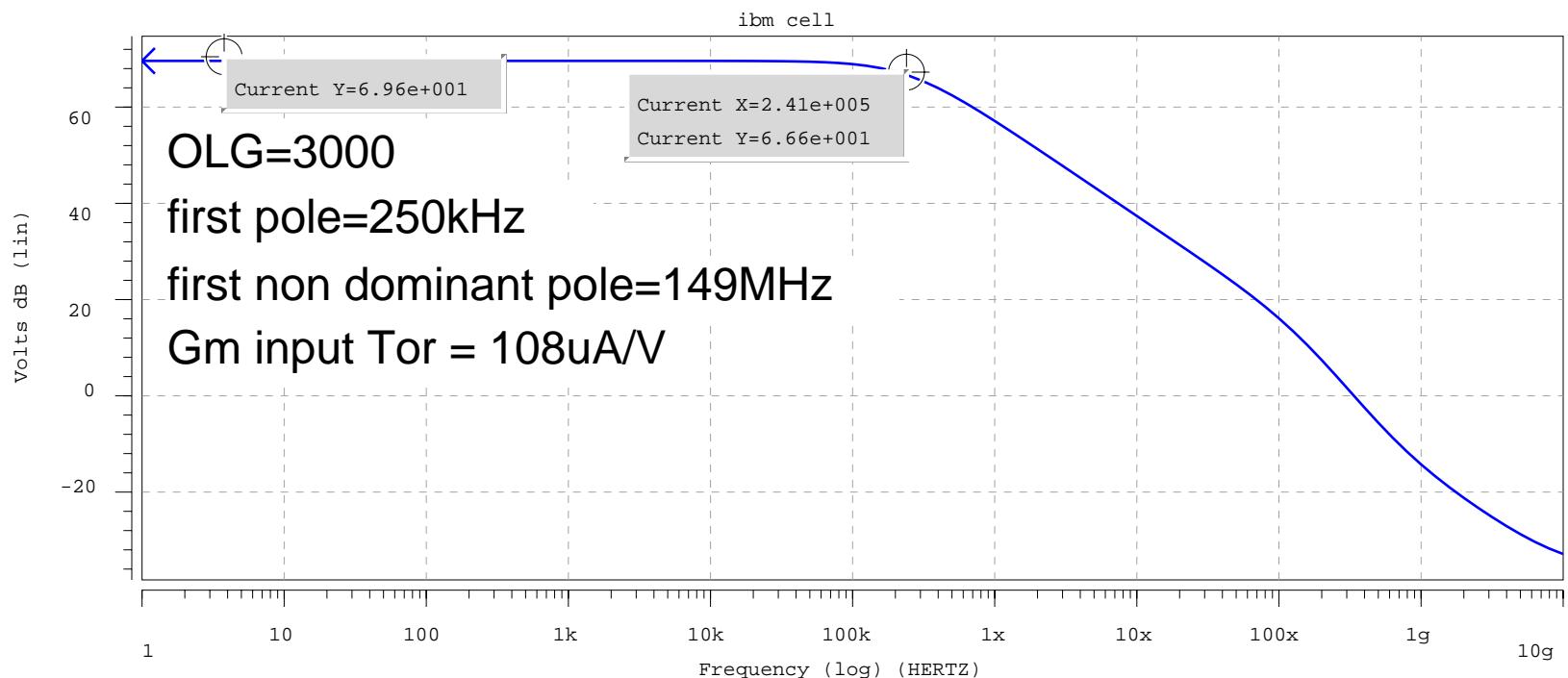


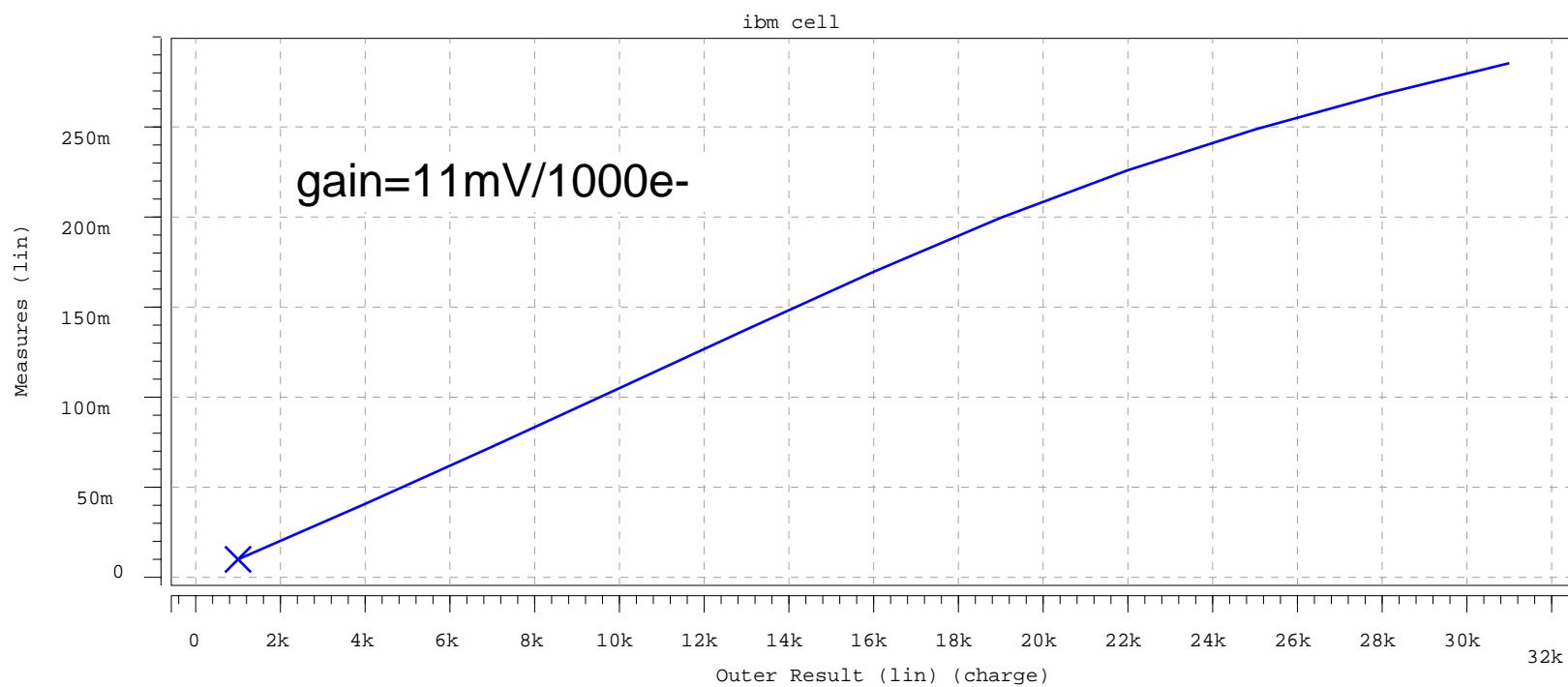
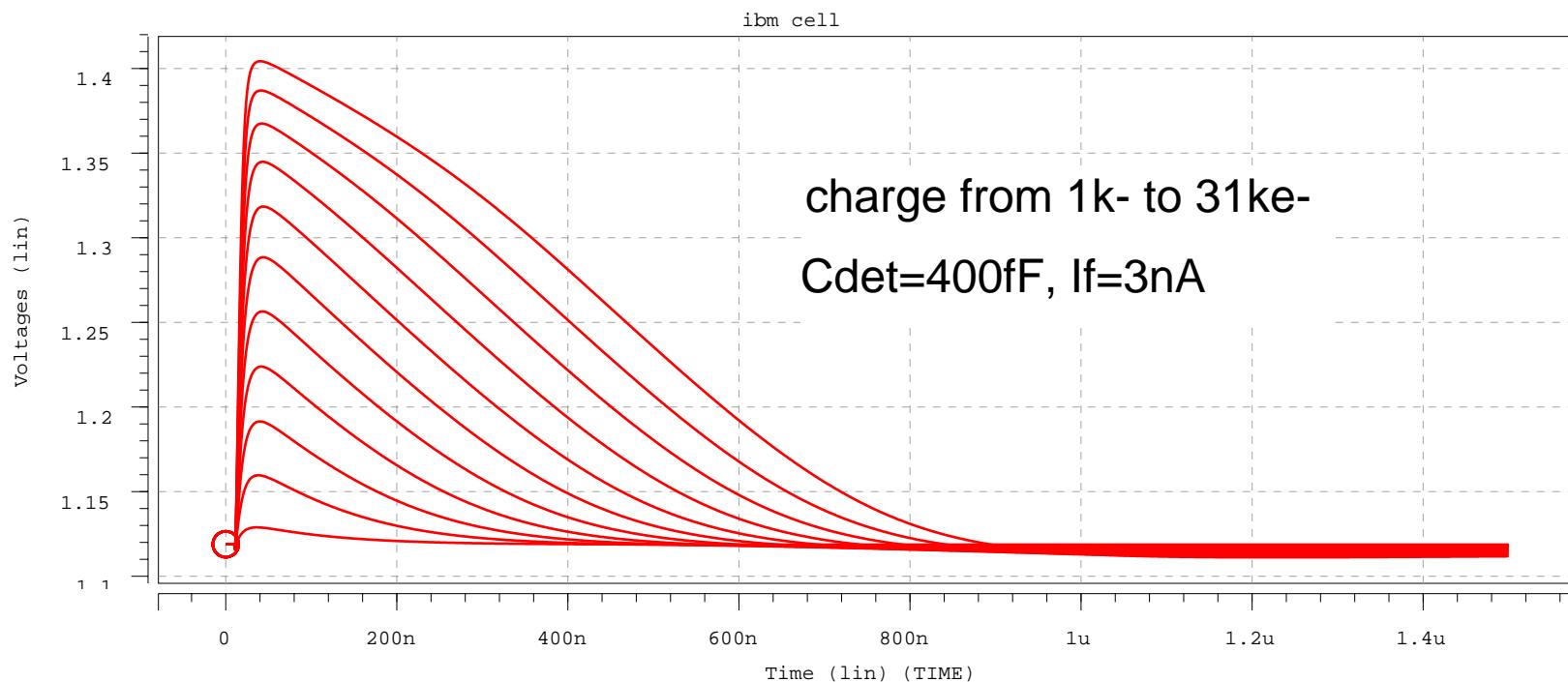


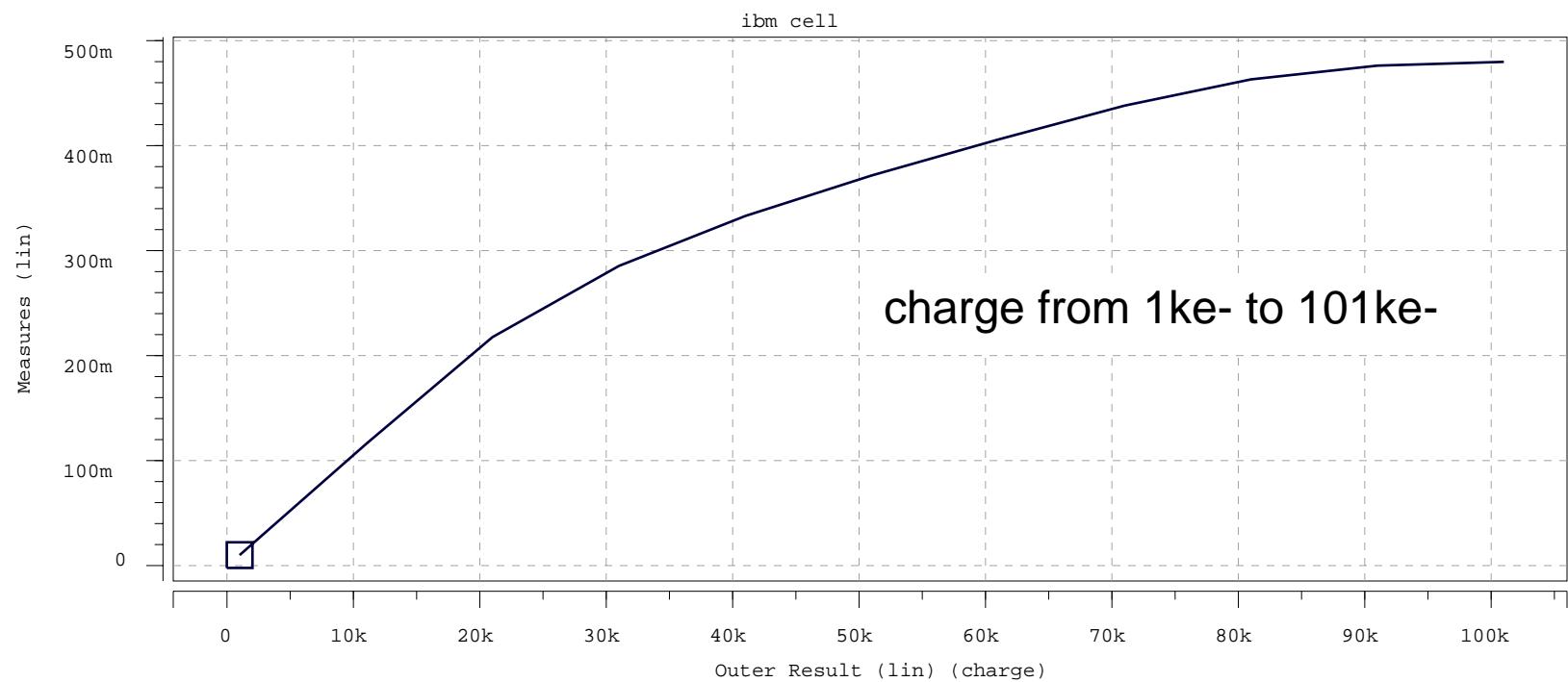
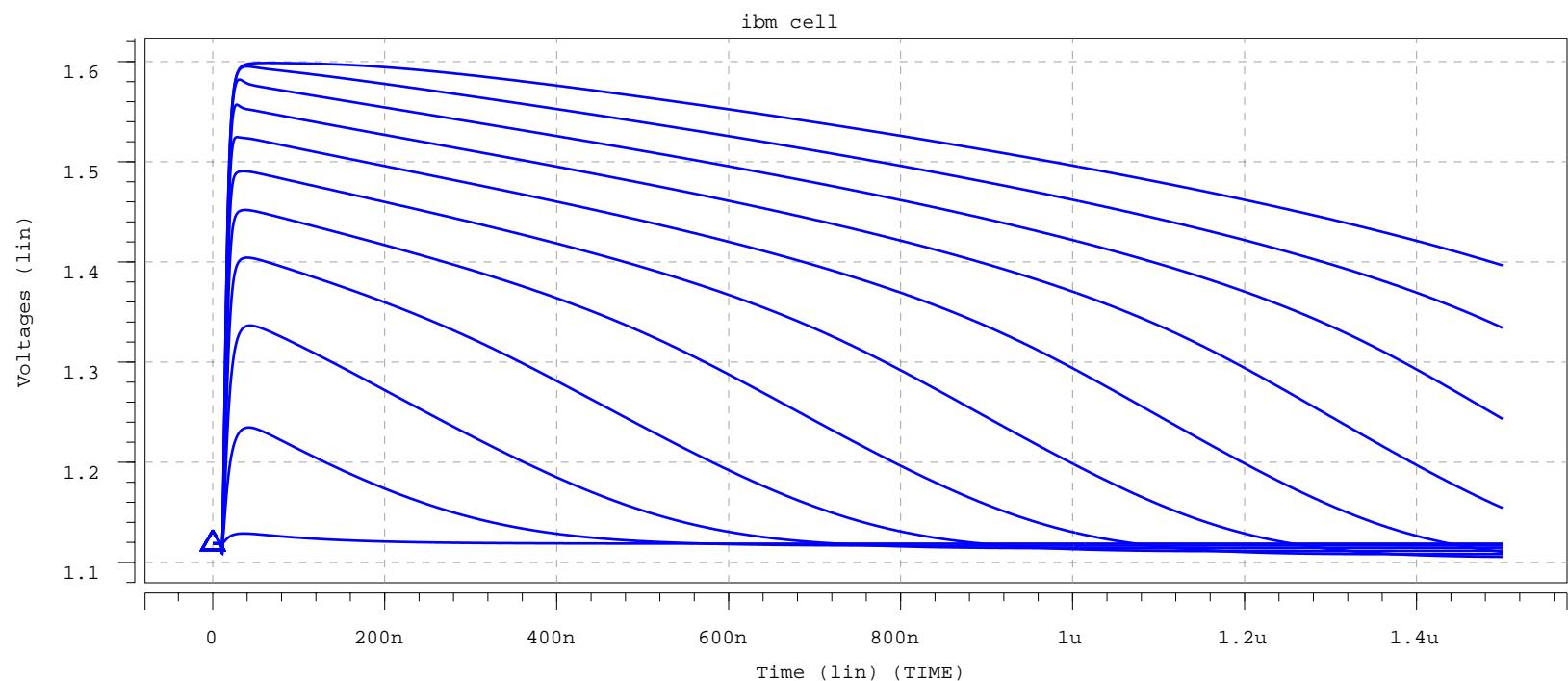


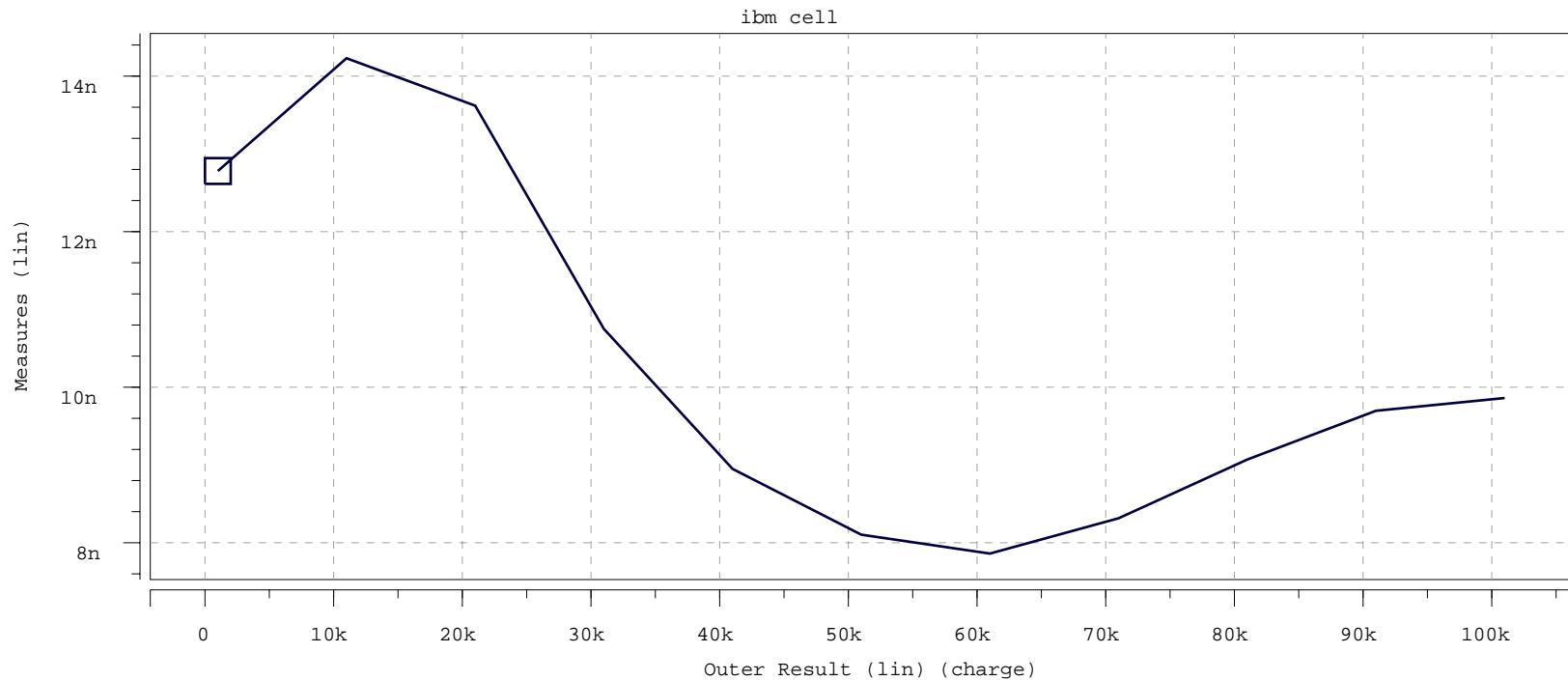


Simulation Results



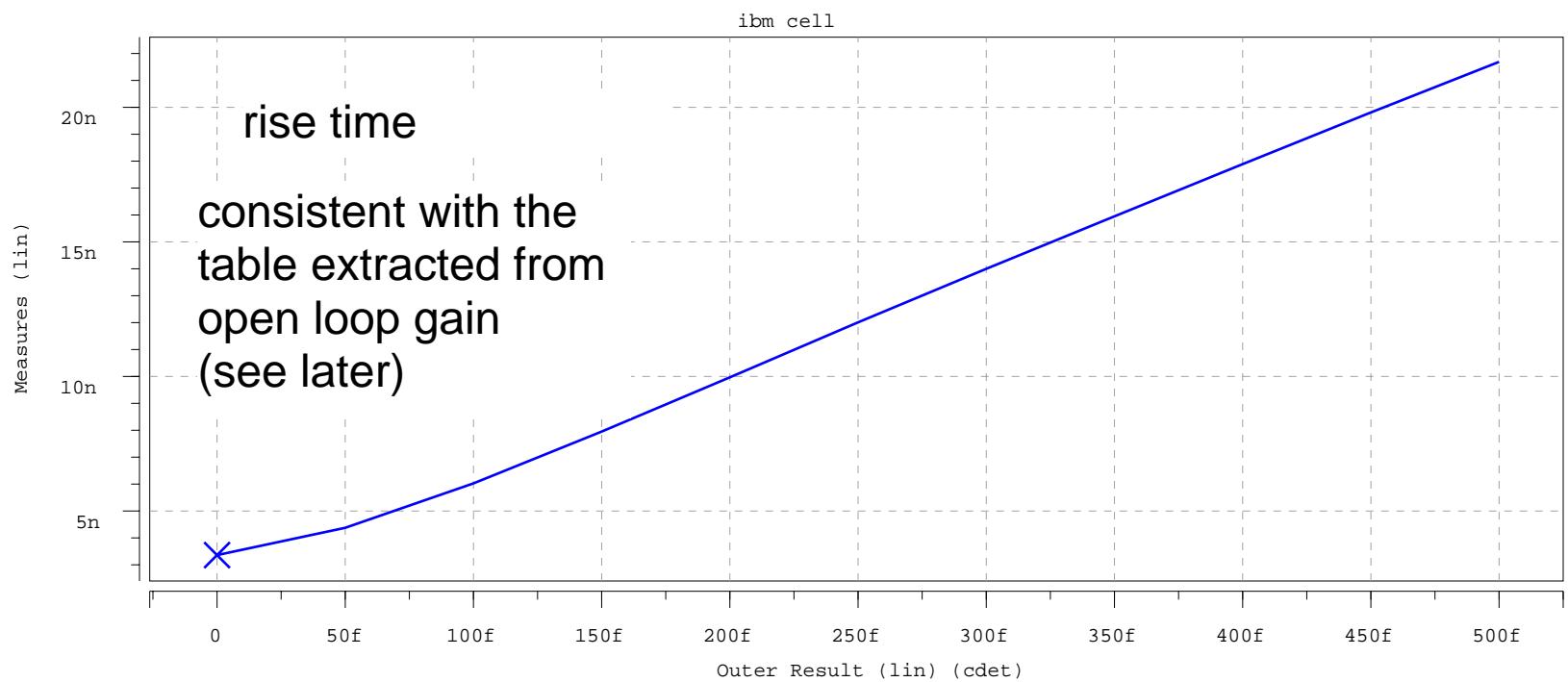
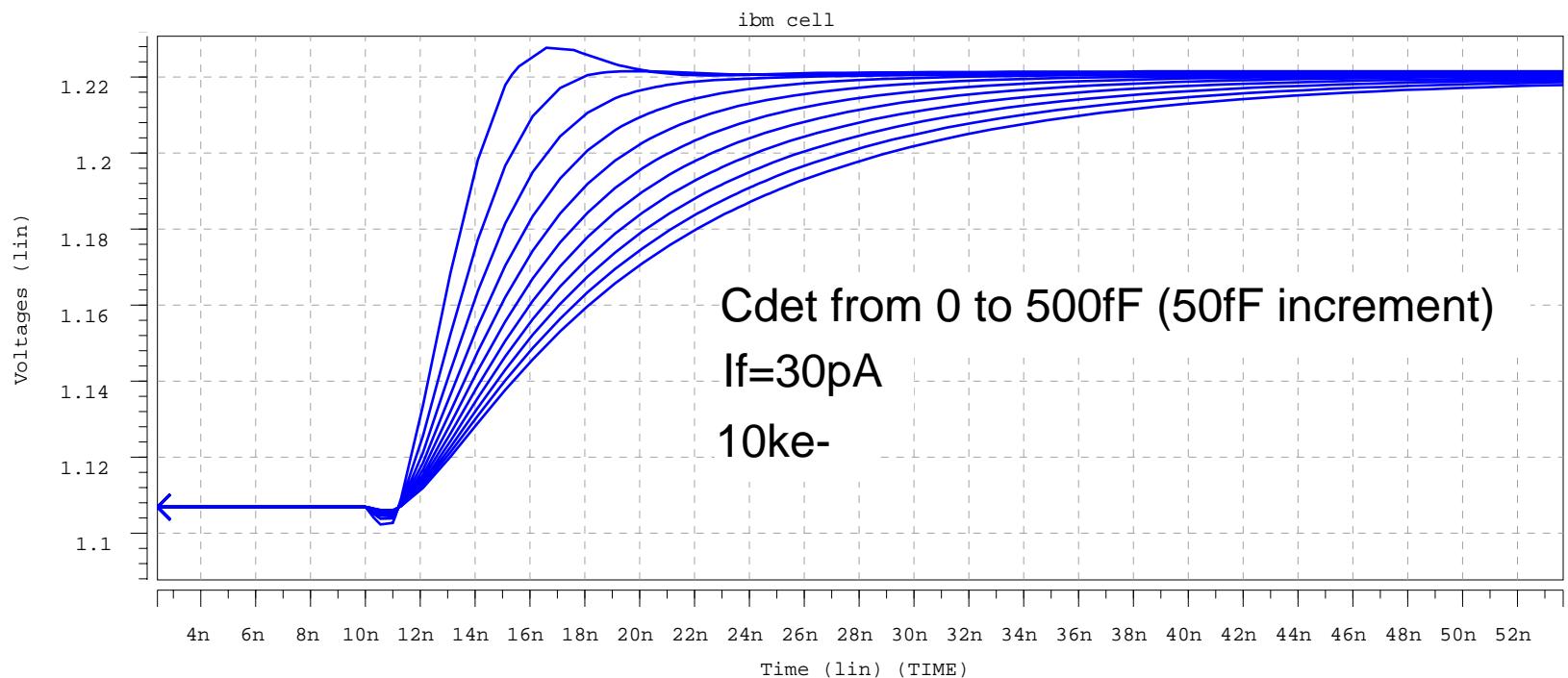


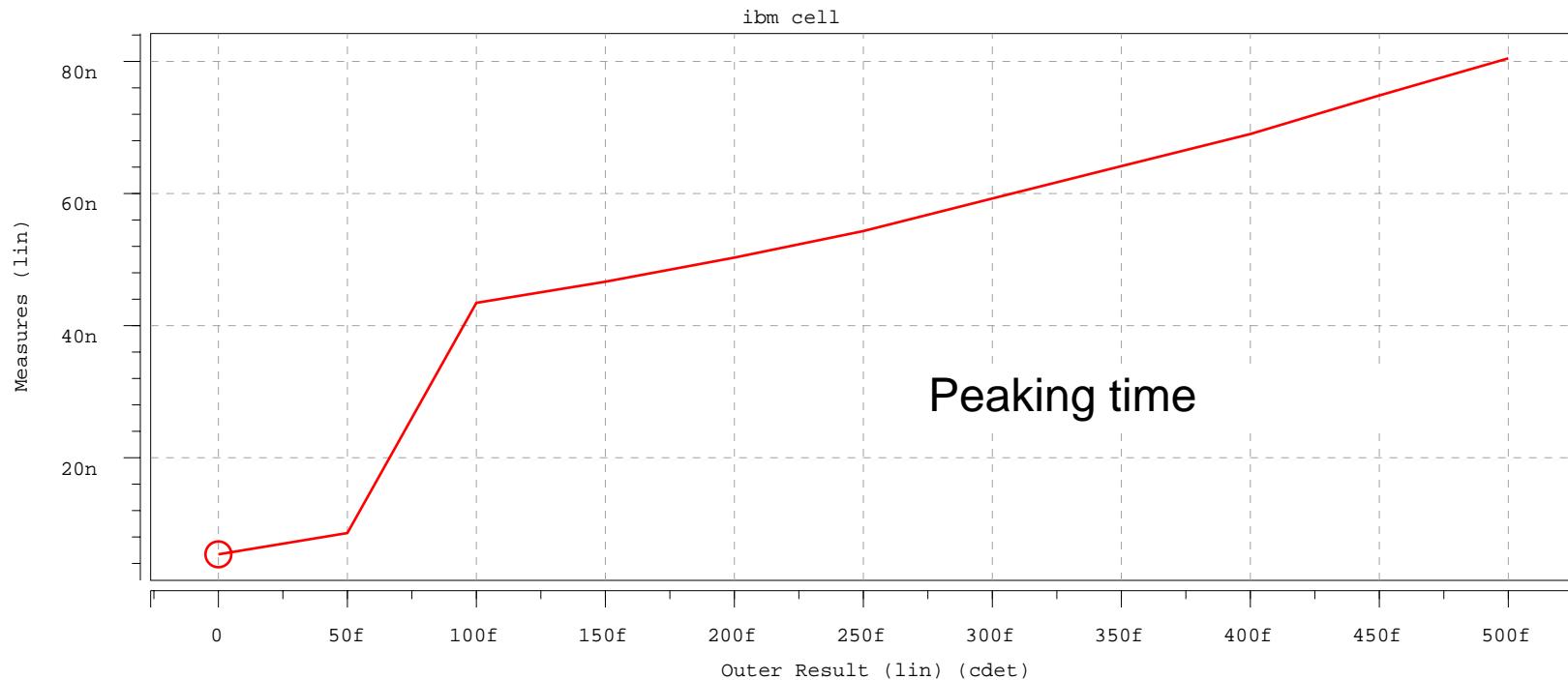
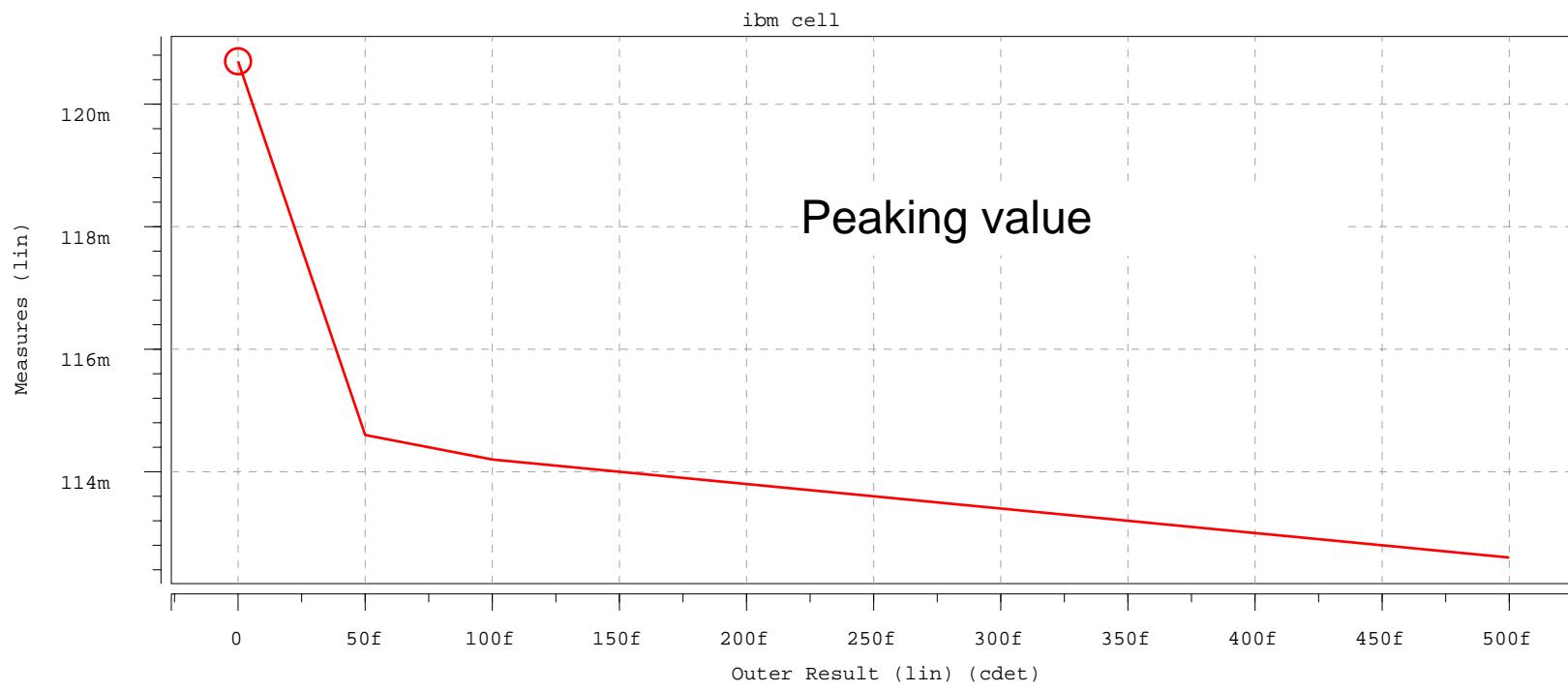


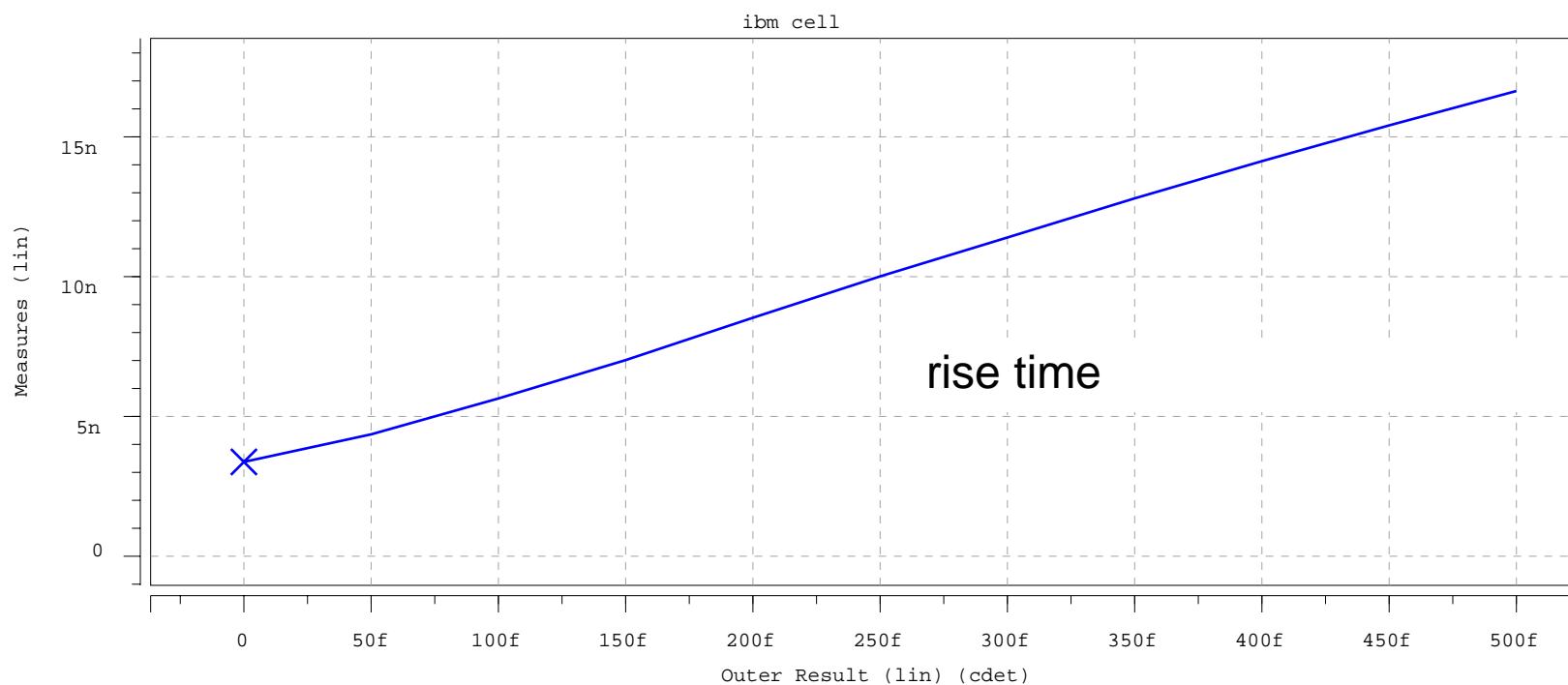
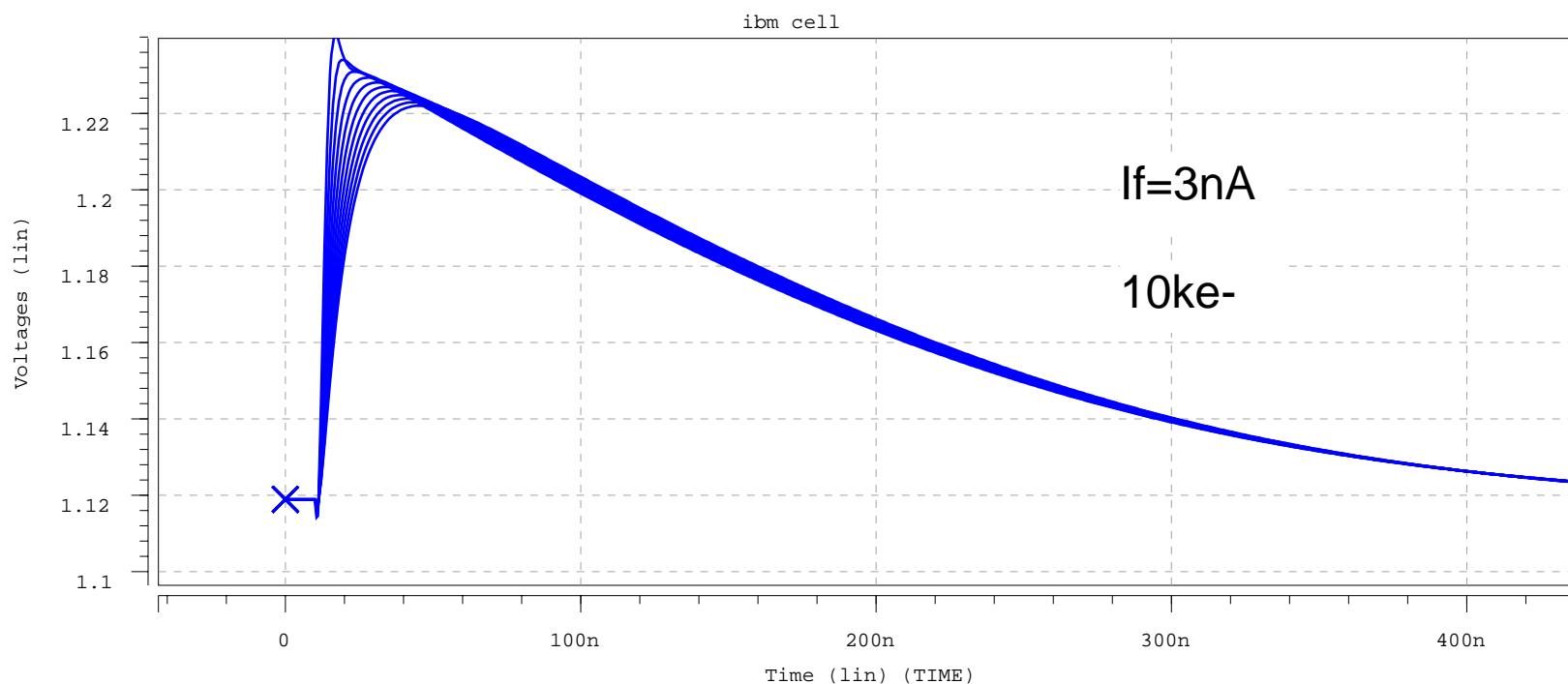


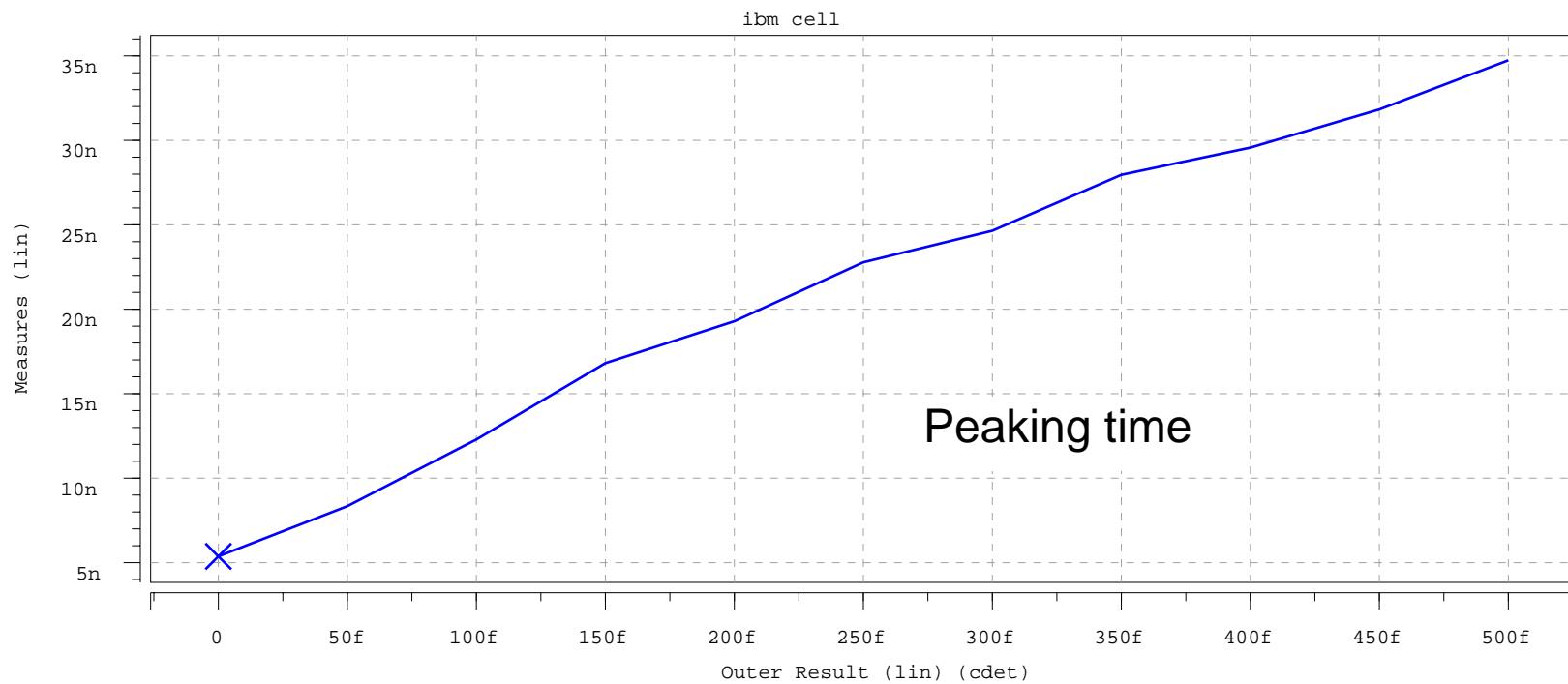
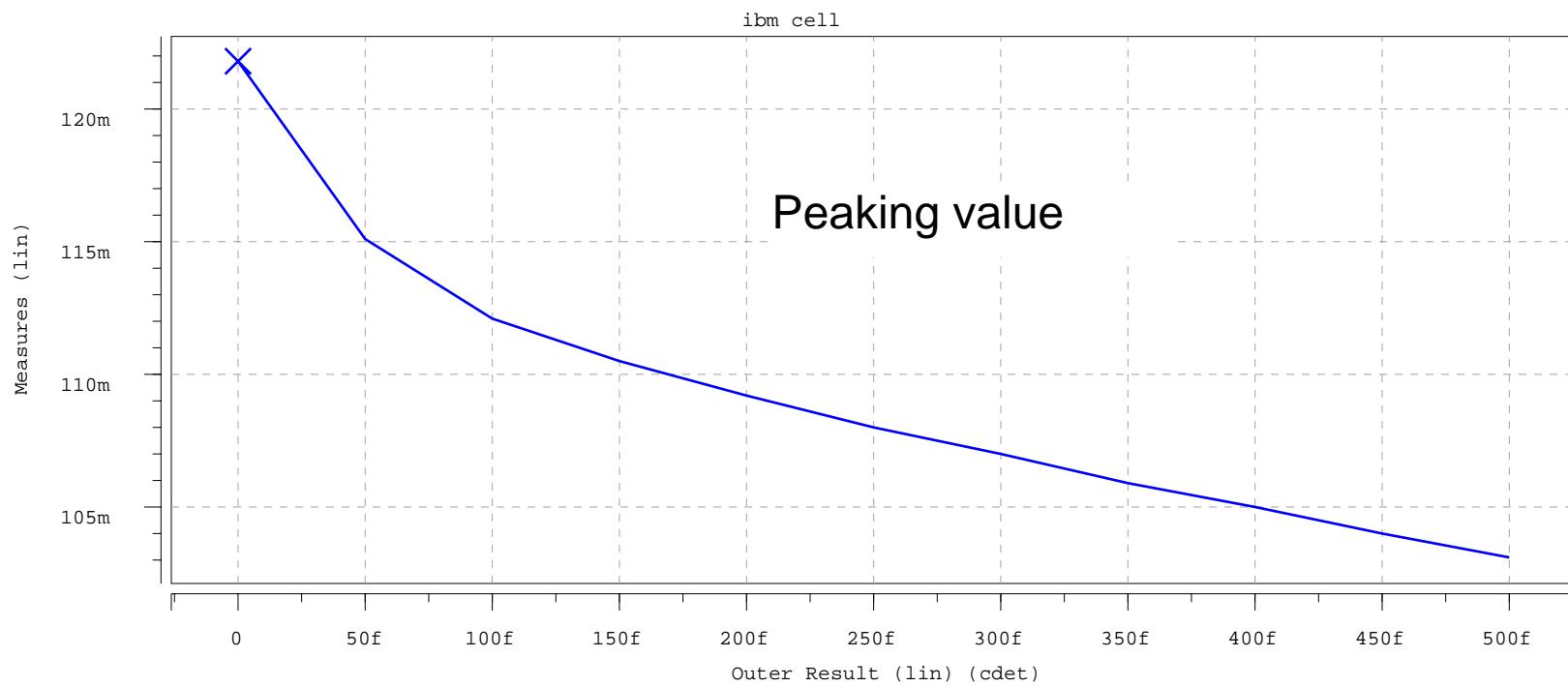
DC LEVEL

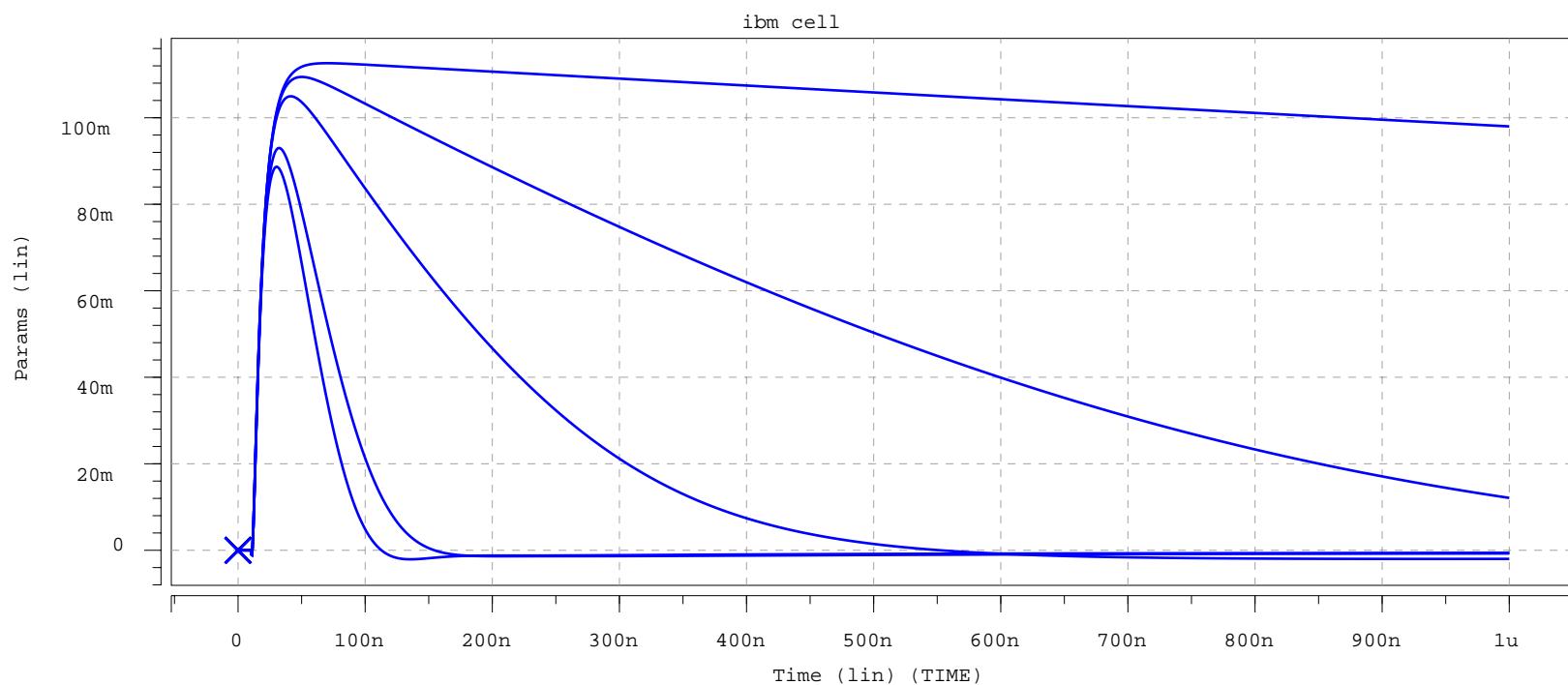
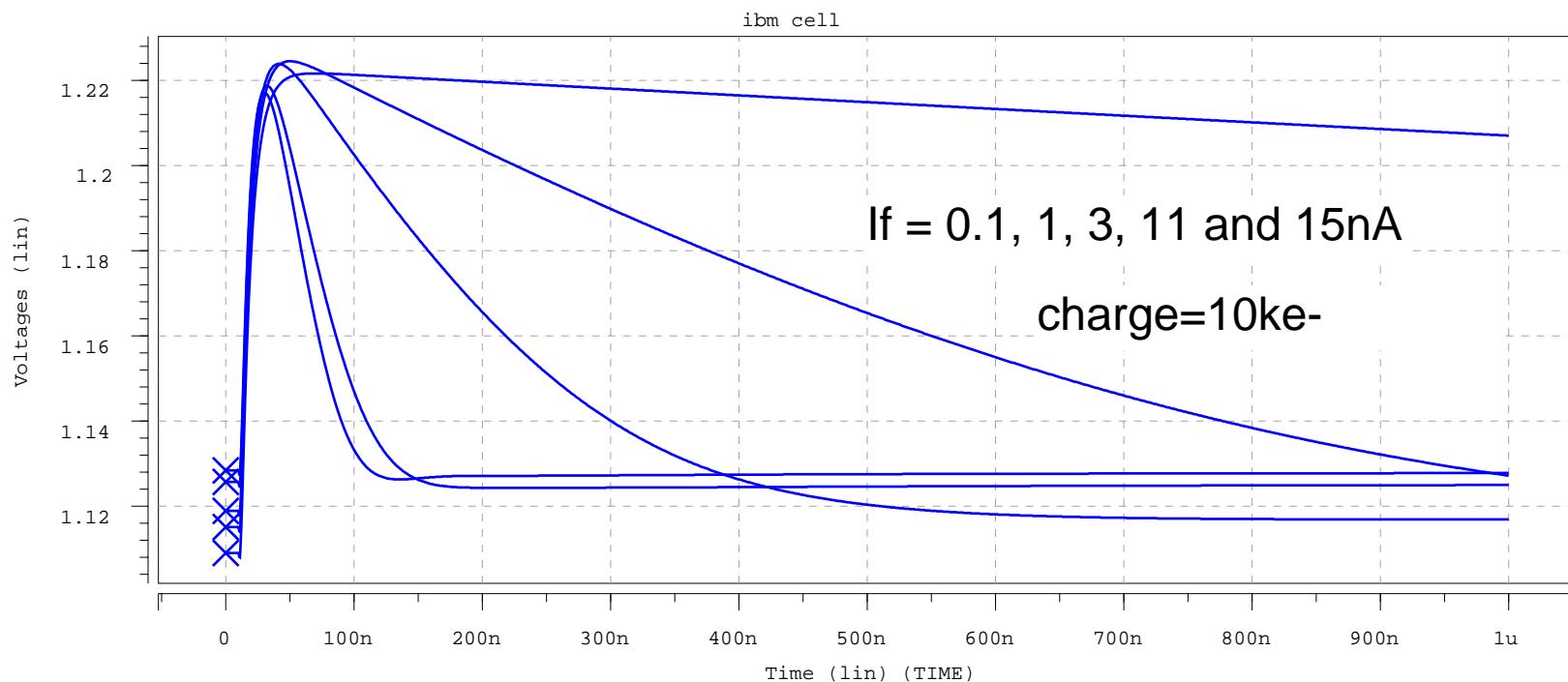
idisc = 885.9232m	if = 313.8621m	il = 706.8171m	il2 = 622.6964m
ip = 684.3992m	ip2 = 851.6837m	isui = 562.8955m	ith = 537.0542m
ivdd2 = 495.0290m			
in = 1.0368	ina = 461.1347m	inb = 480.7910m	
out1 = 1.1189	out2a = 611.6471m	out2b = 438.5009m	
outdisb = 649.3152m	outleak = 1.1444	sour2 = 1.0427	
sourdis = 1.0683	v1 = 1.0297	v3 = 460.7145m	
vcasc = 412.7314m	vcomp = 641.4577m	vcopy1 = 659.8074m	
vcopy2 = 1.1202	vdd2 = 1.1444	vif = 659.9670m	
vif2 = 1.2224			

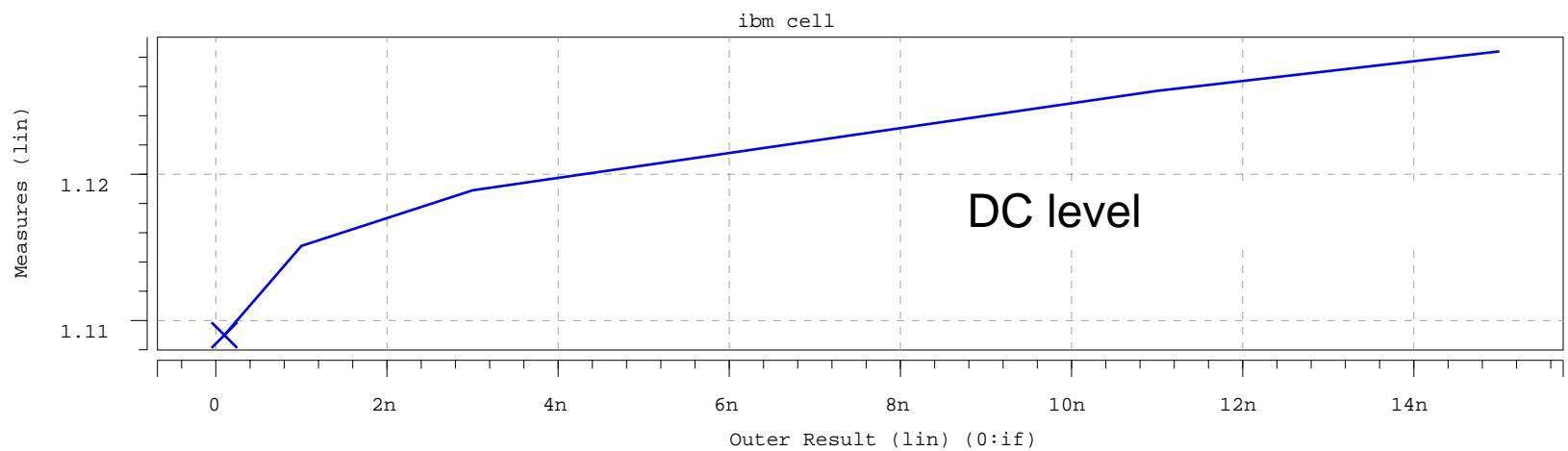
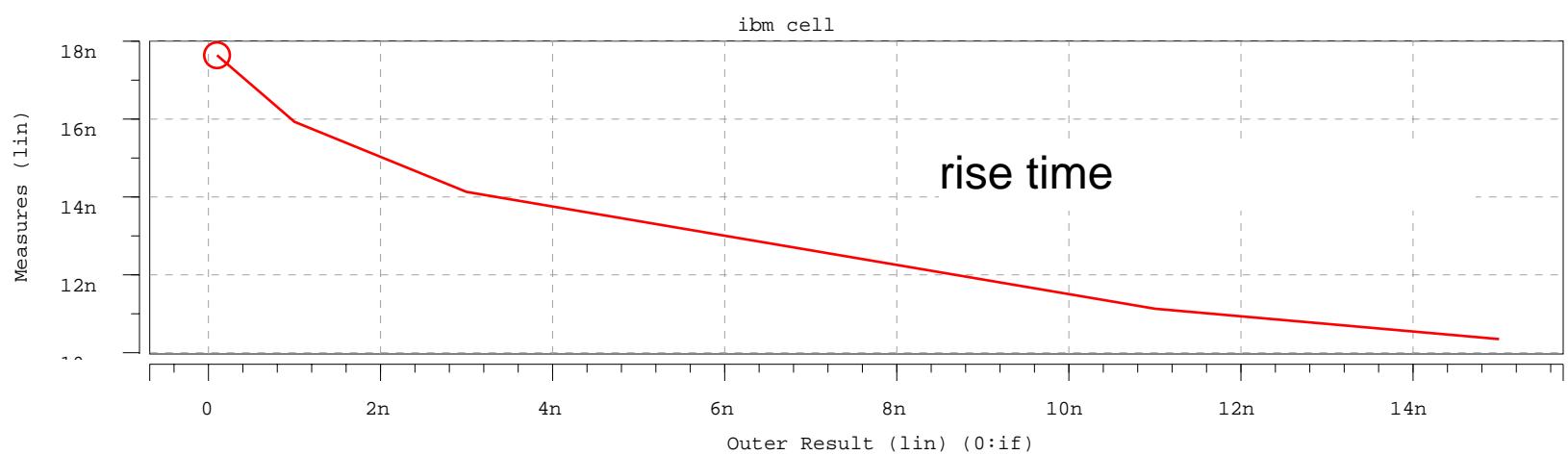
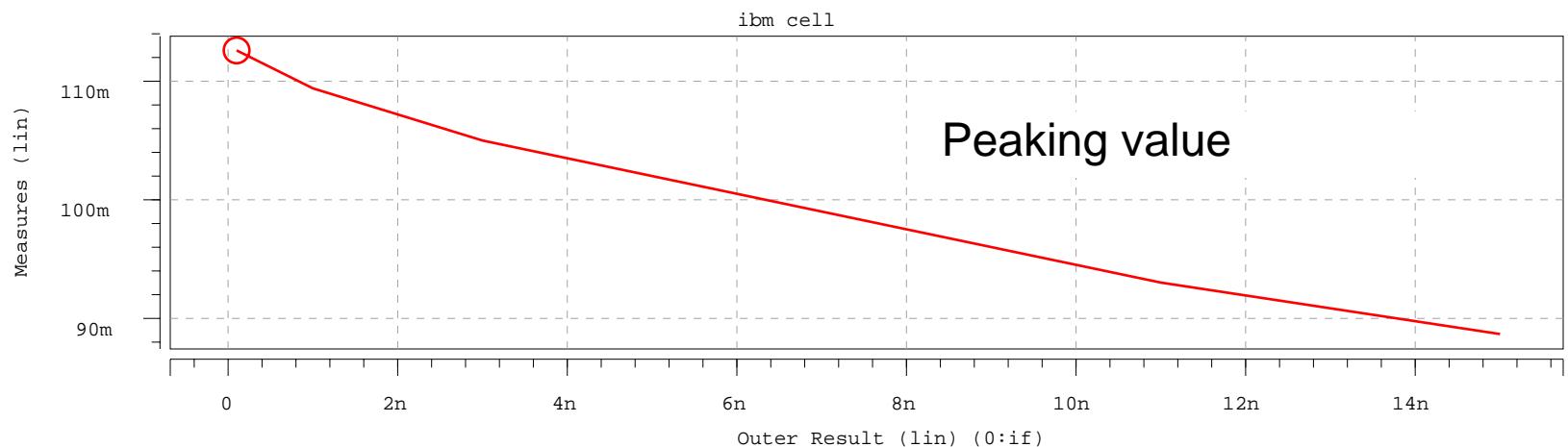


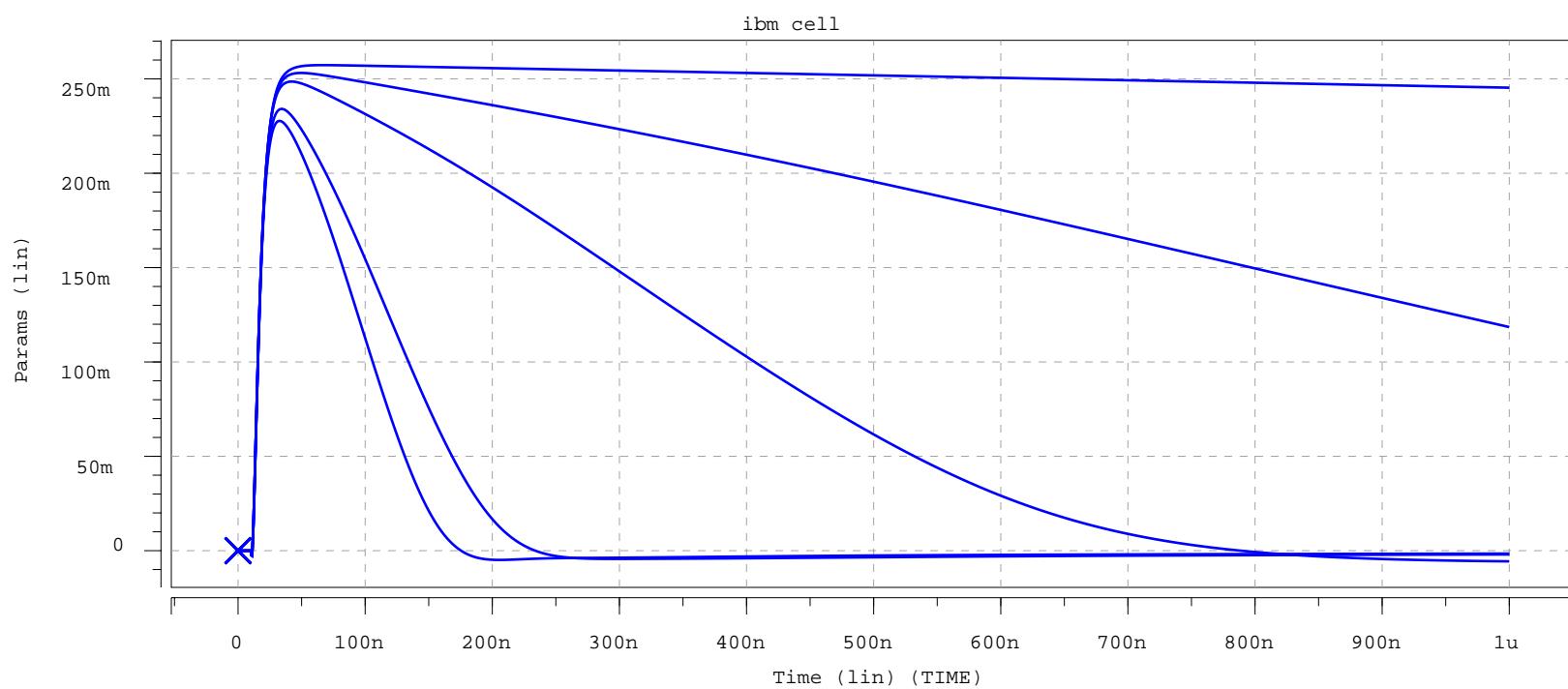
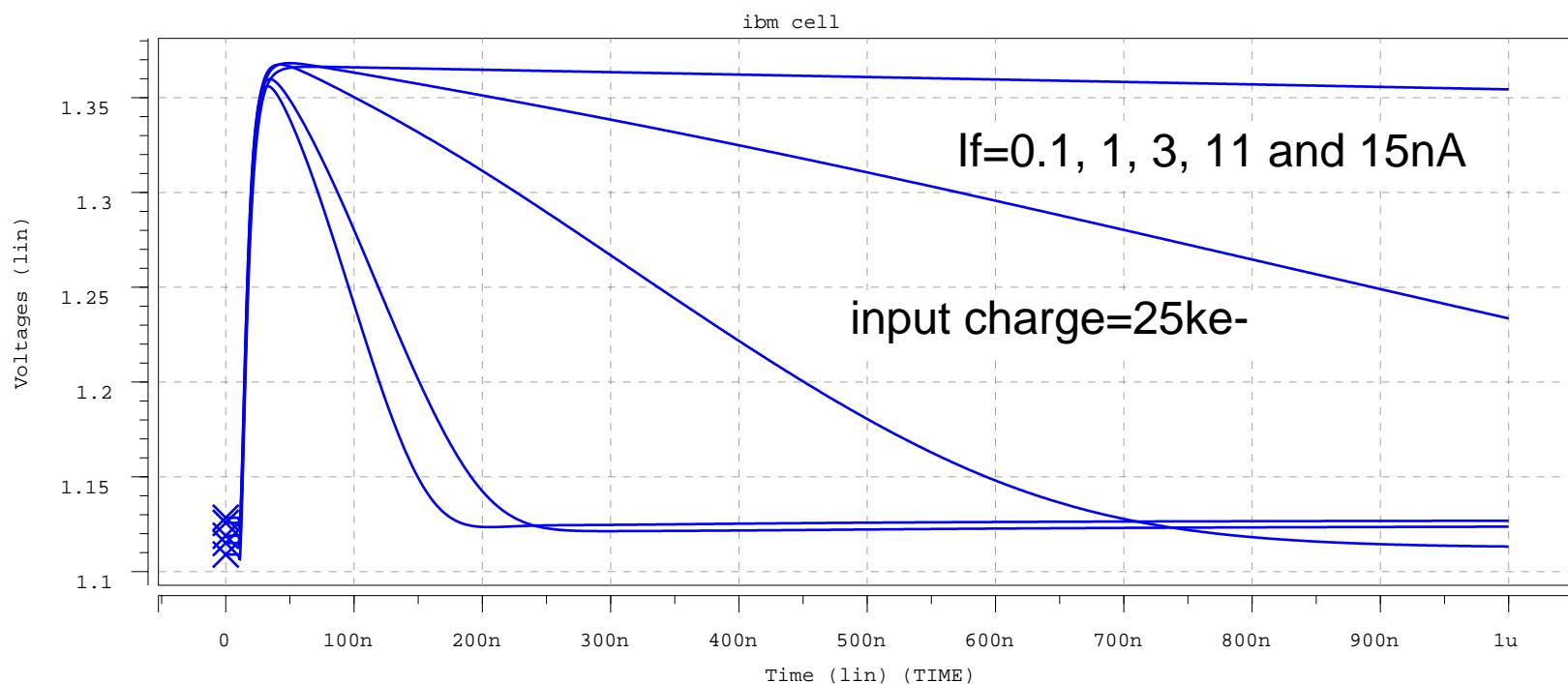


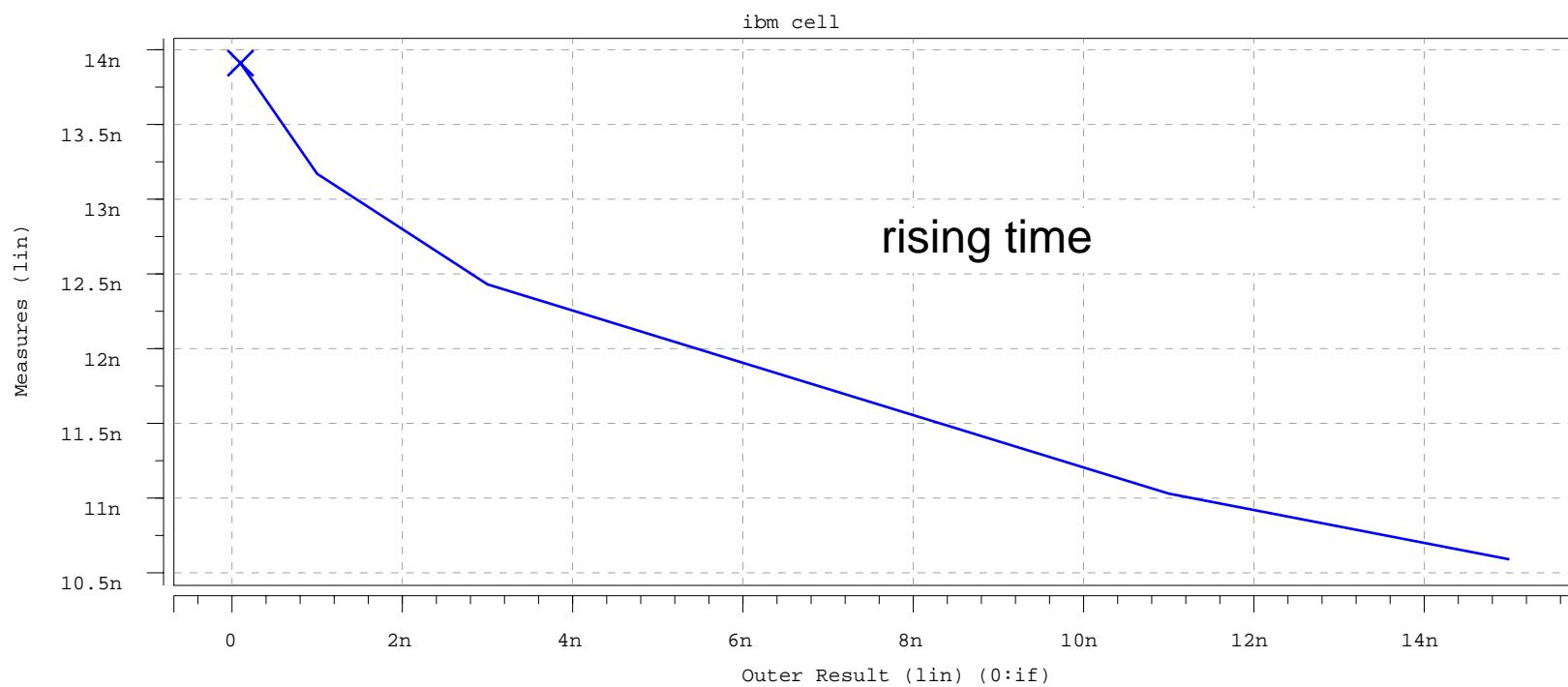
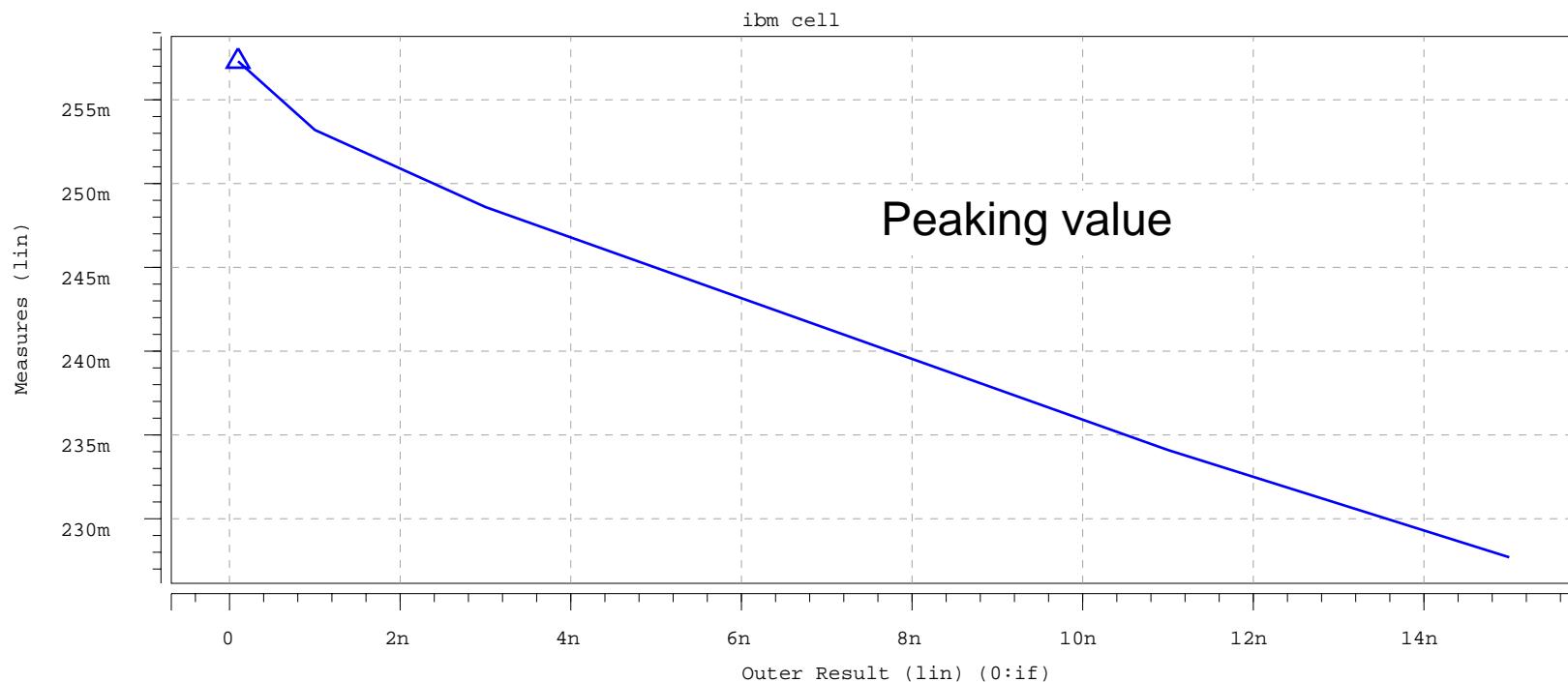


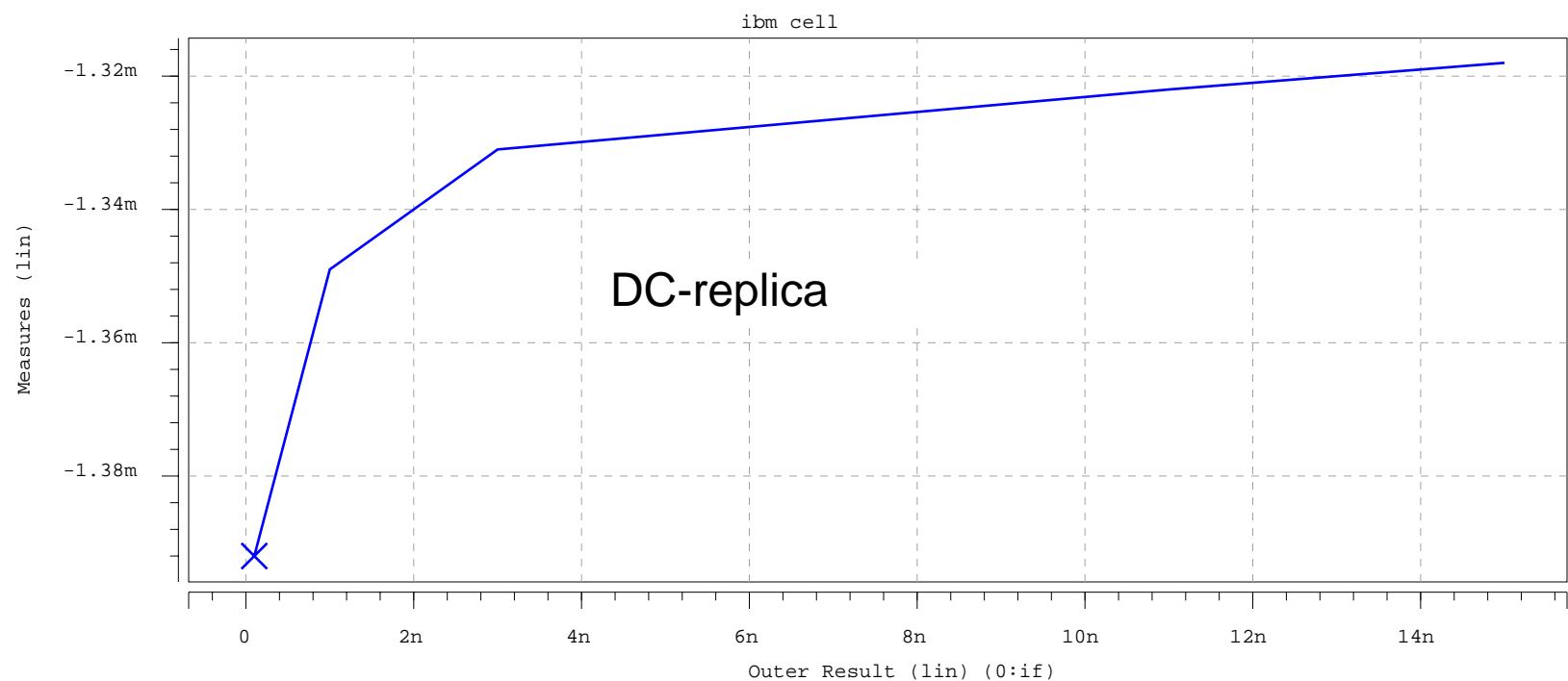
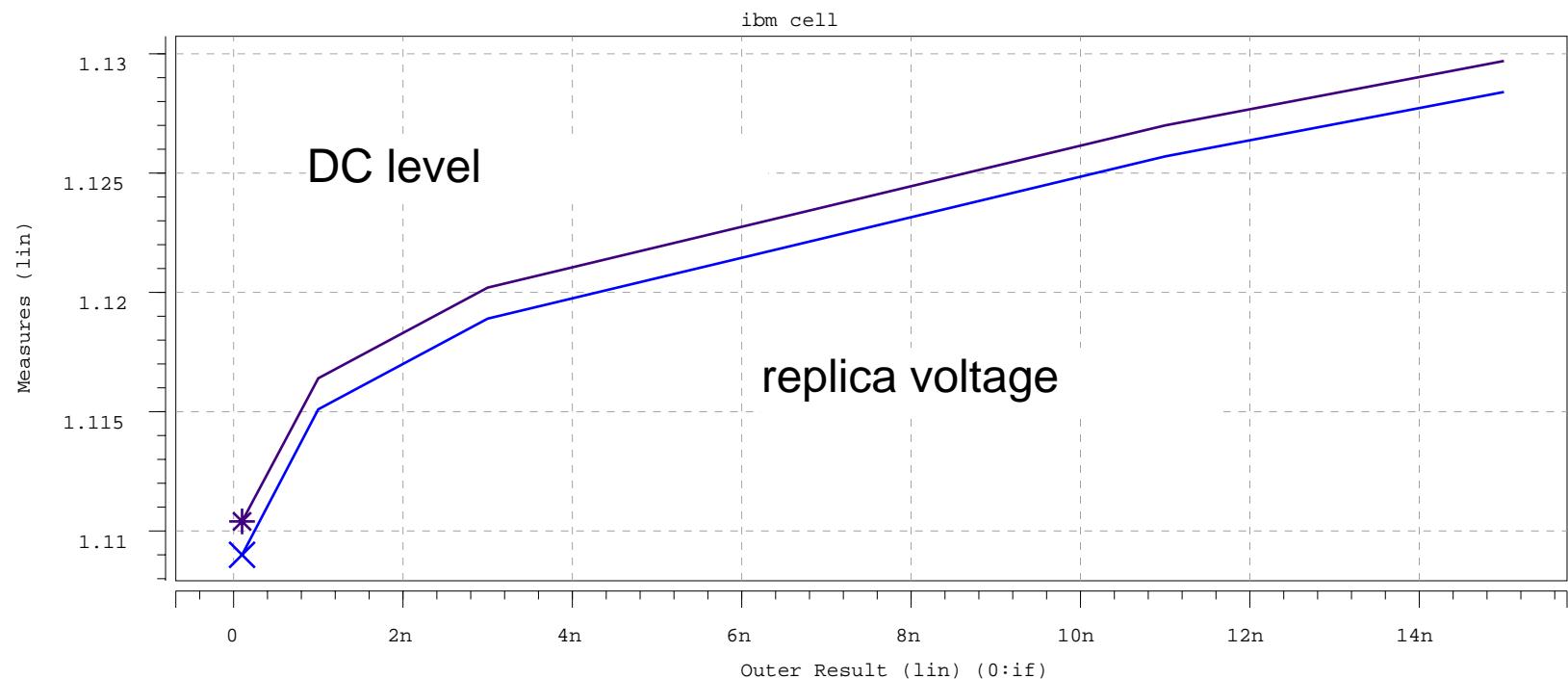


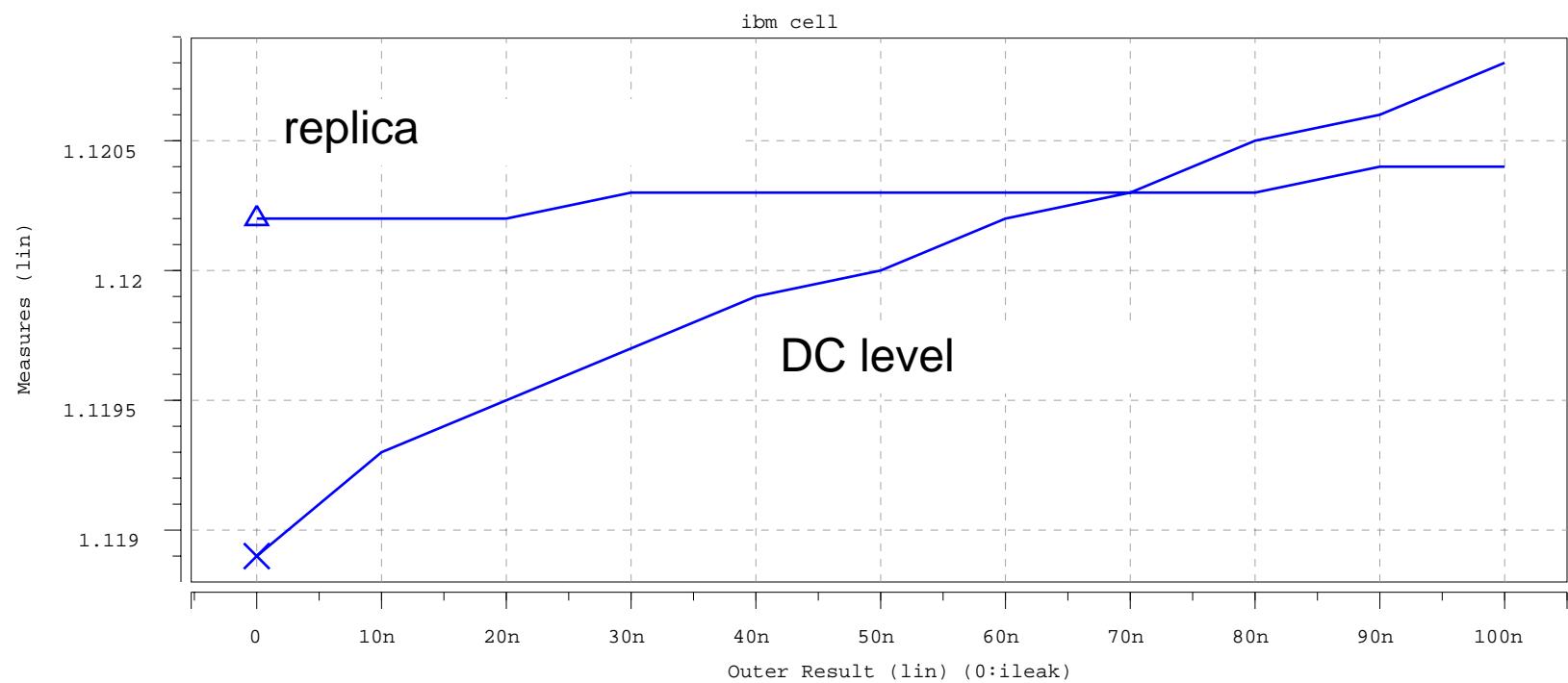
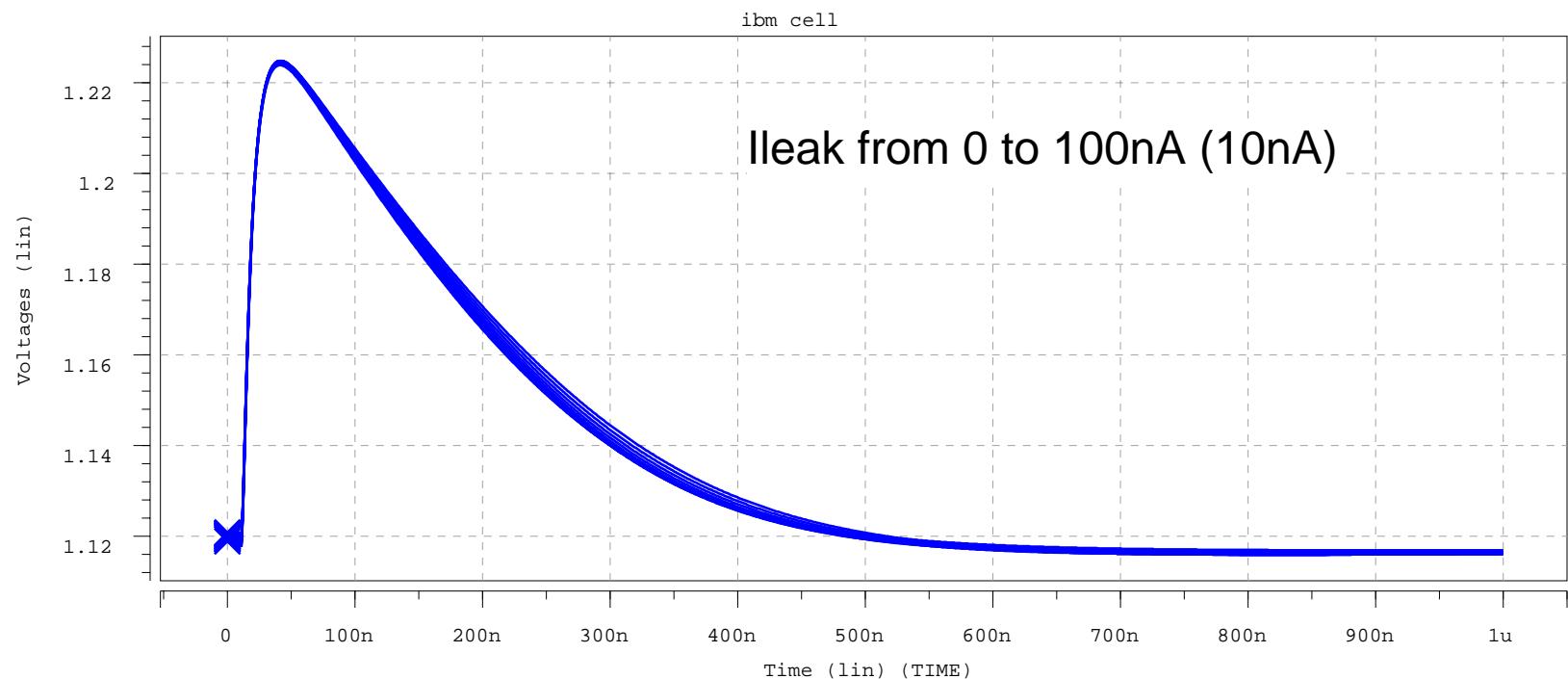


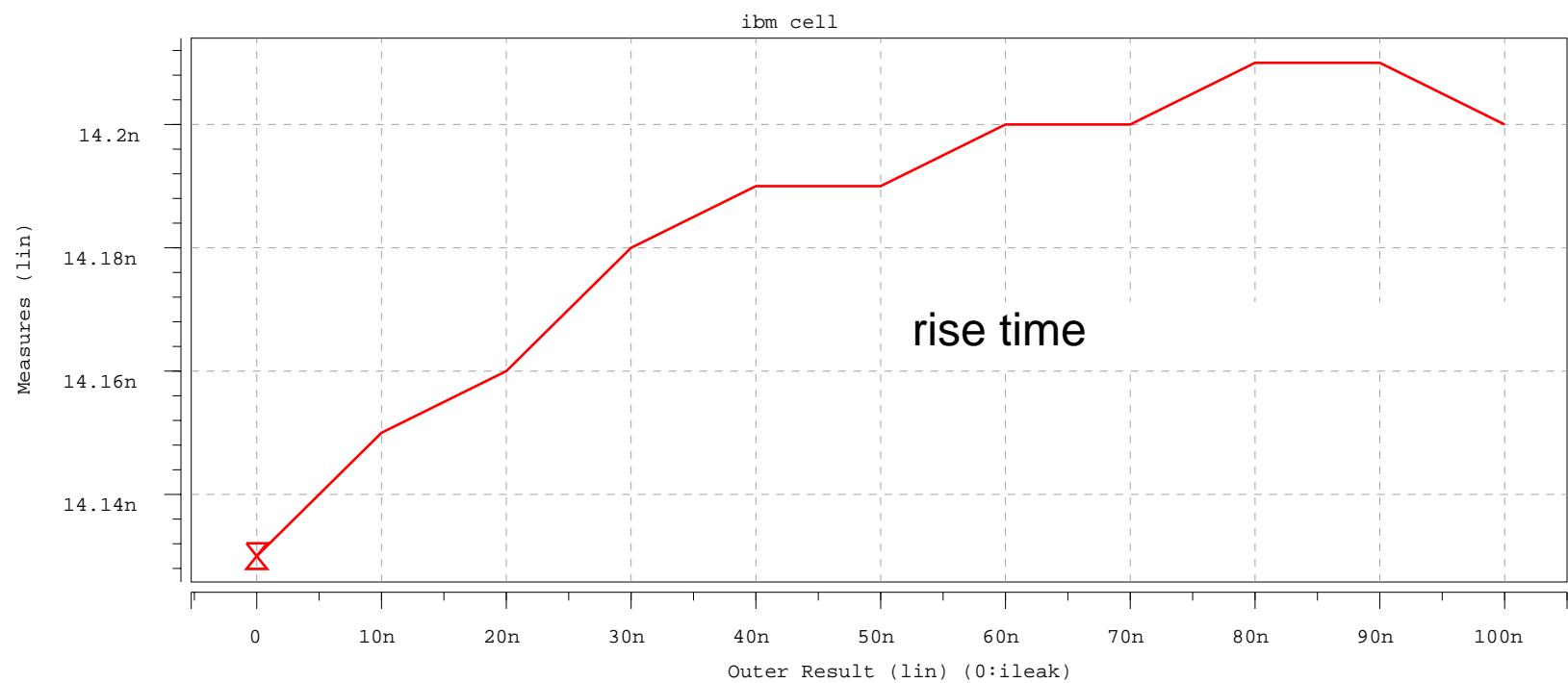
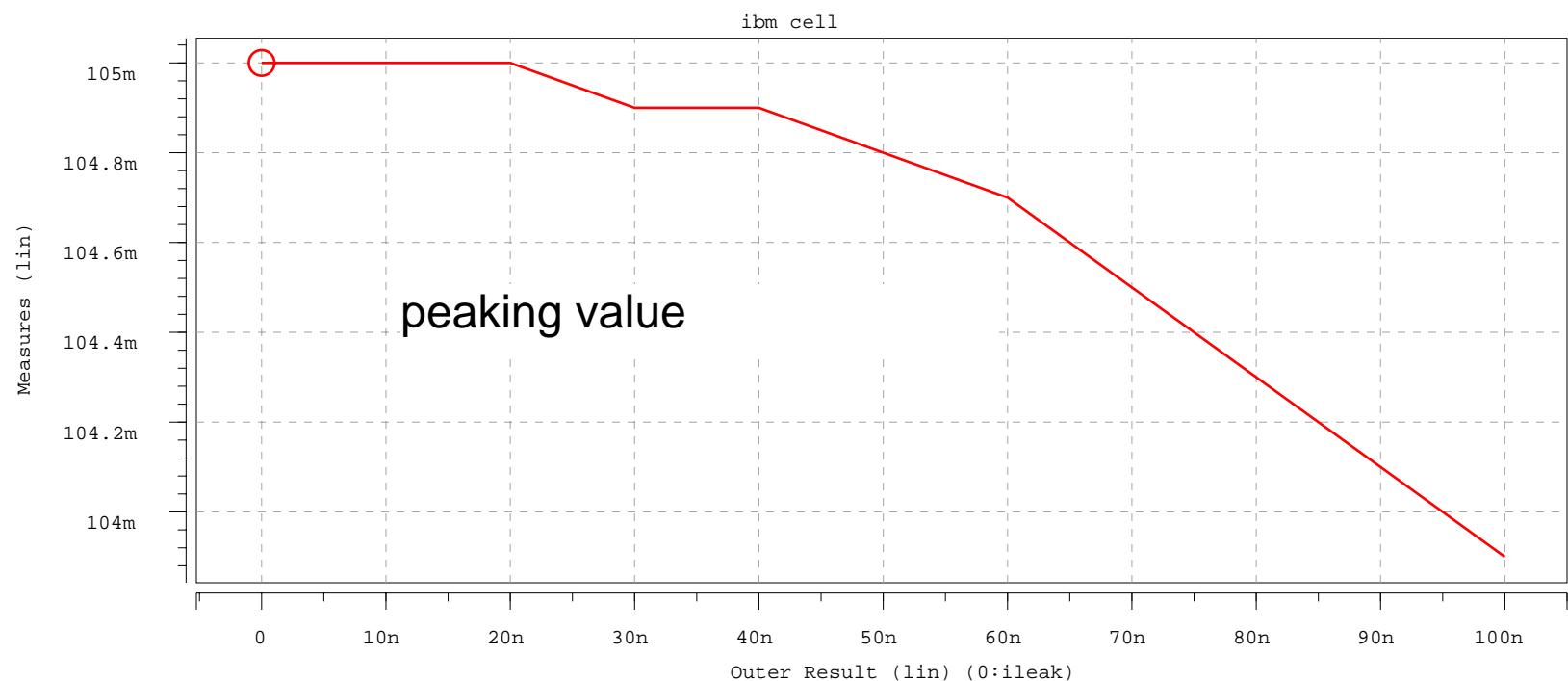


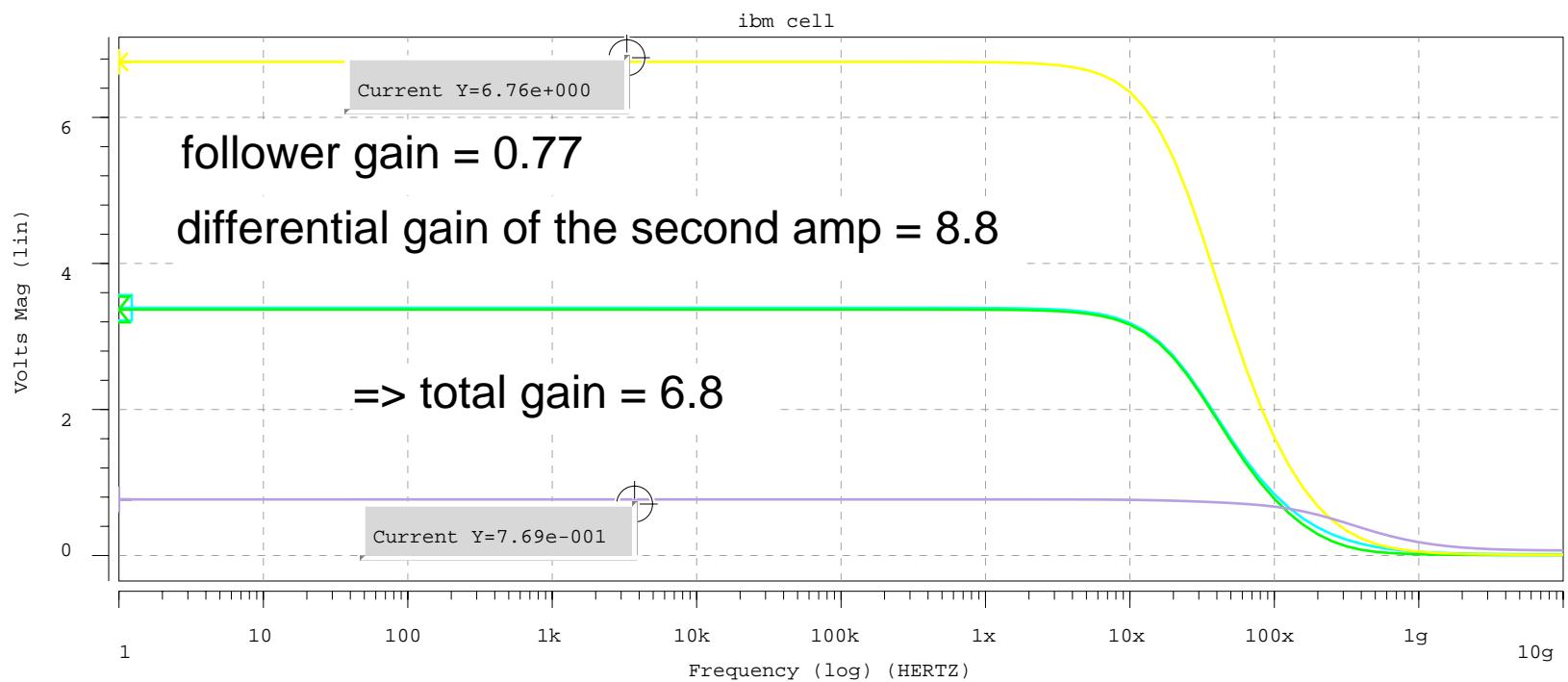
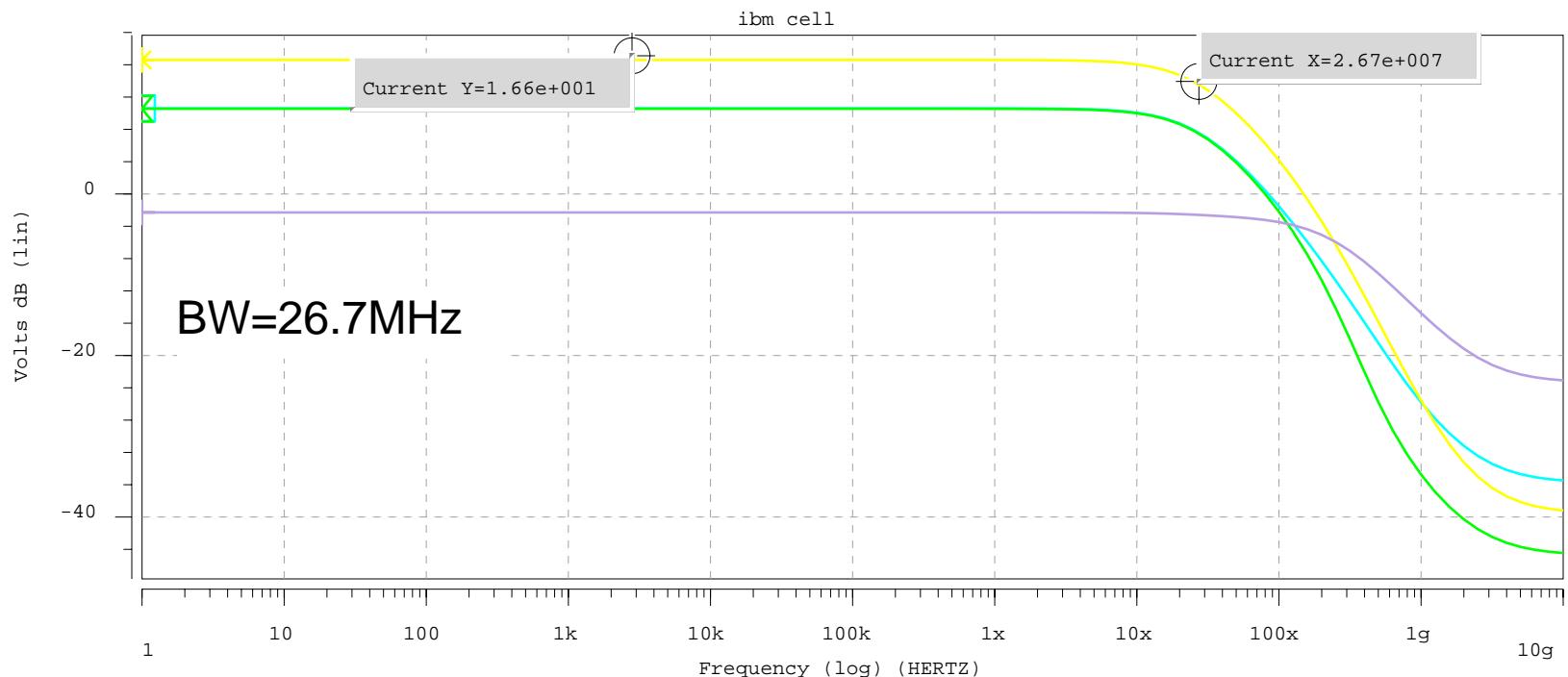


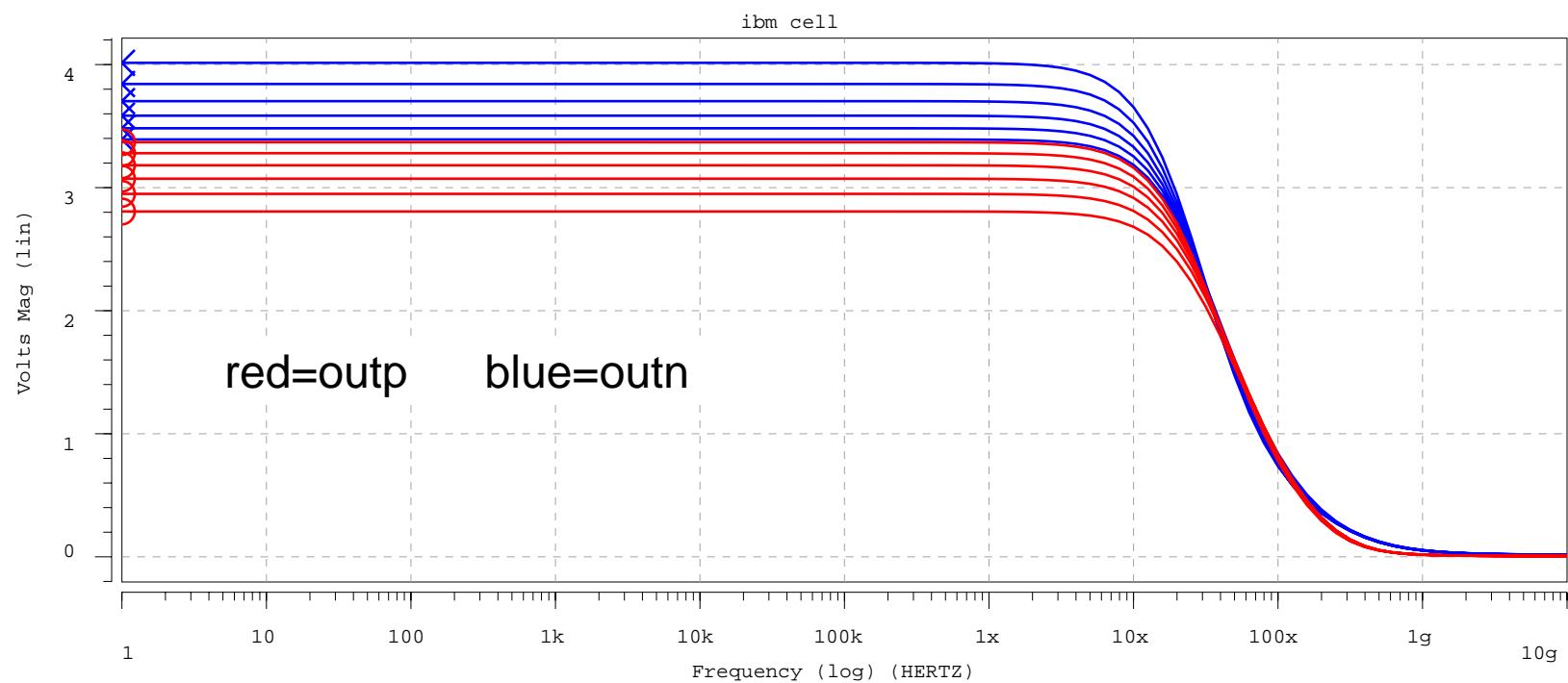
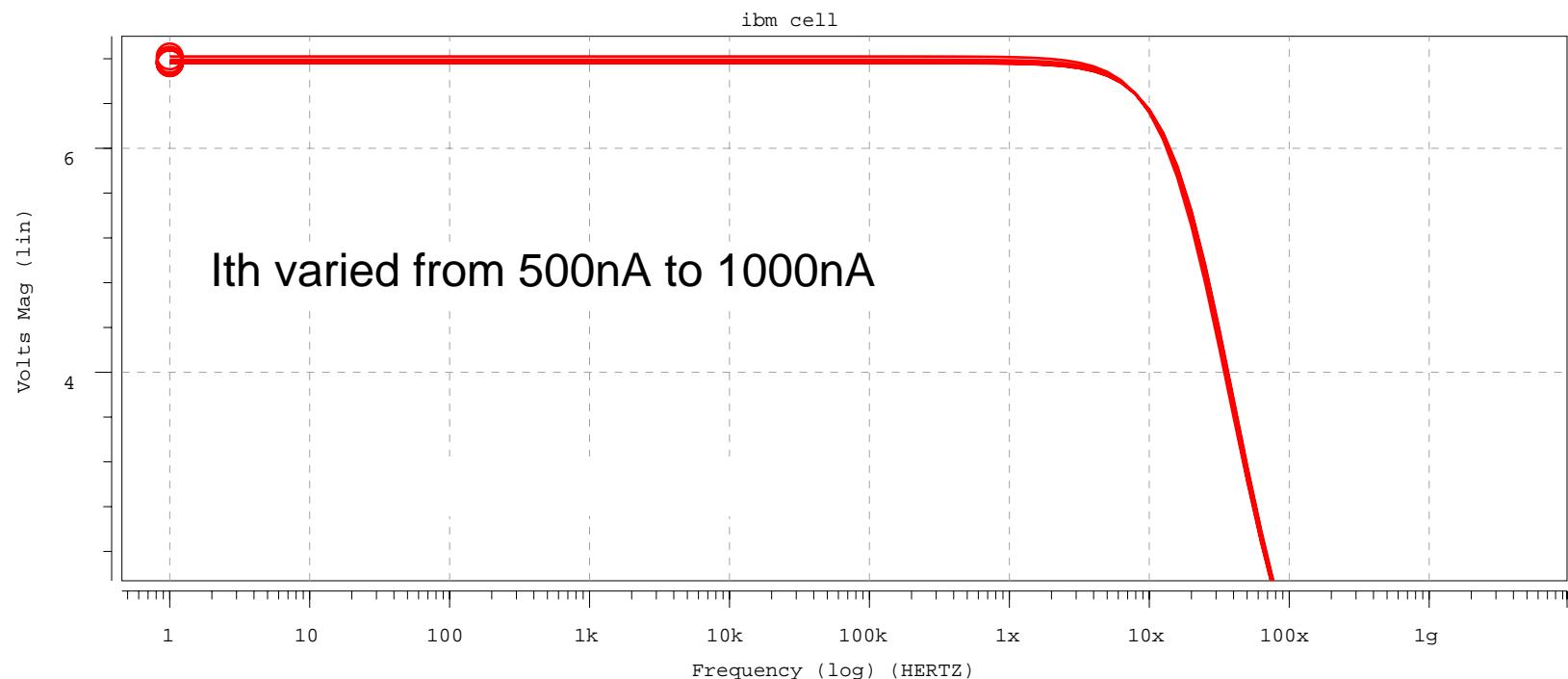


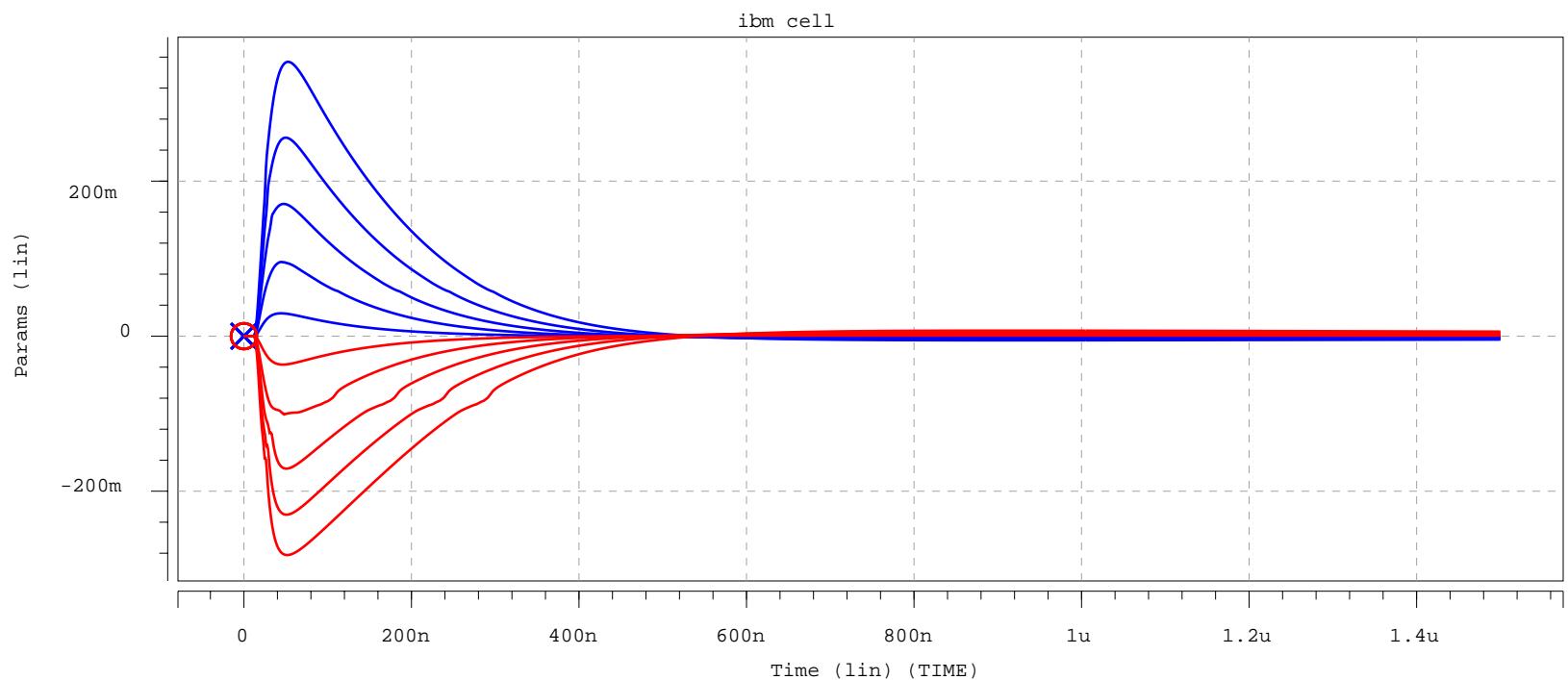
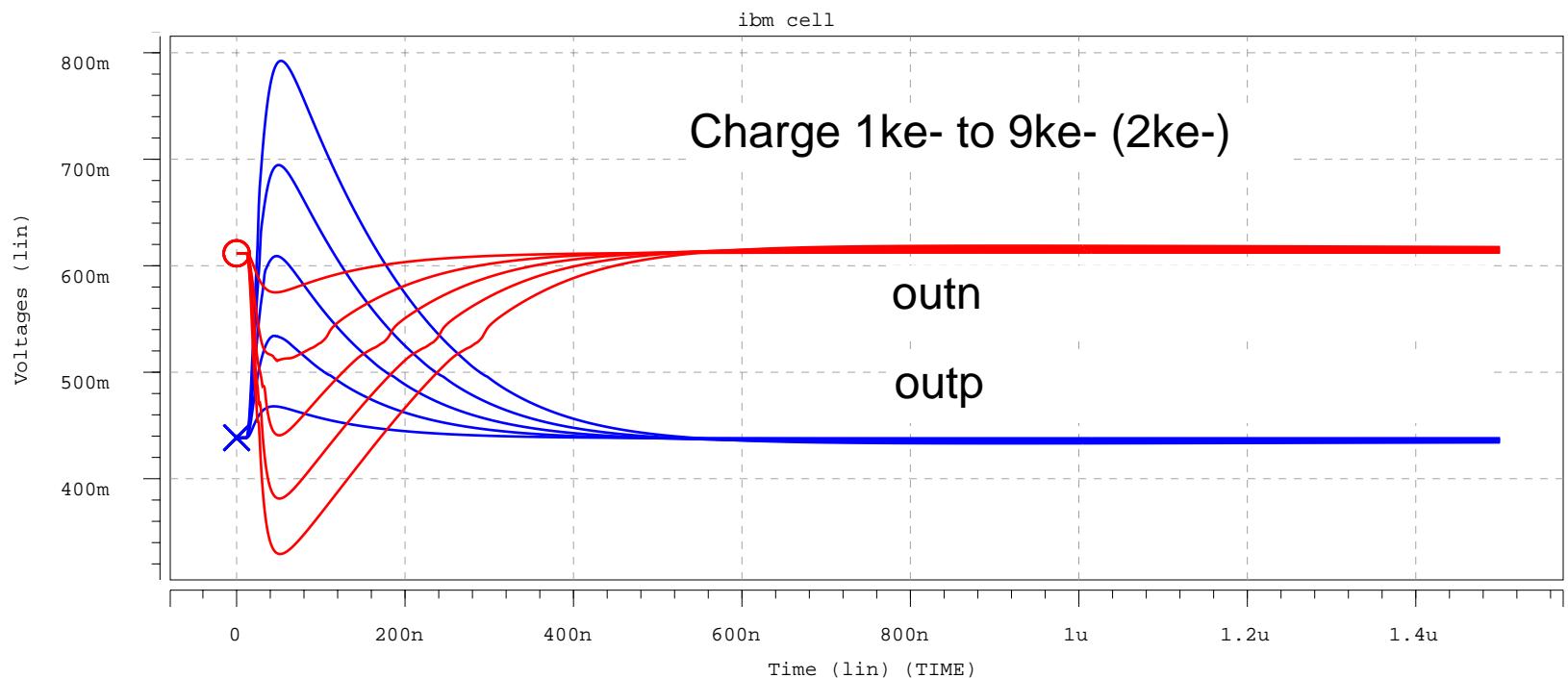


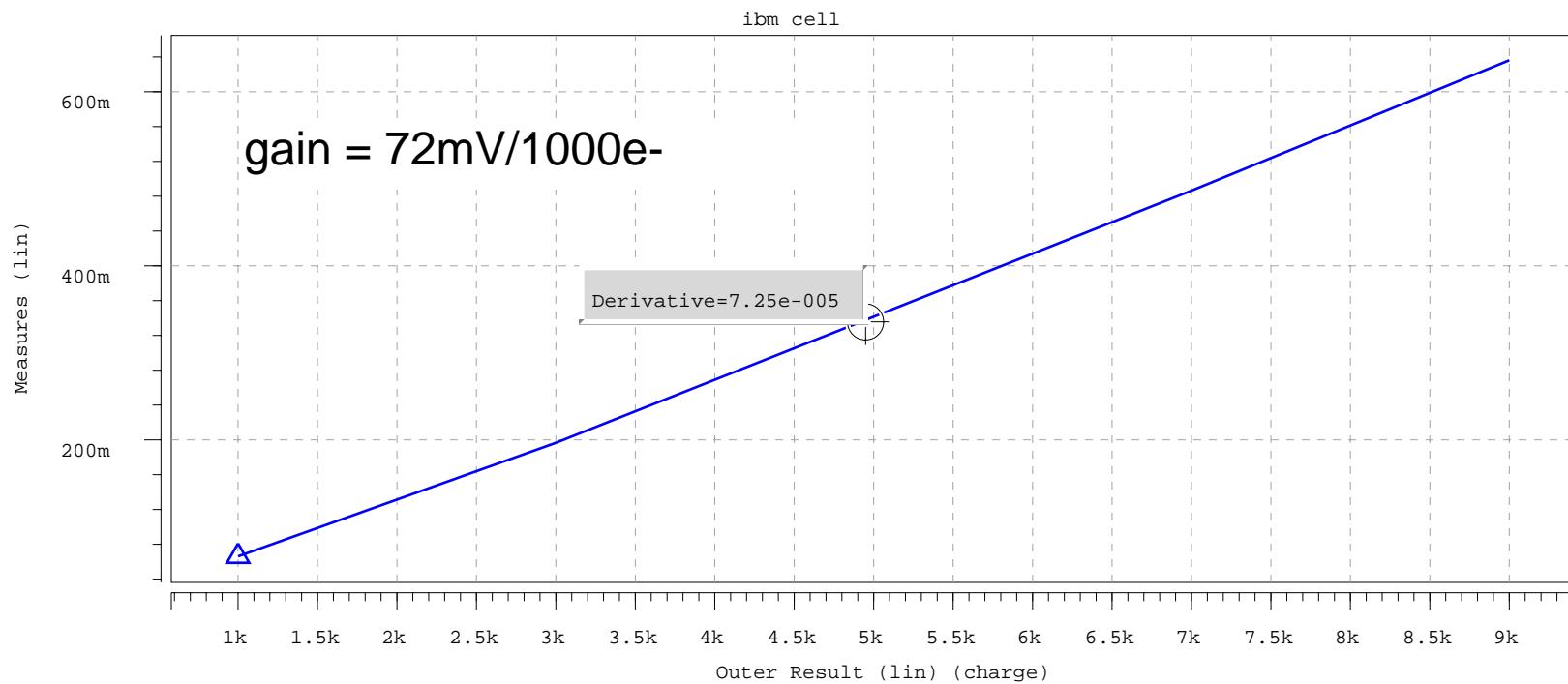
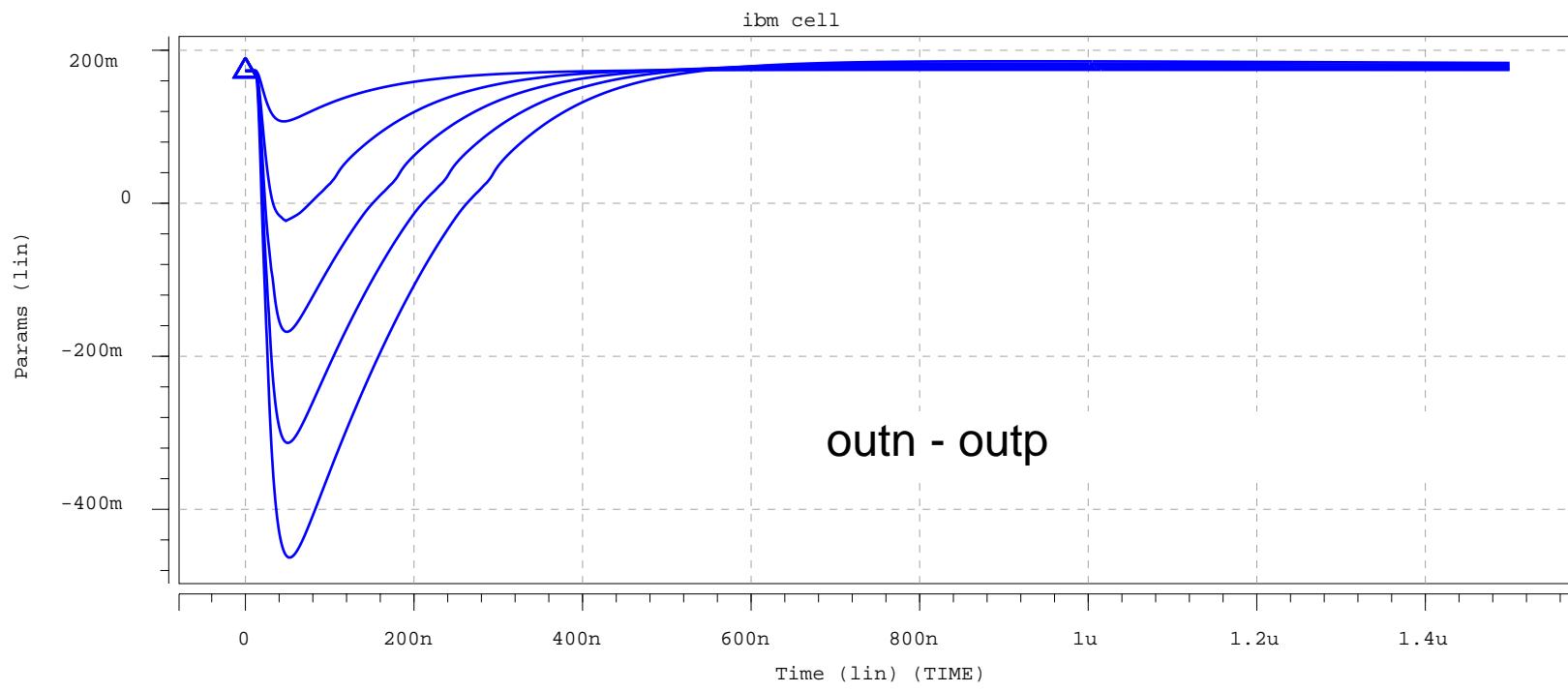


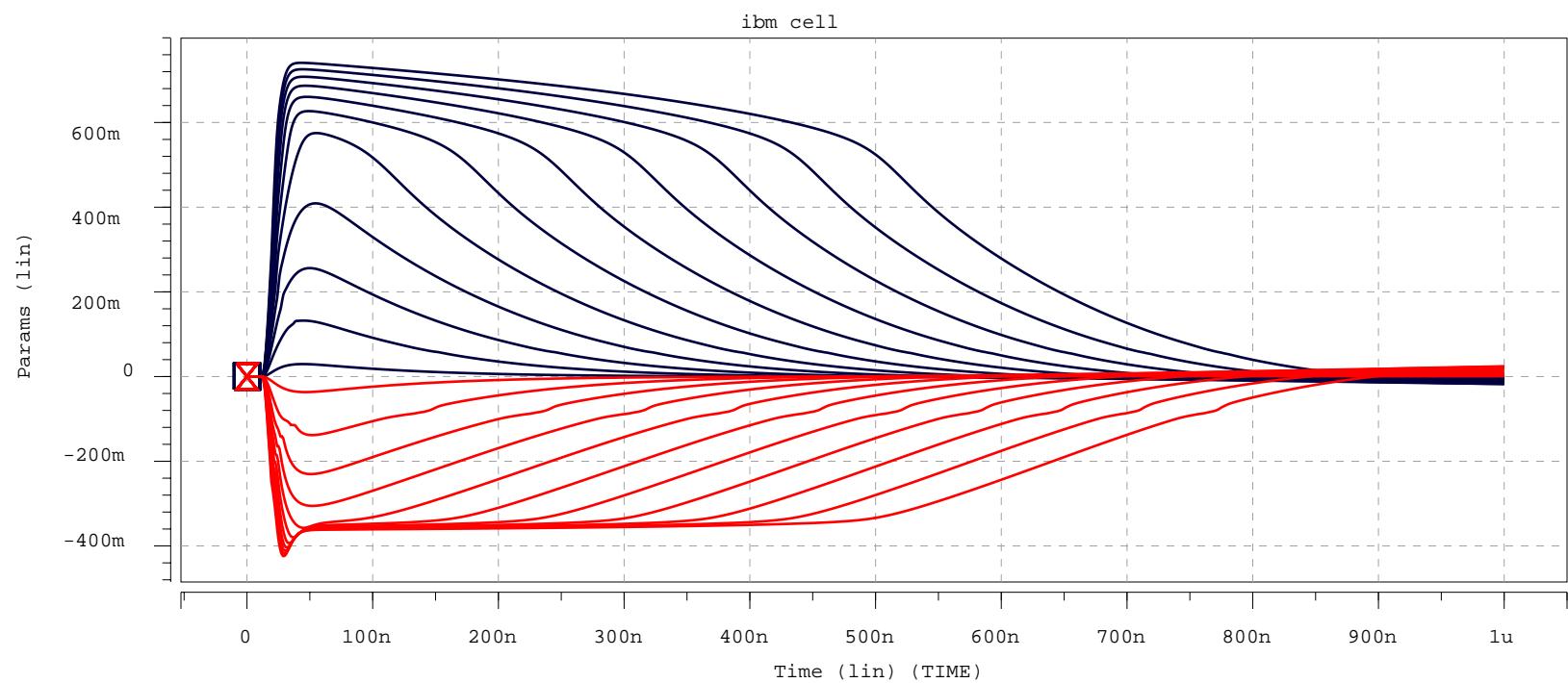
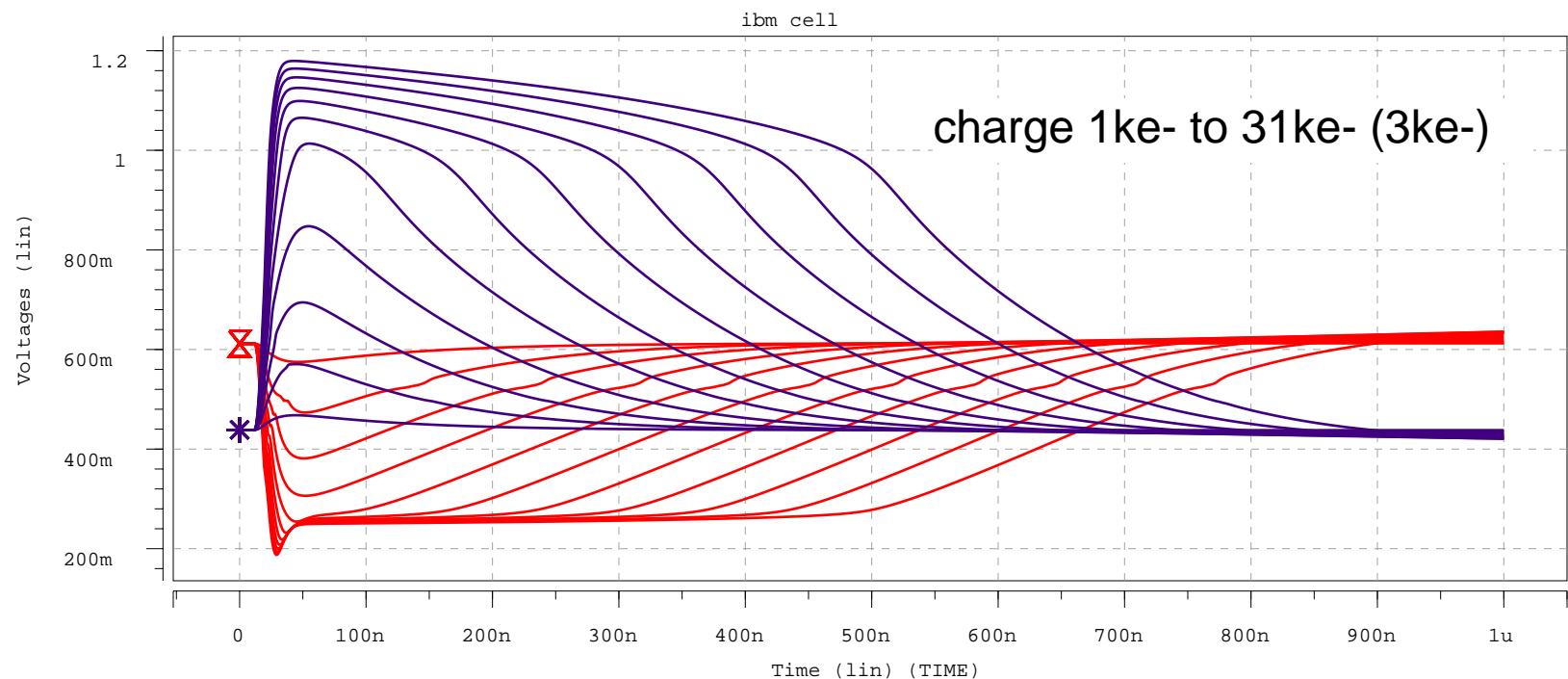


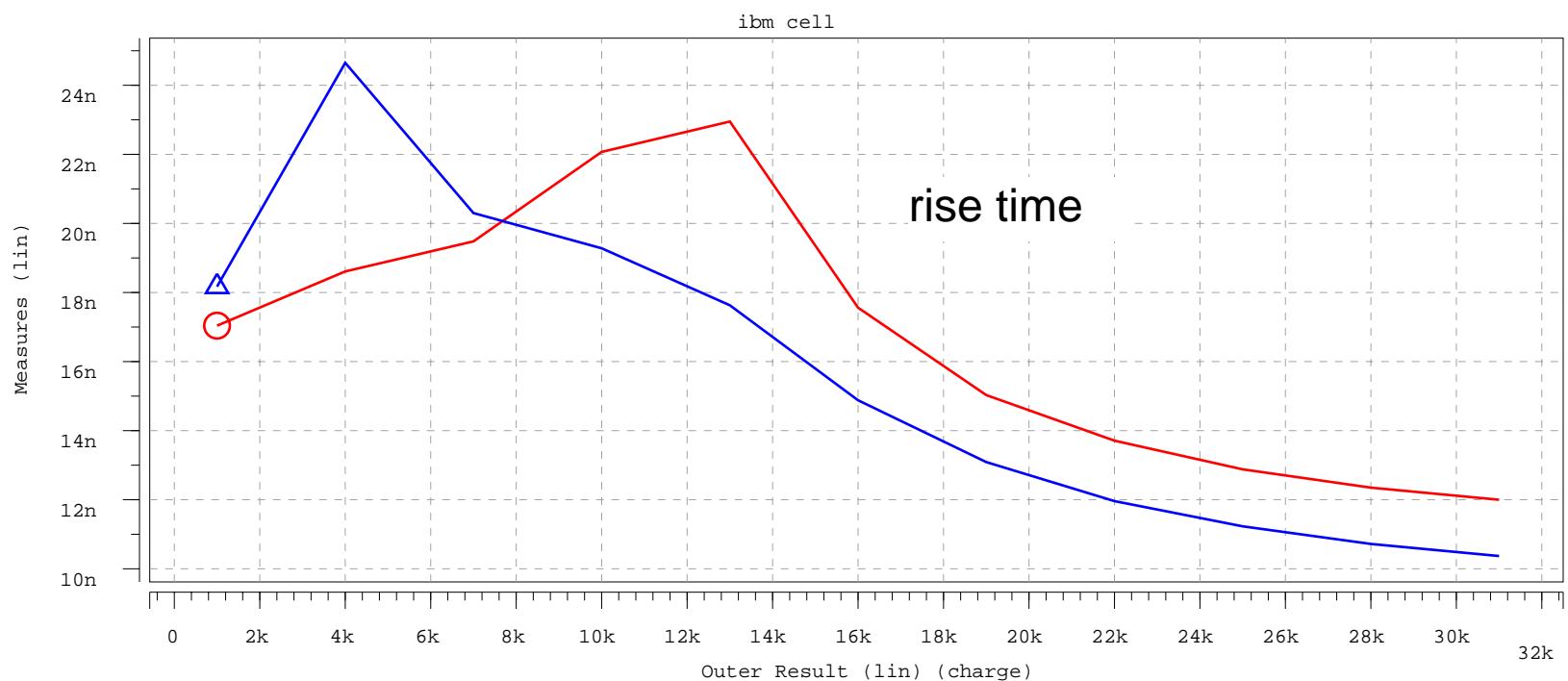
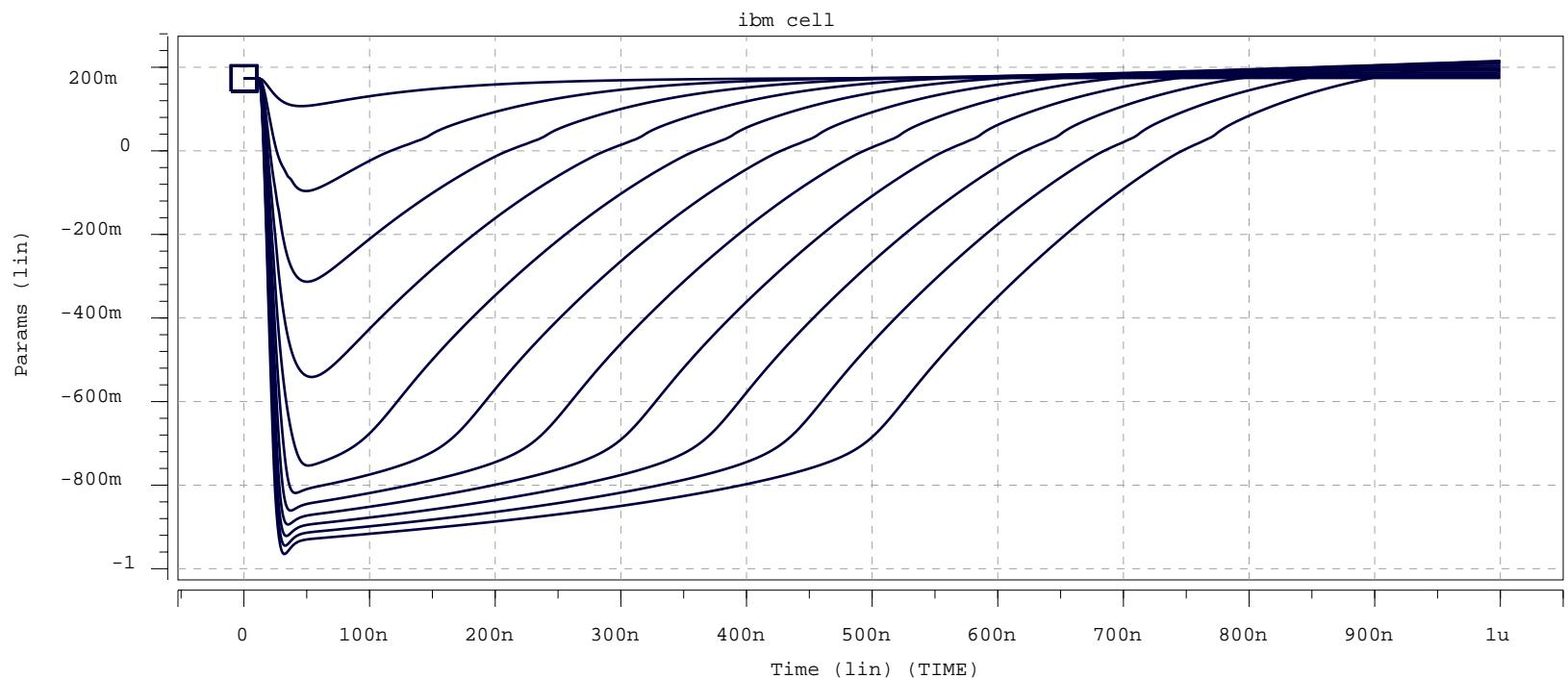


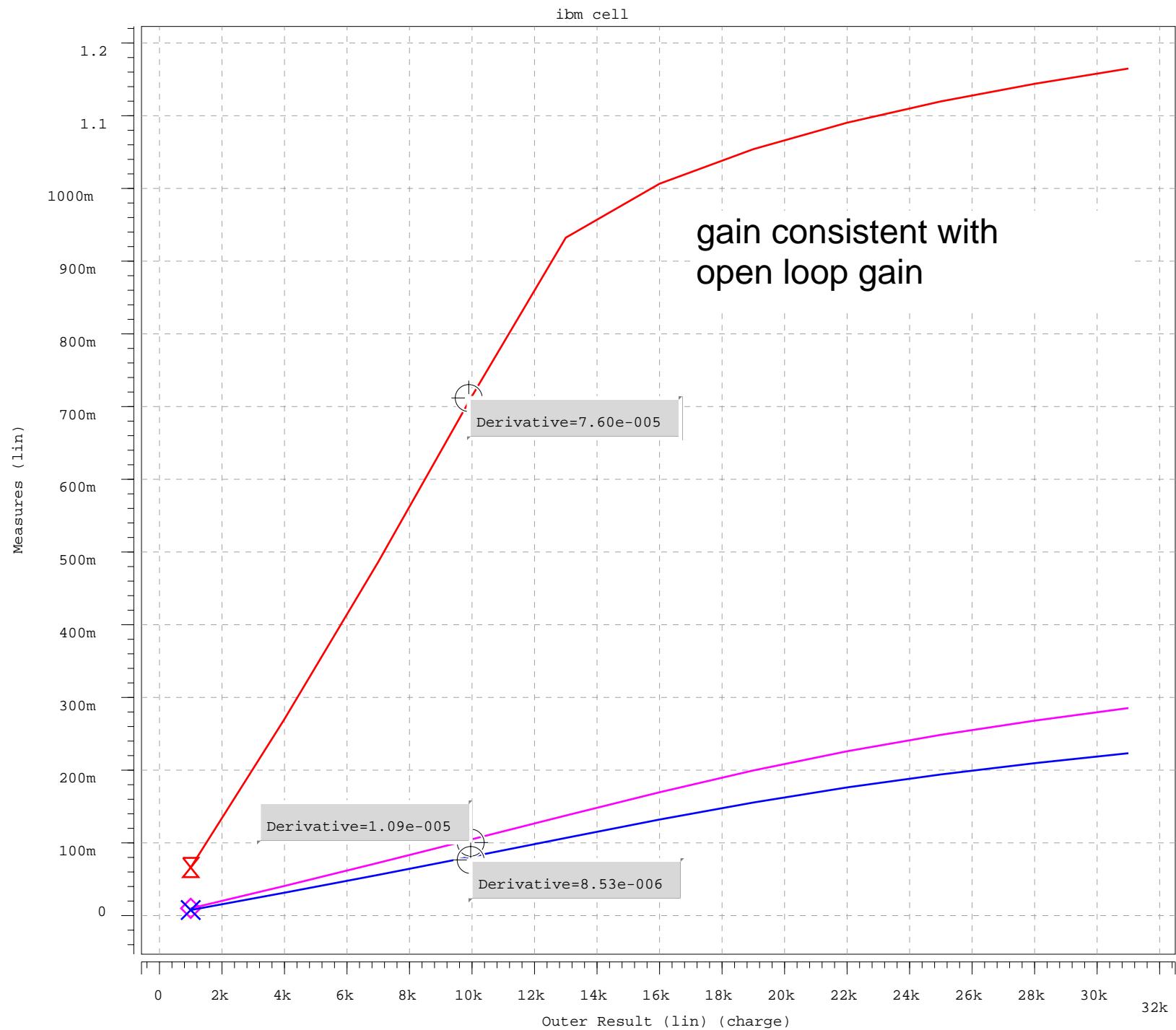


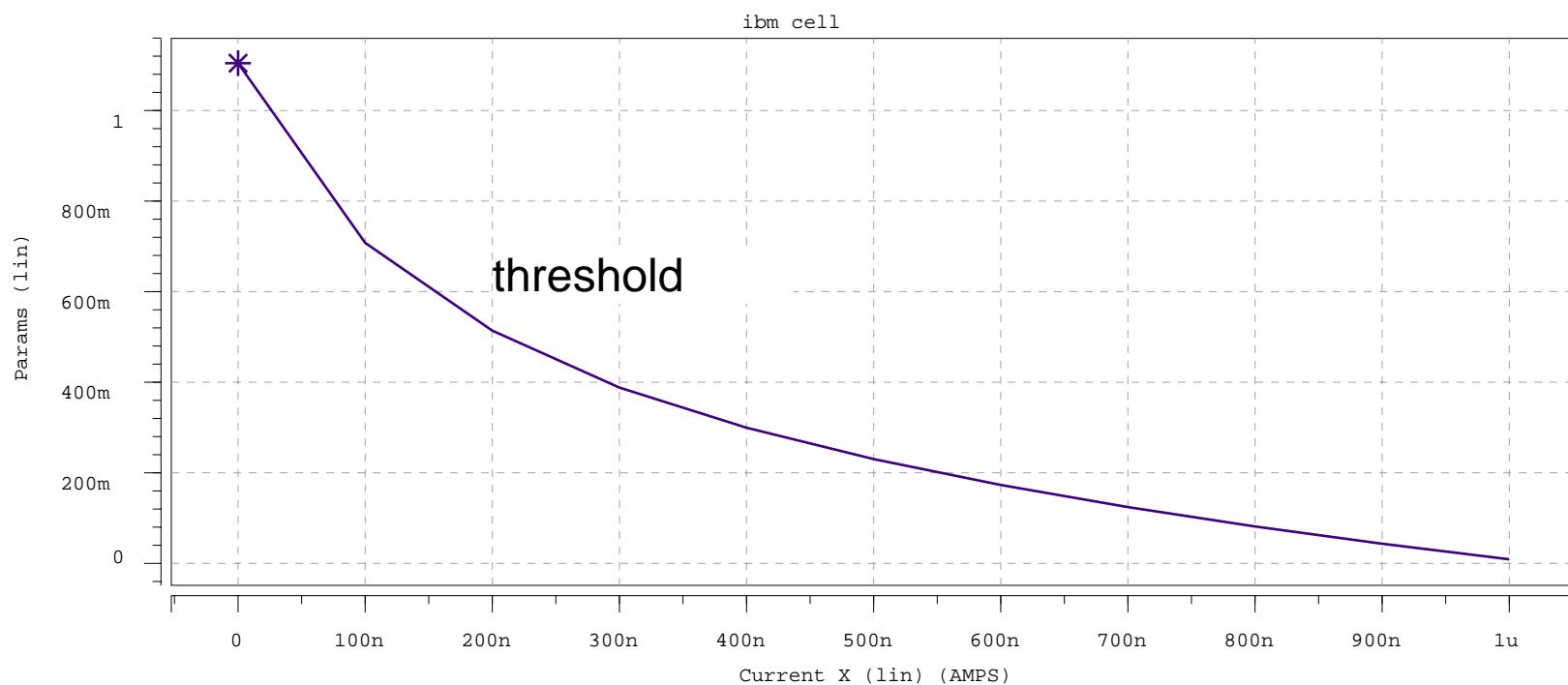
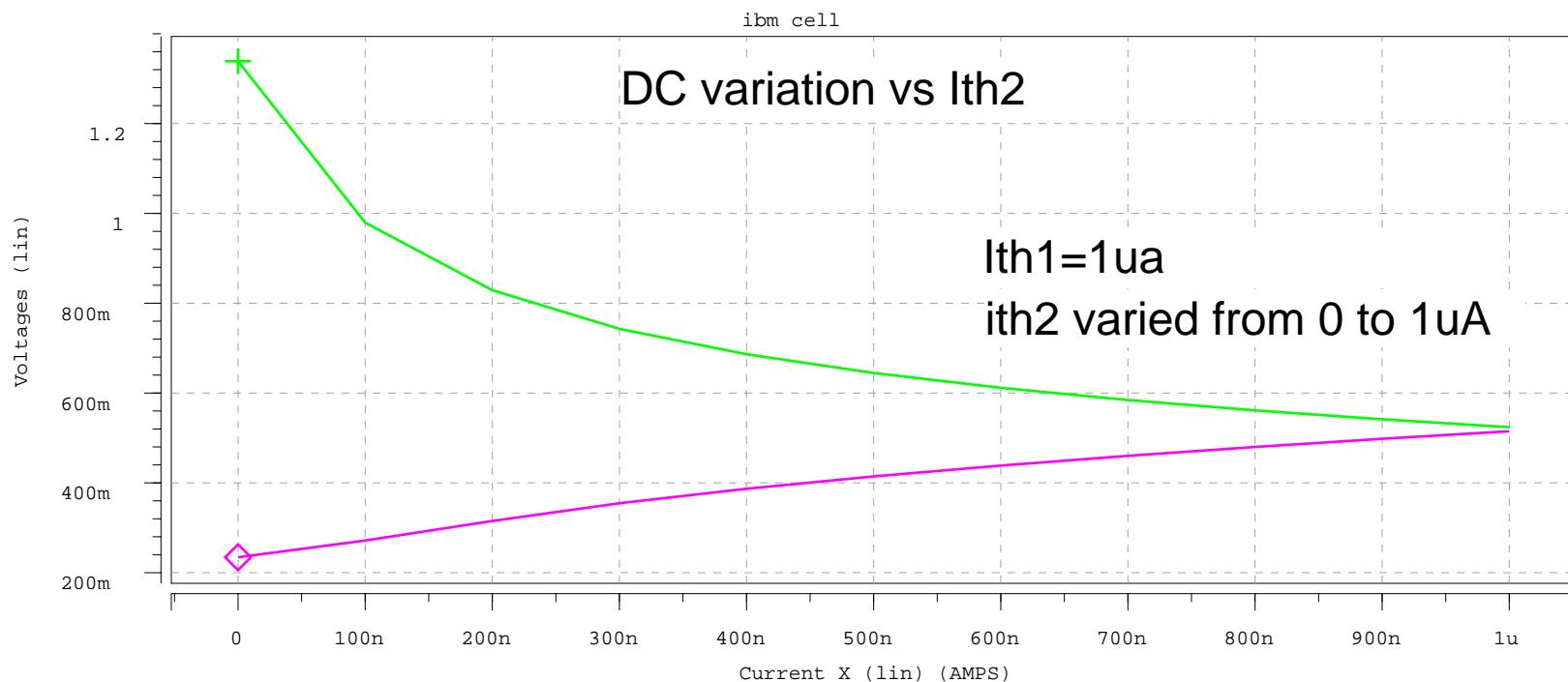


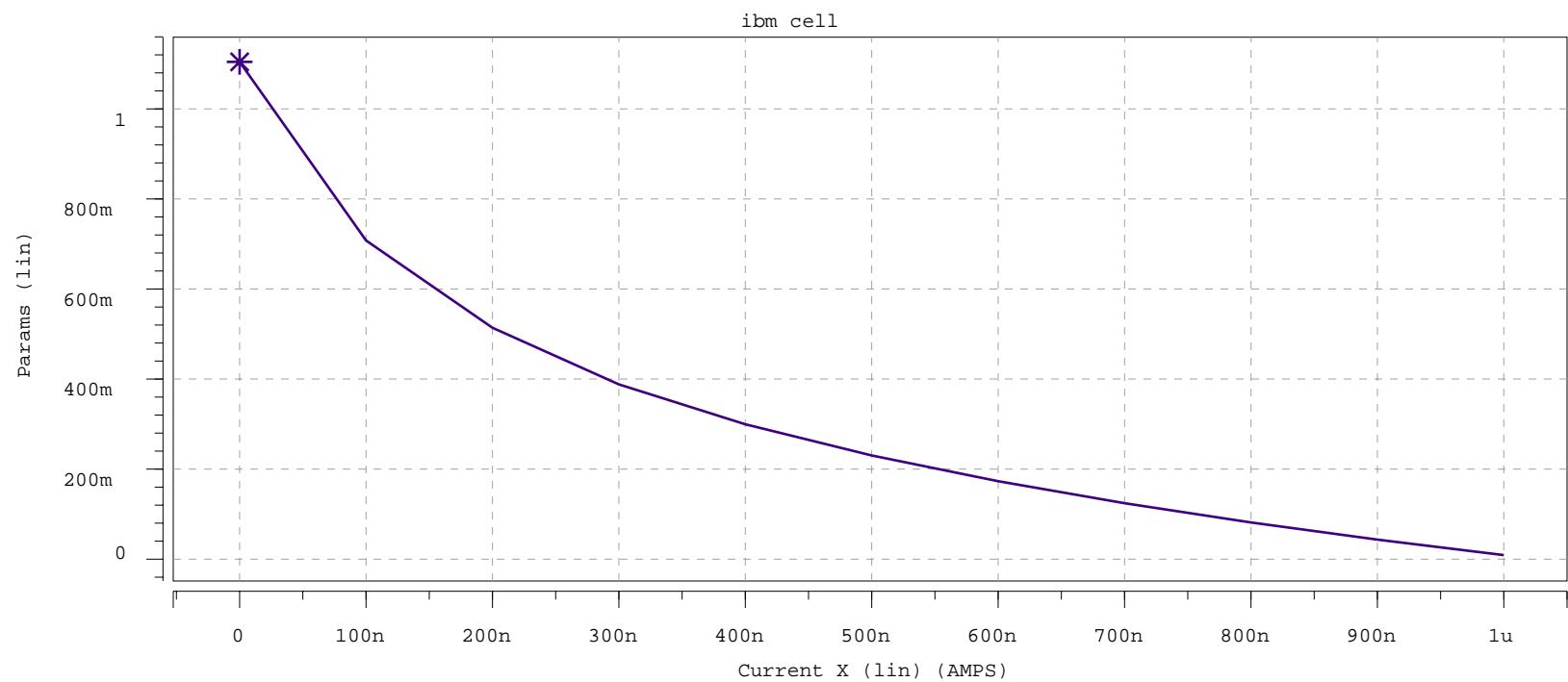
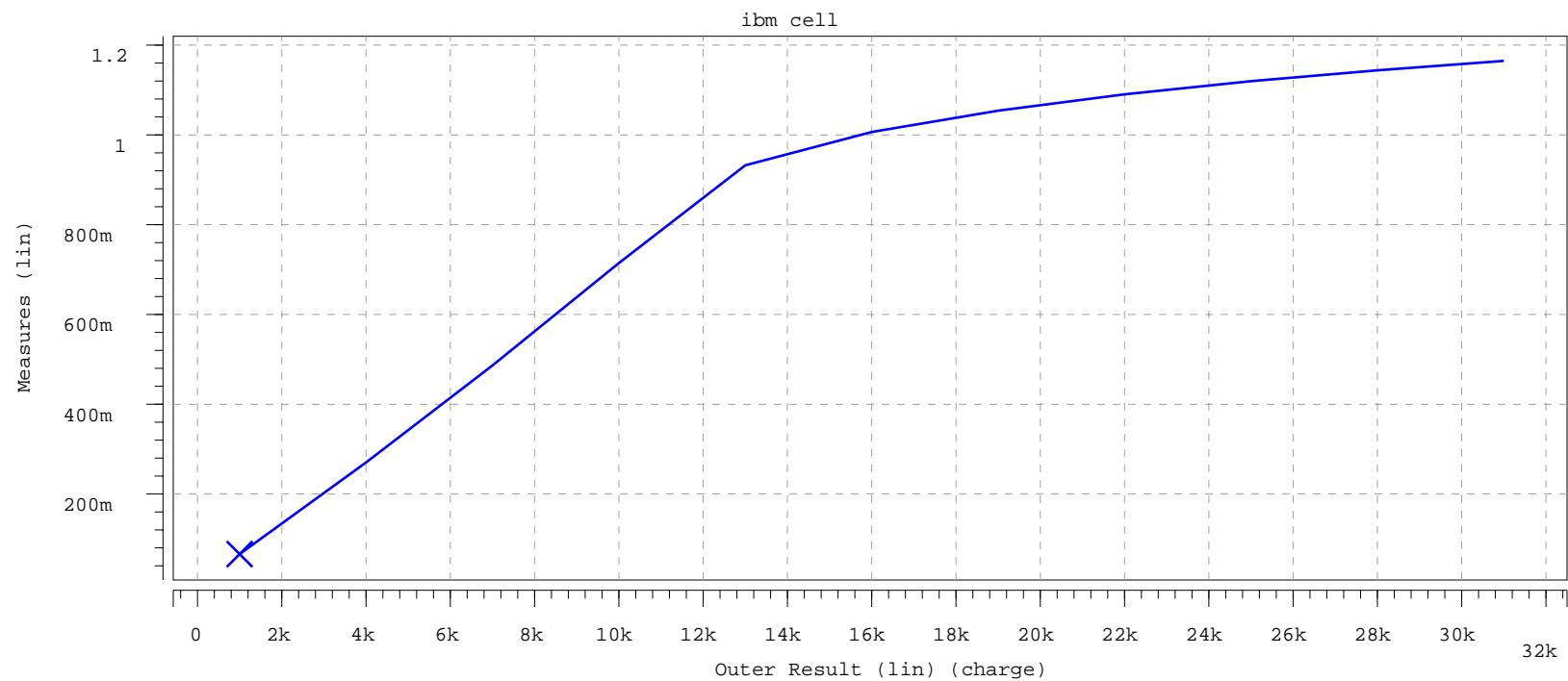


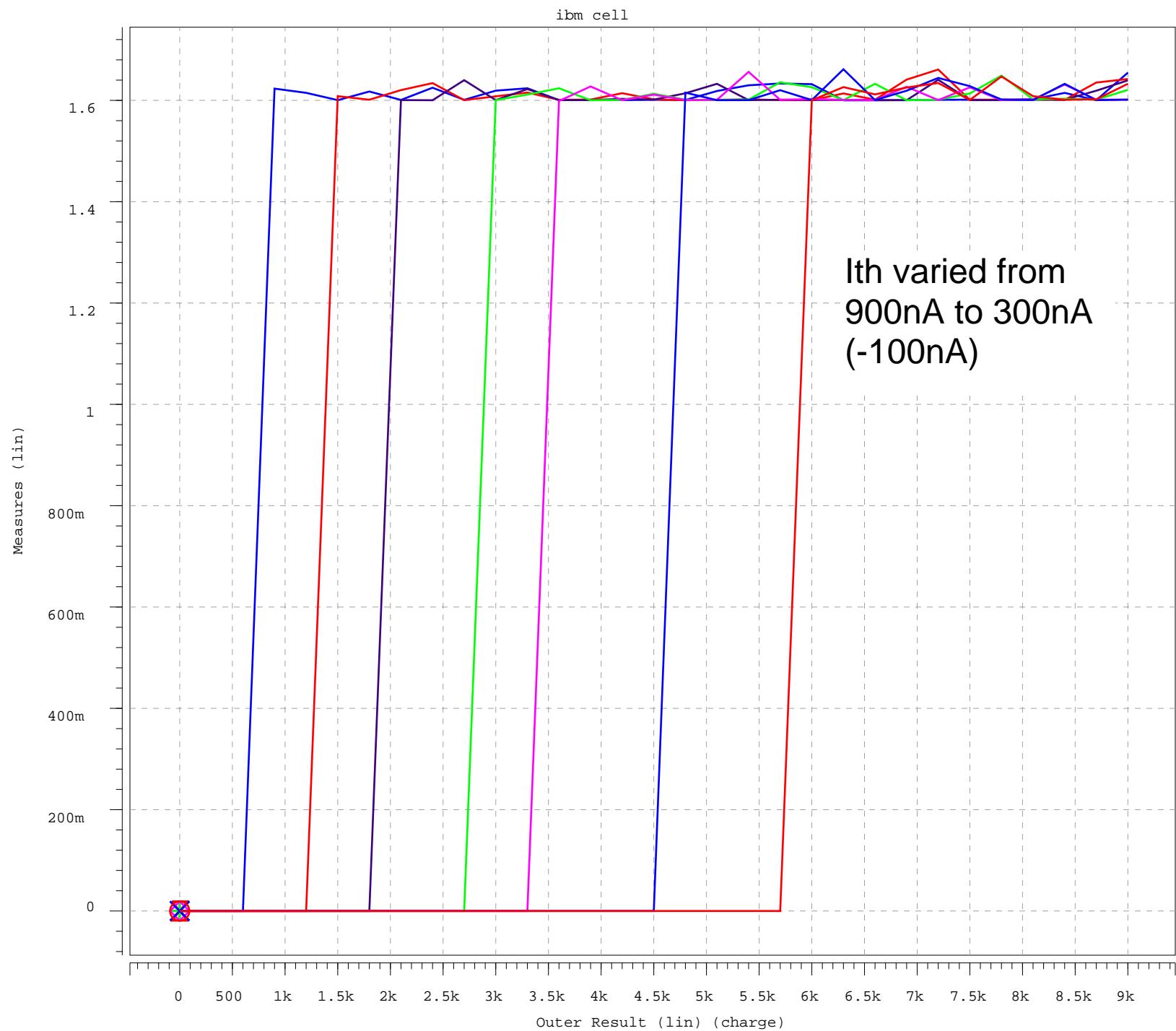


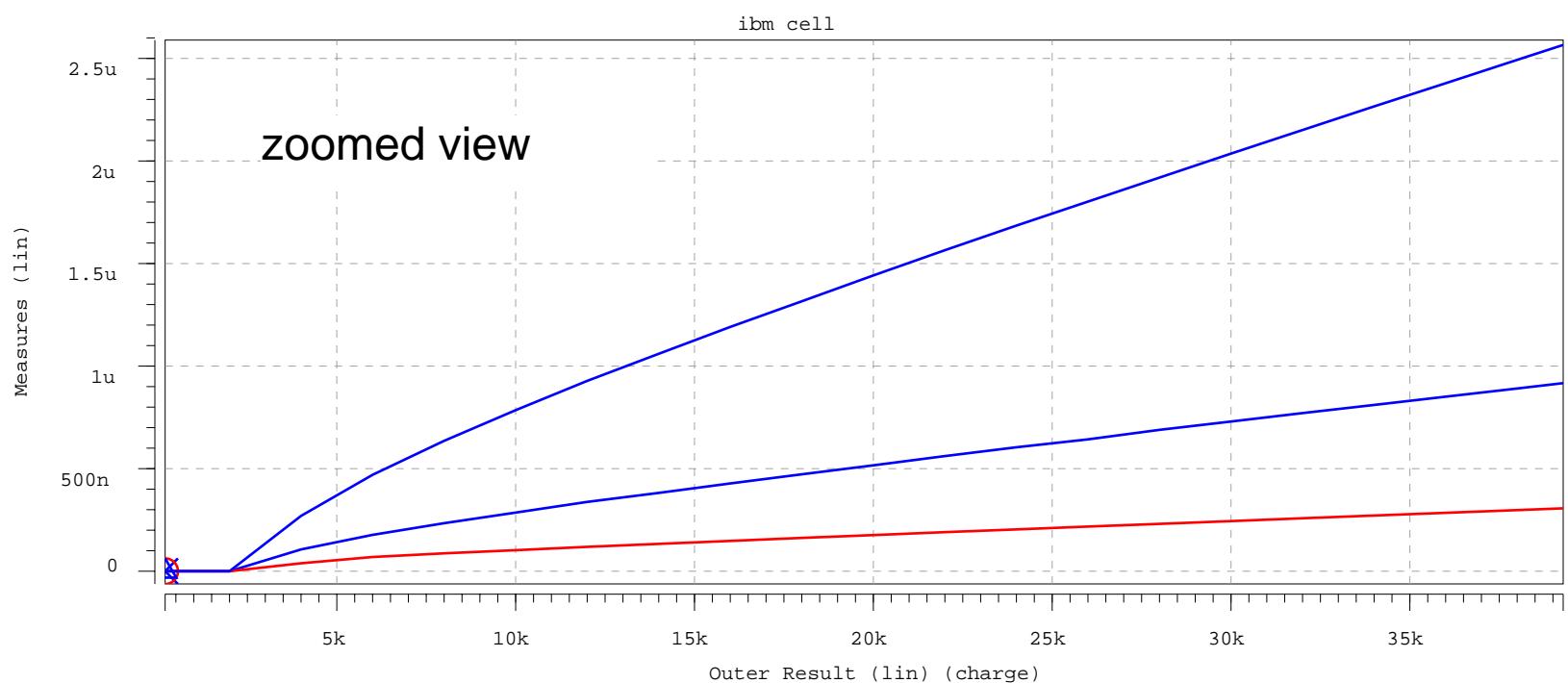
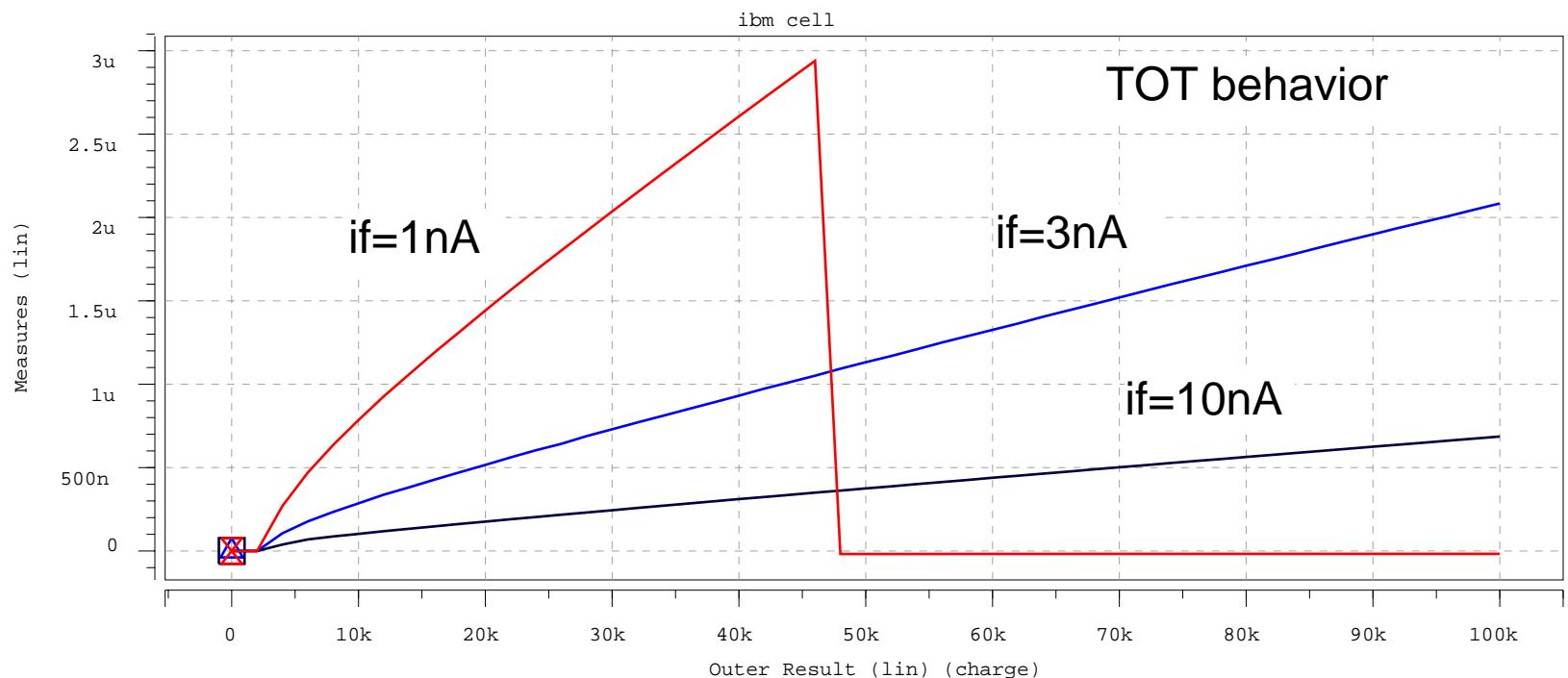


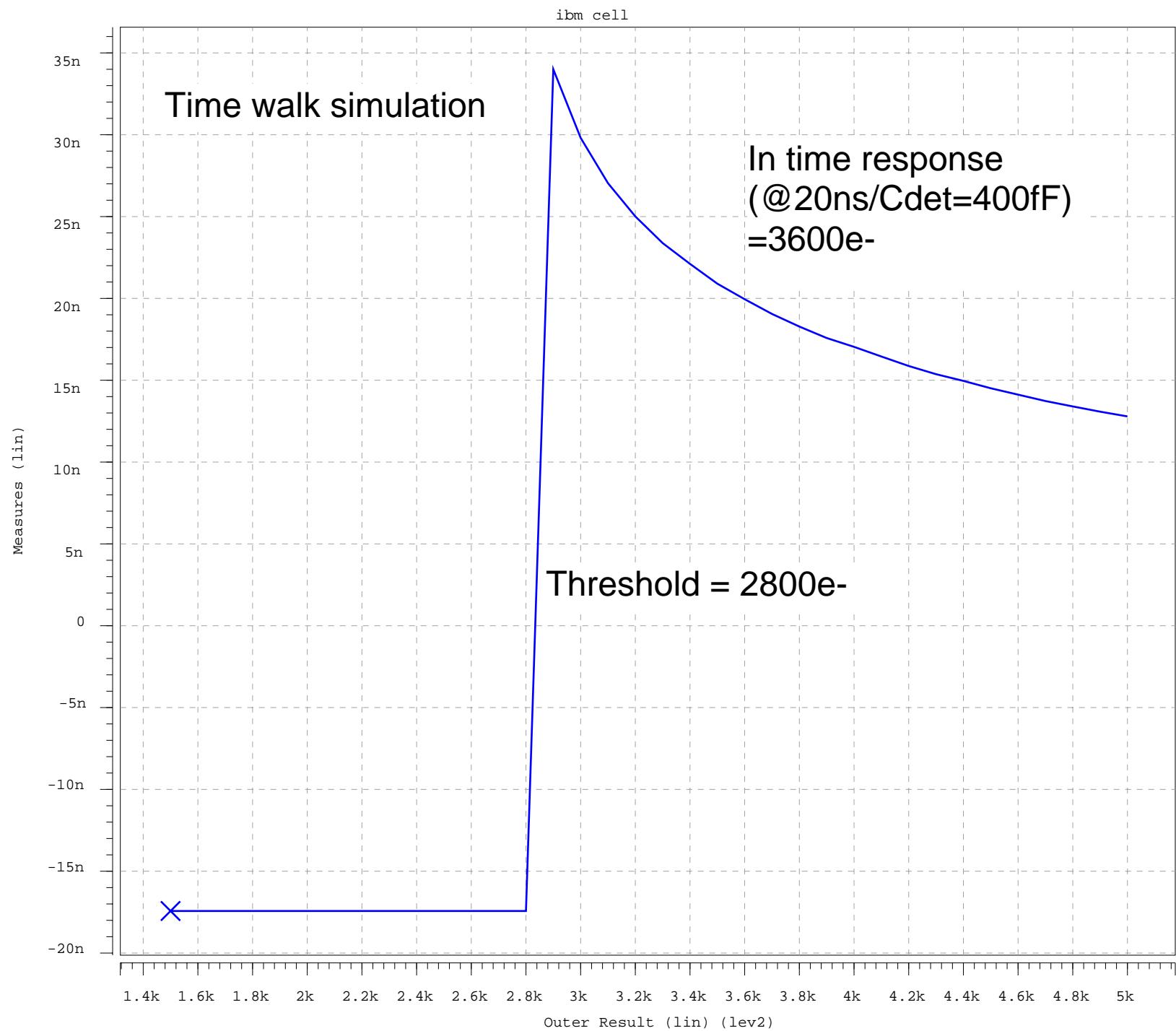


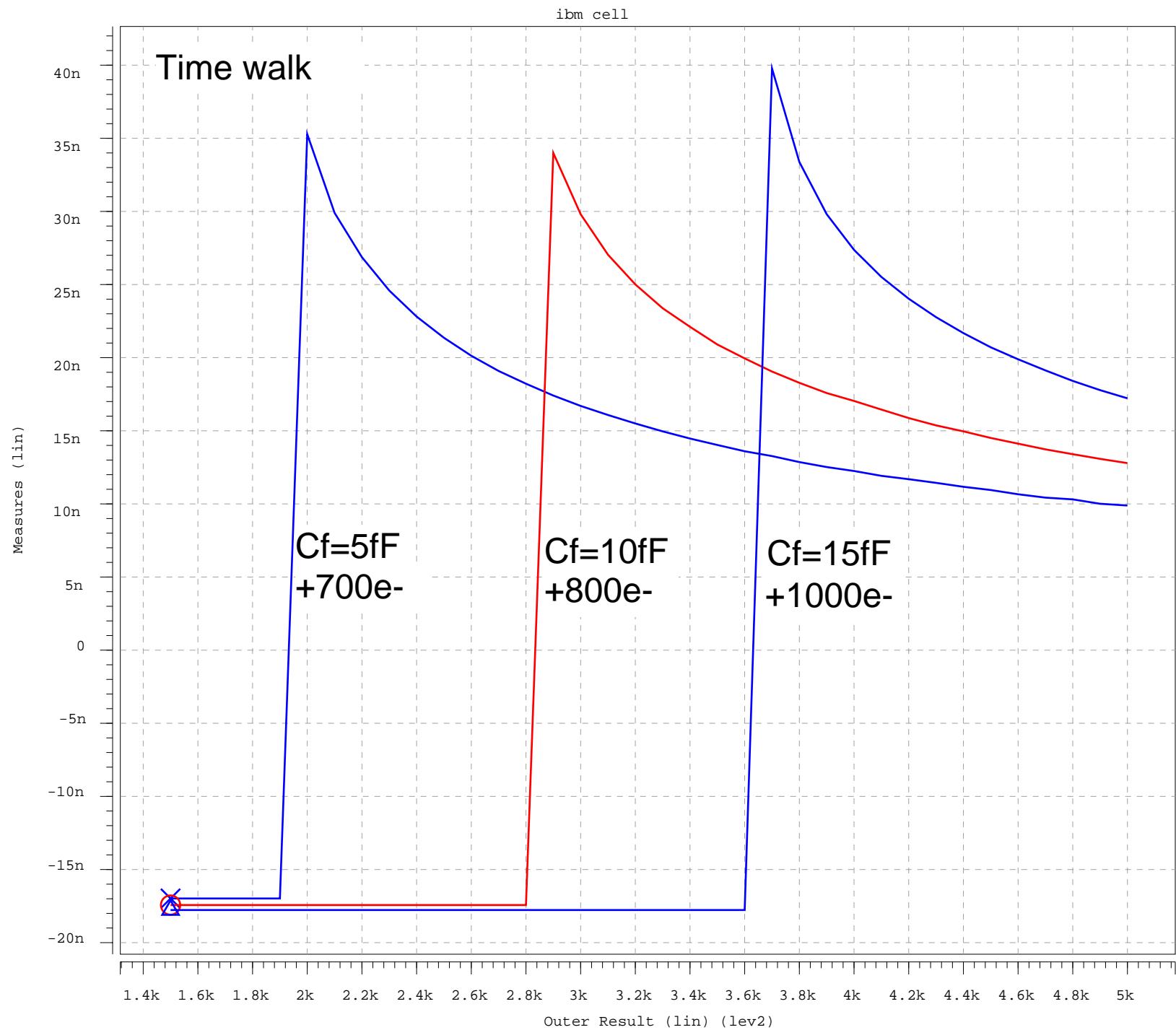




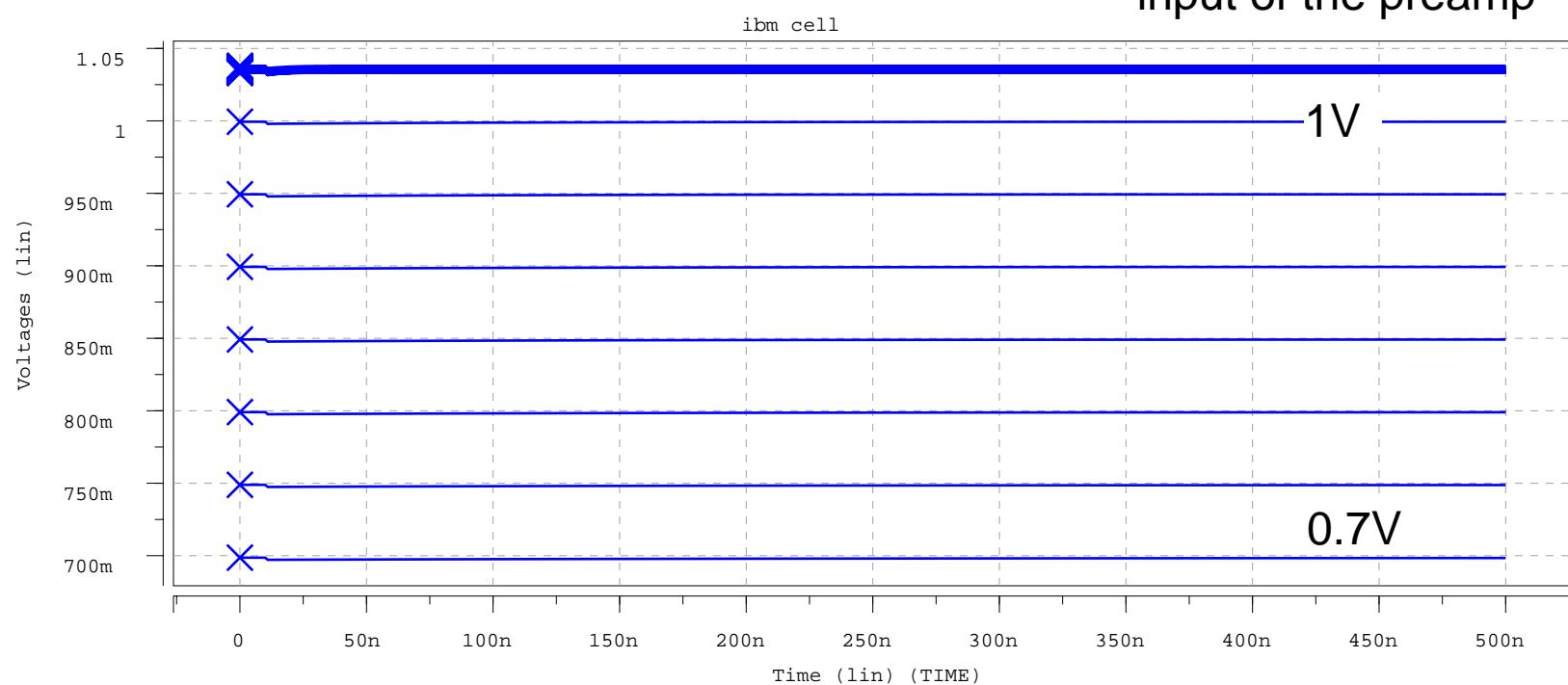
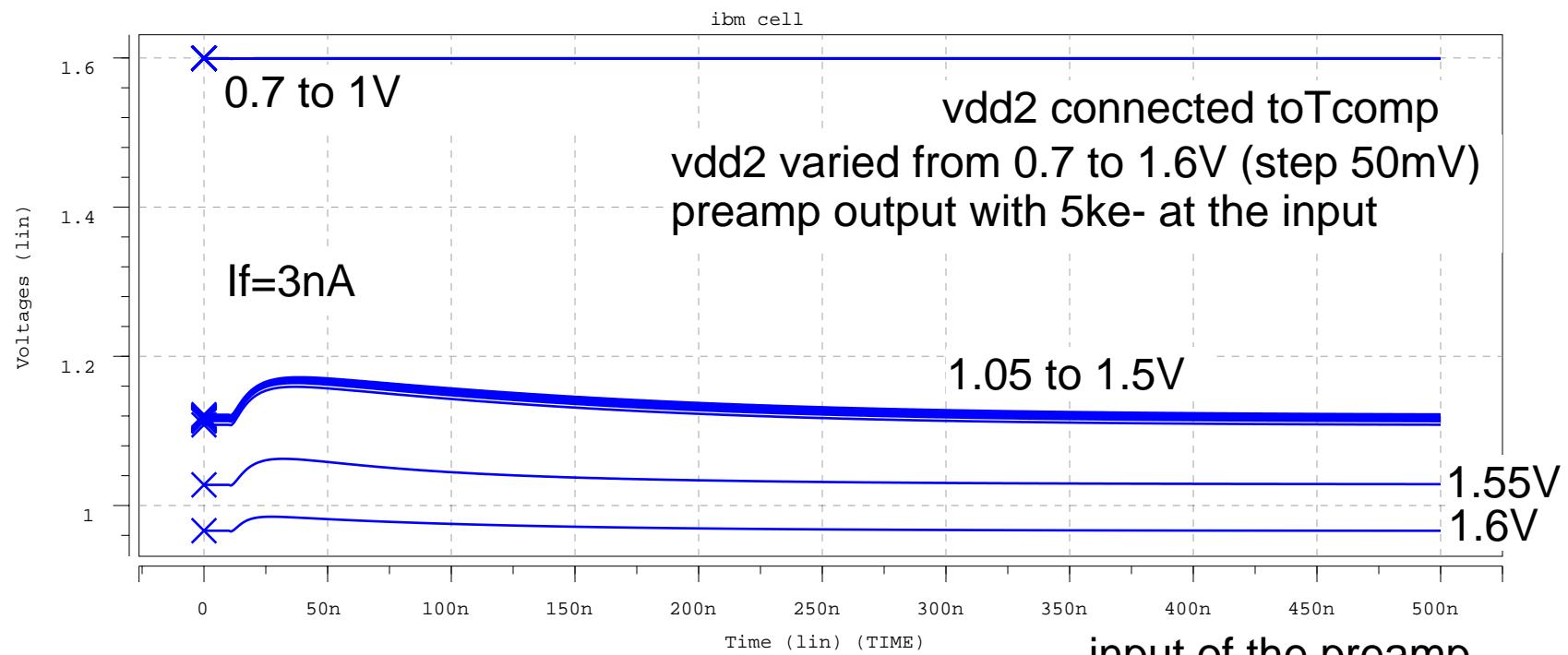


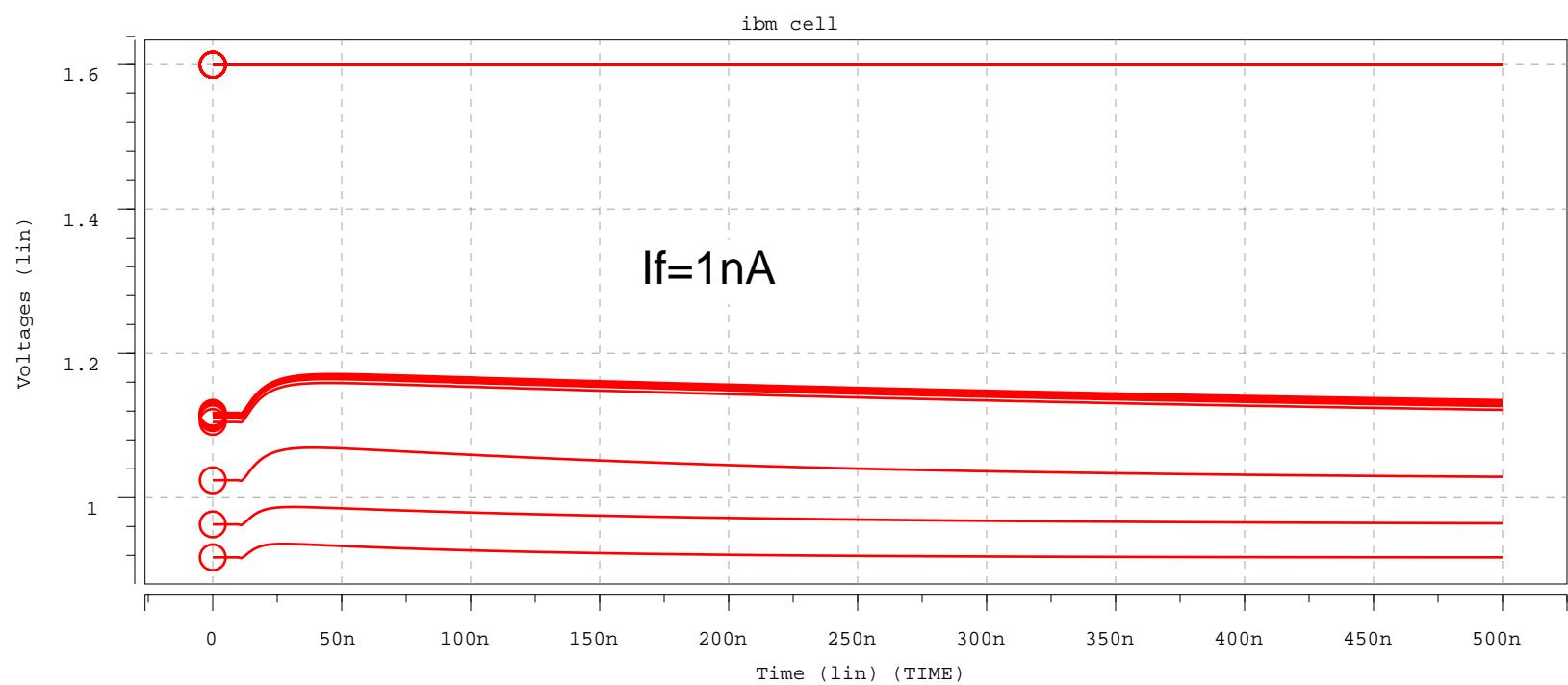
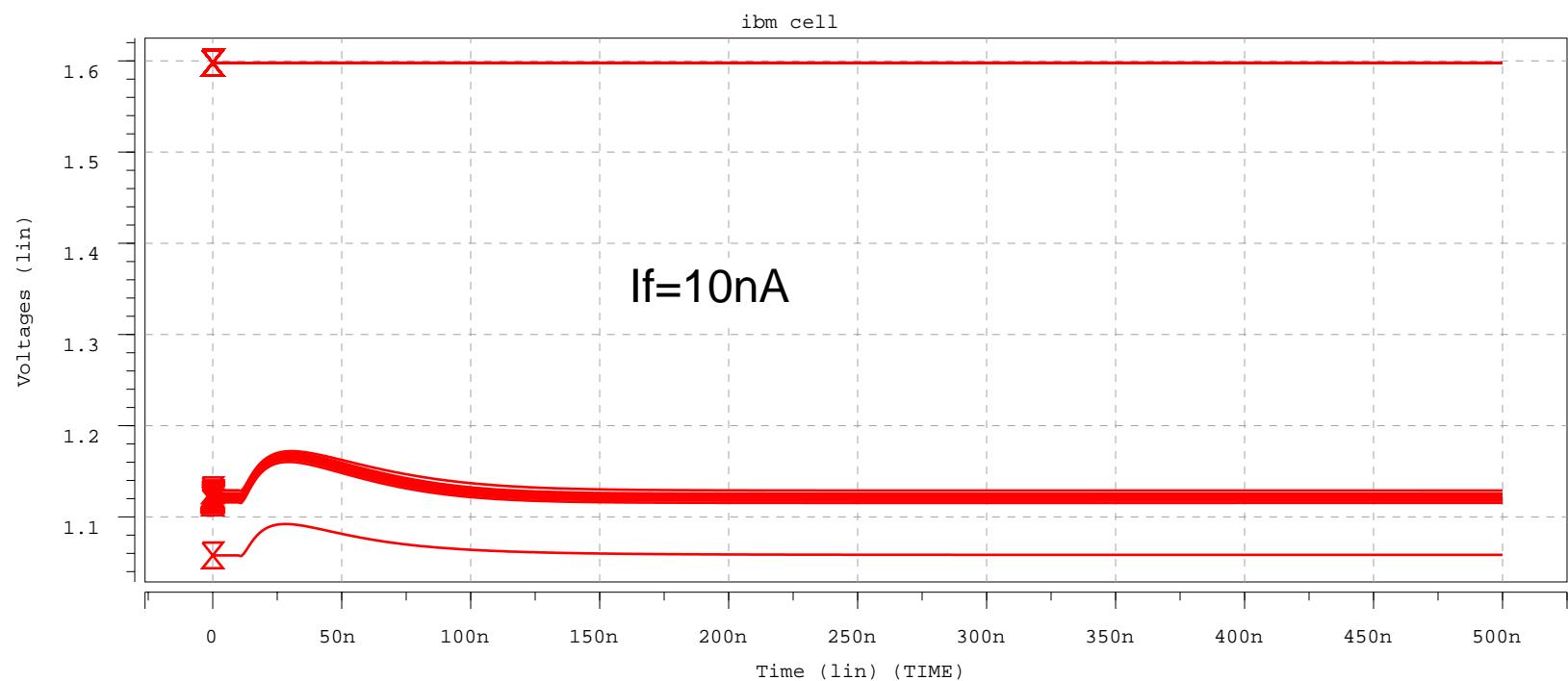


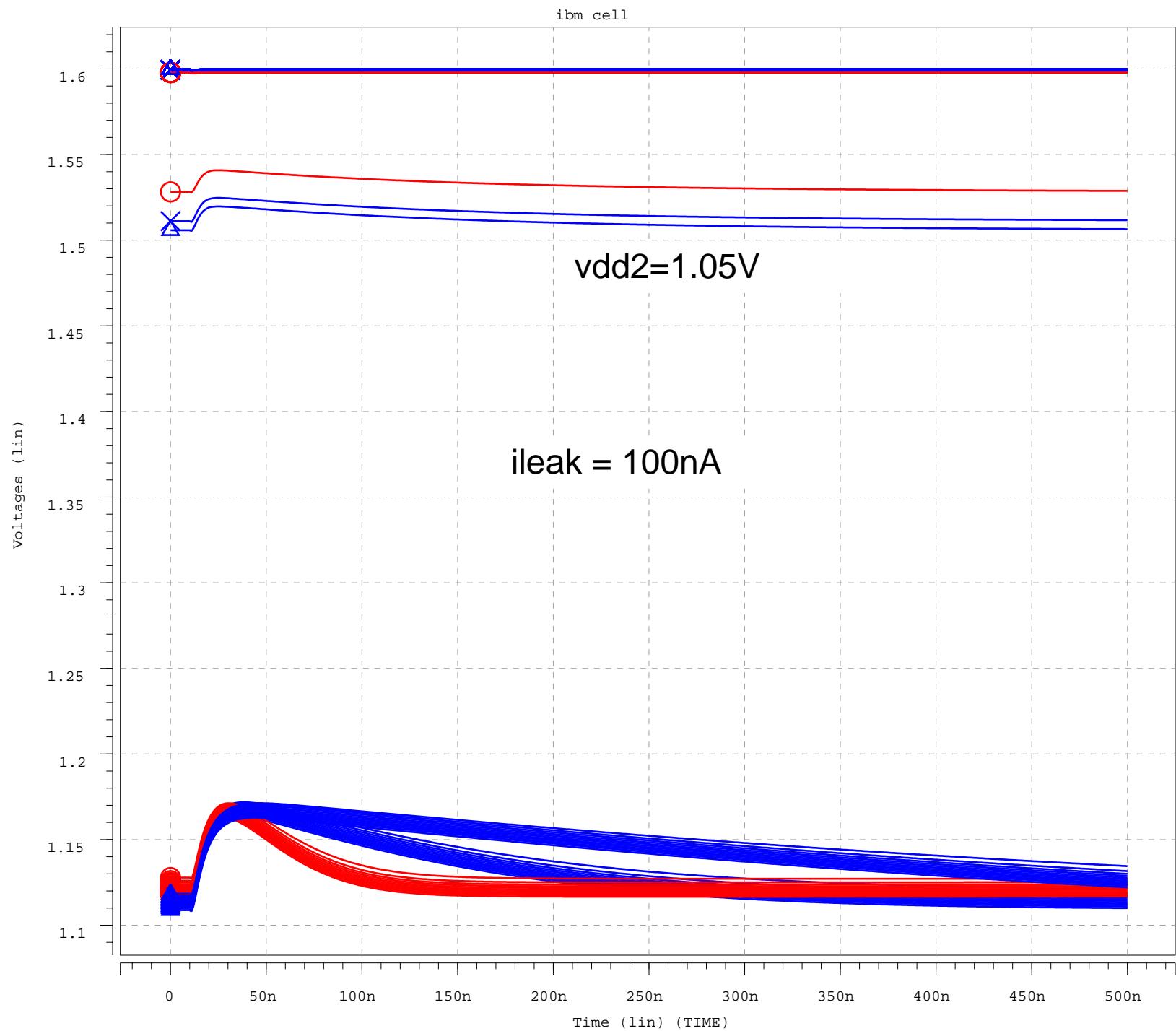


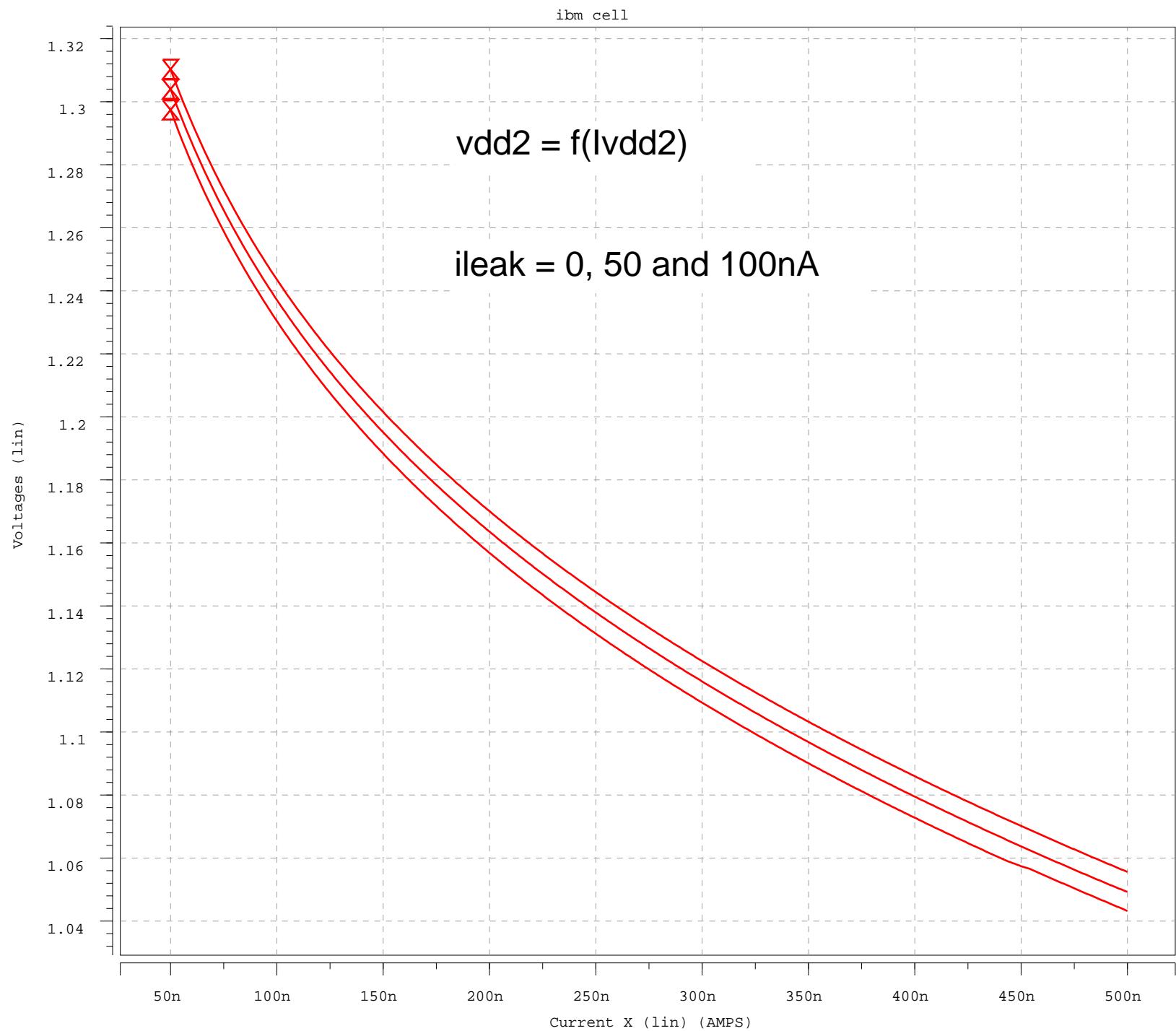


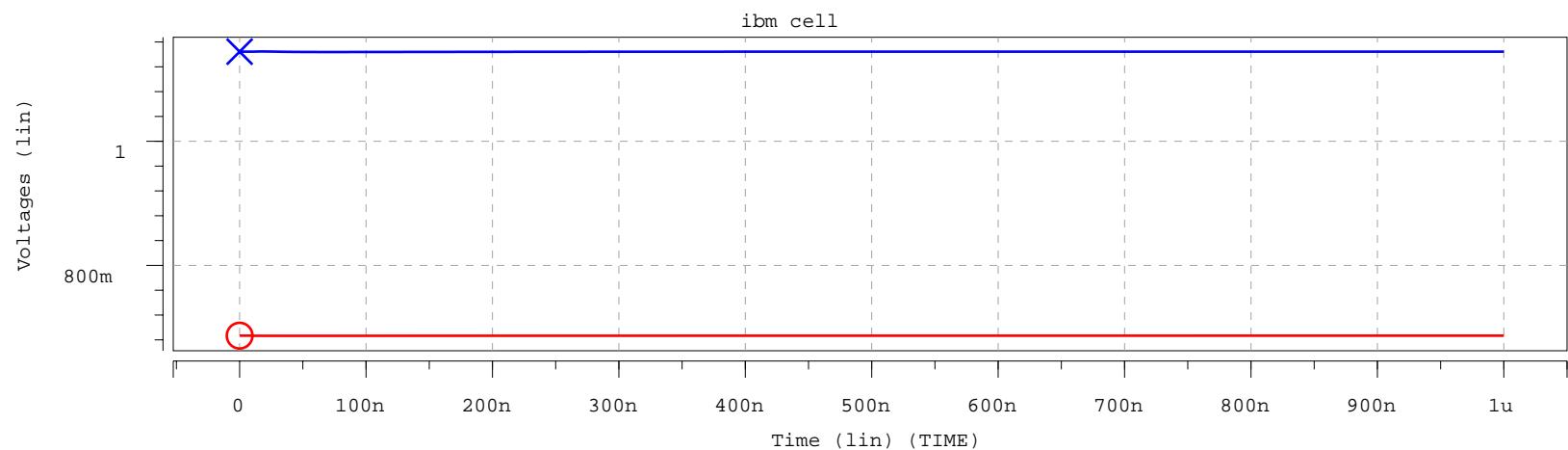
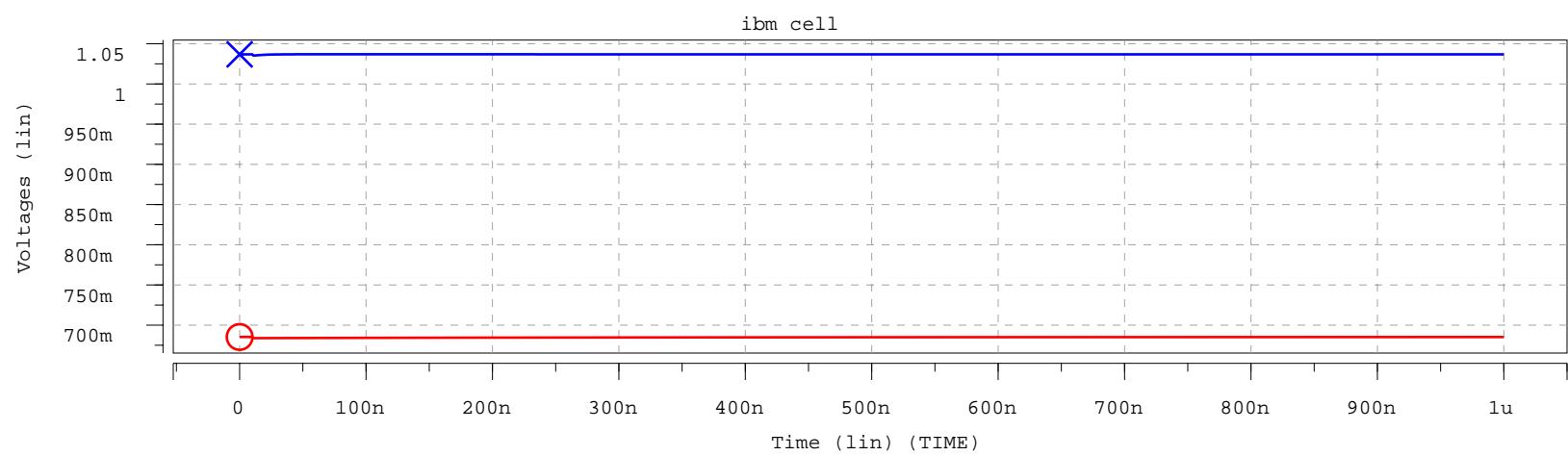
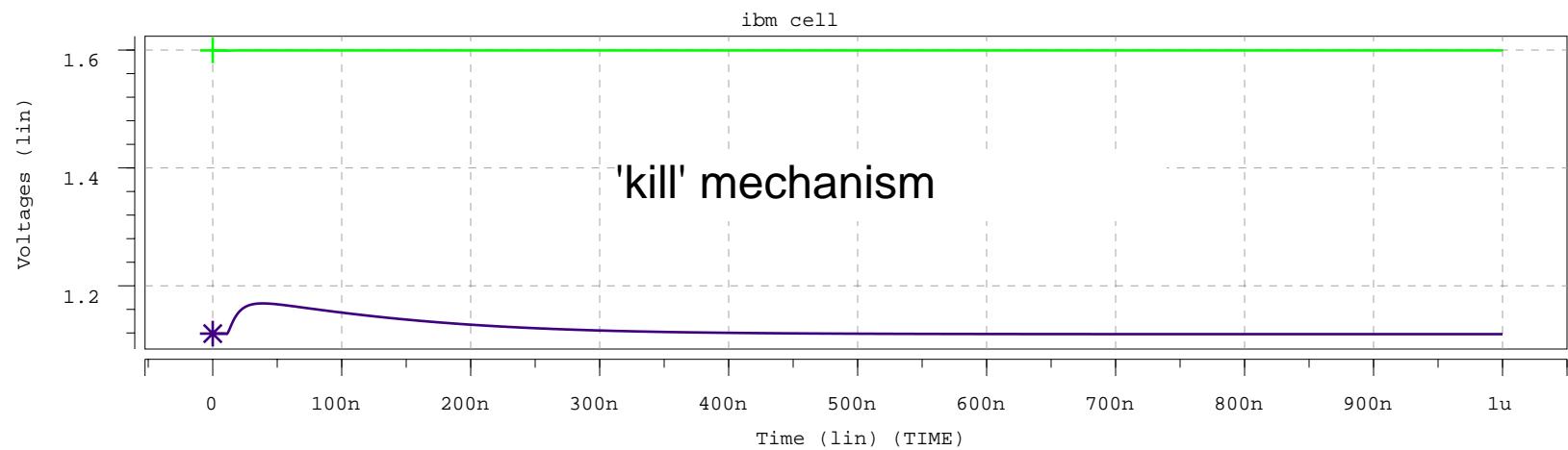
	Noise after preamp					Noise before discriminator			
Sensor capacitance (fF)	RMS noise at the output (mV)	Tin Contribution (mV)	Peaking value of 2000e- (mV)	ENC (e-)	RMS noise at the output (mV)	Tin Contribution (mV)	Peaking value of 2000e- (mV)	ENC (e-)	
0	2.25	0.93	23.6	190	15.9	3.16	138	230	
100	2.8	1.41	21.8	256	18.07	6.07	138	261	
200	3.27	1.76	21.1	309	20.51	8.51	137	299	
300	3.69	2.06	20.6	358	22.96	10.64	135	340	
400	4.06	2.32	20.1	403	25.34	12.52	133	380	
500	4.41	2.55	19.7	447	27.64	14.23	131	421	
600	4.73	2.76	19.3	490	29.84	15.8	129	462	

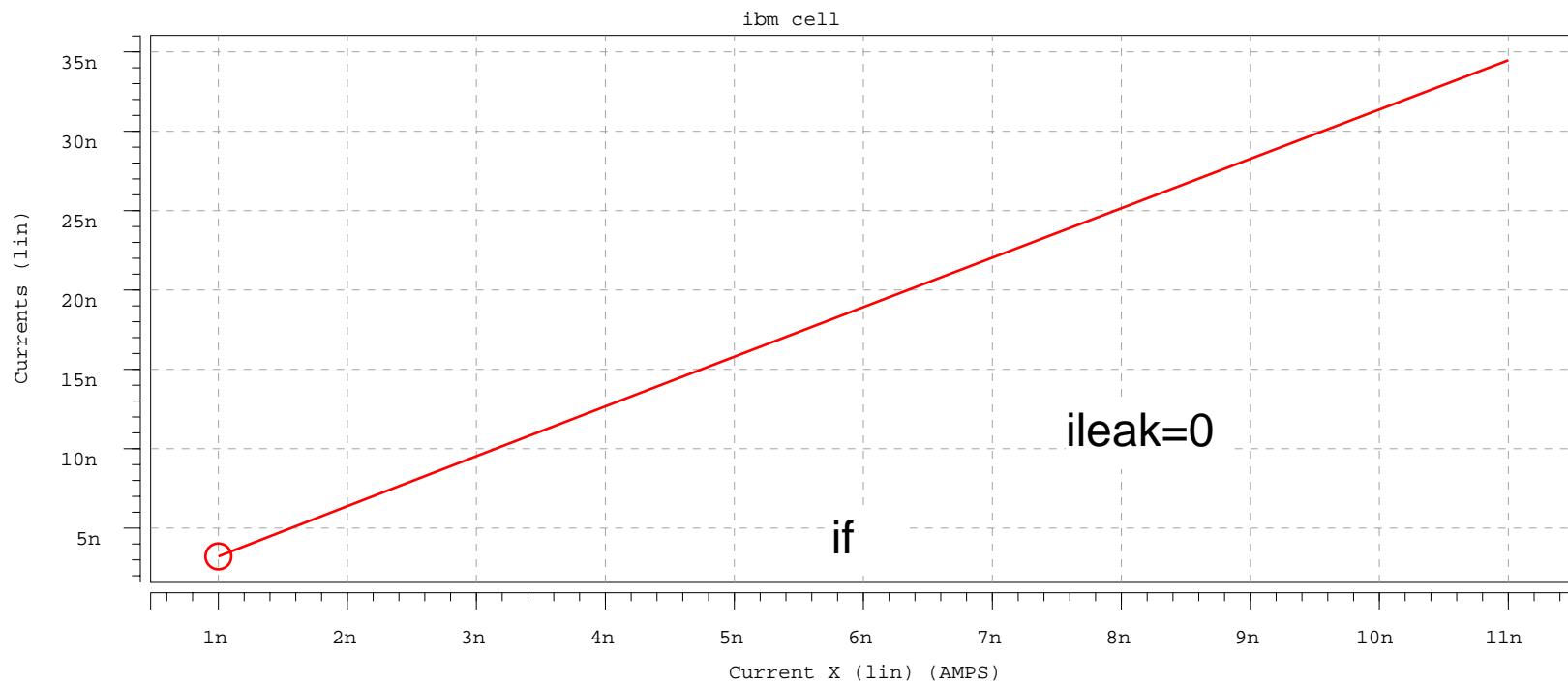
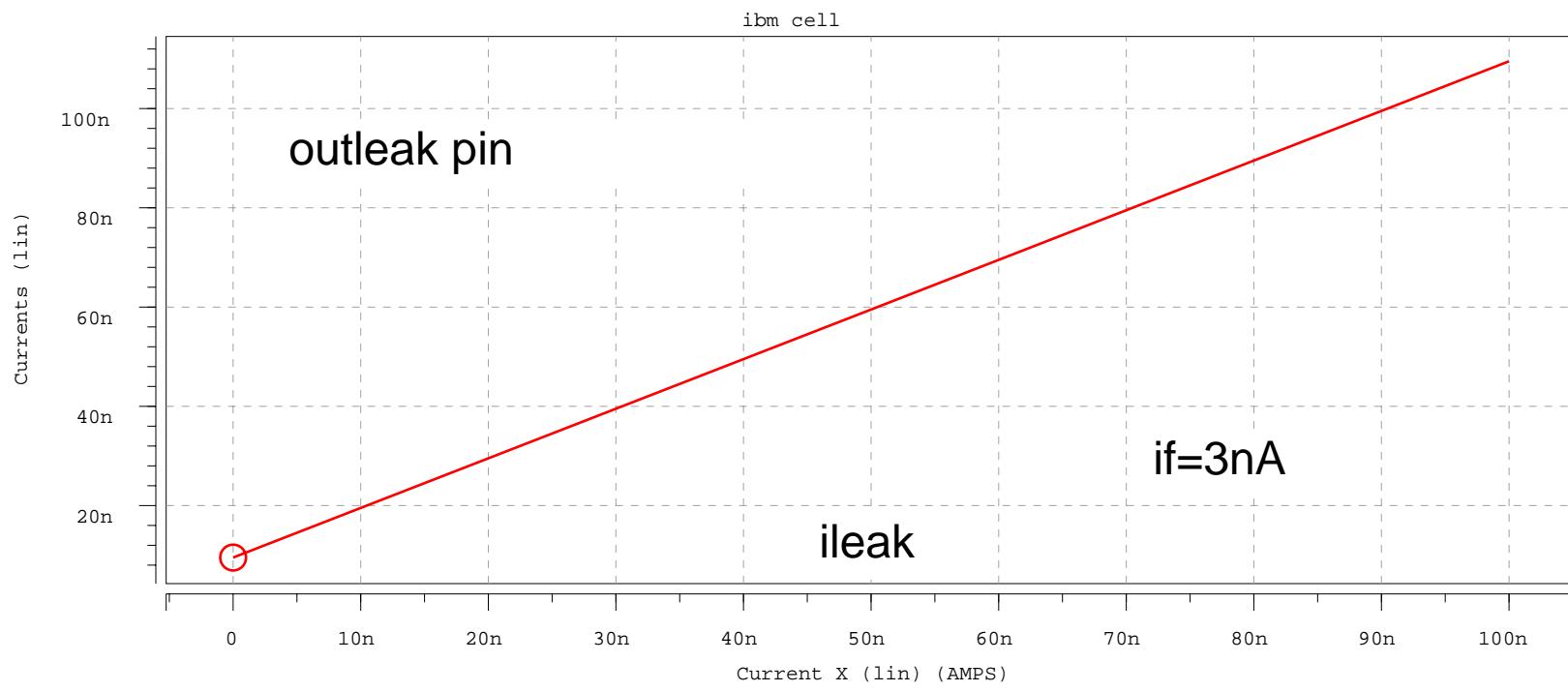


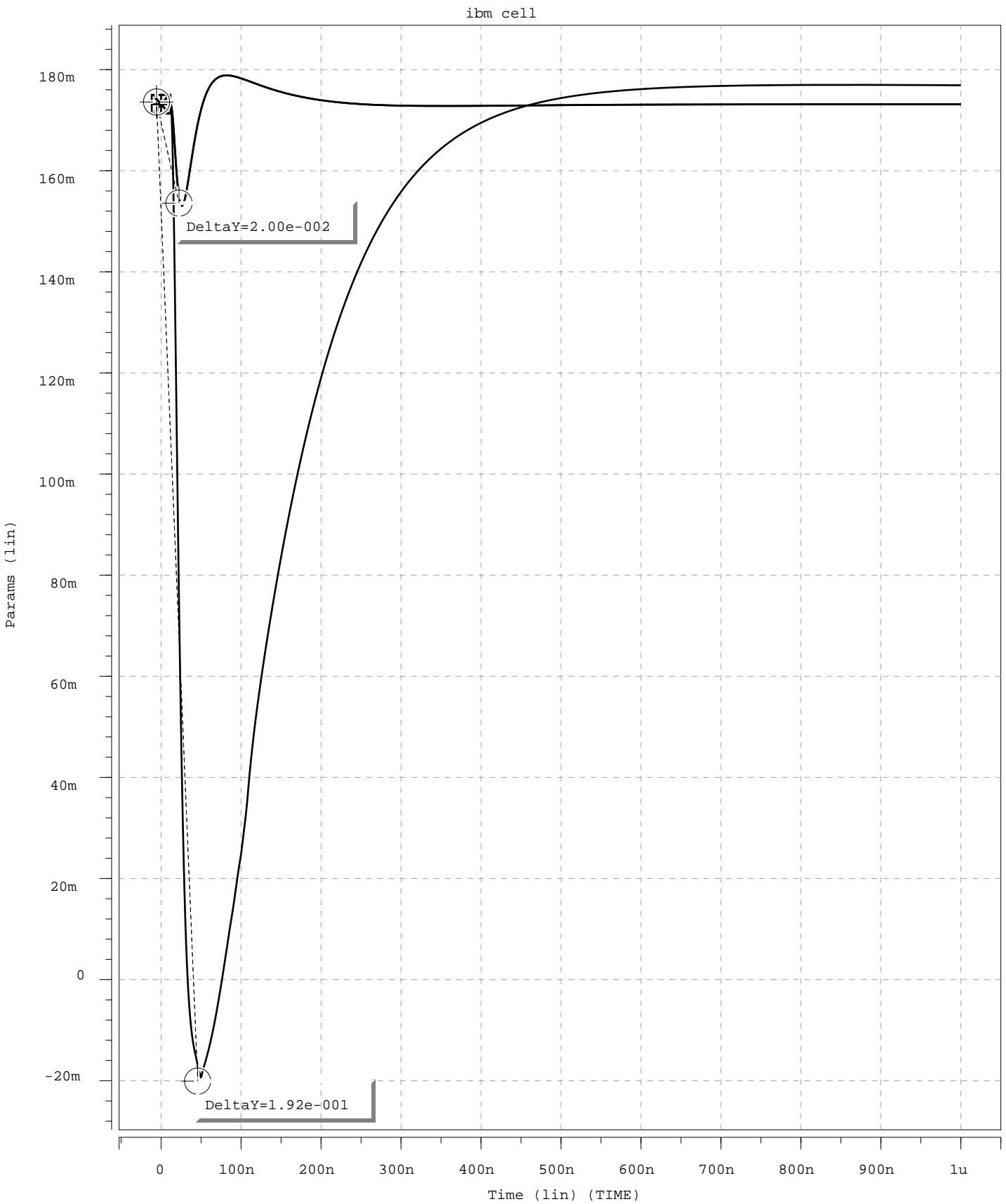






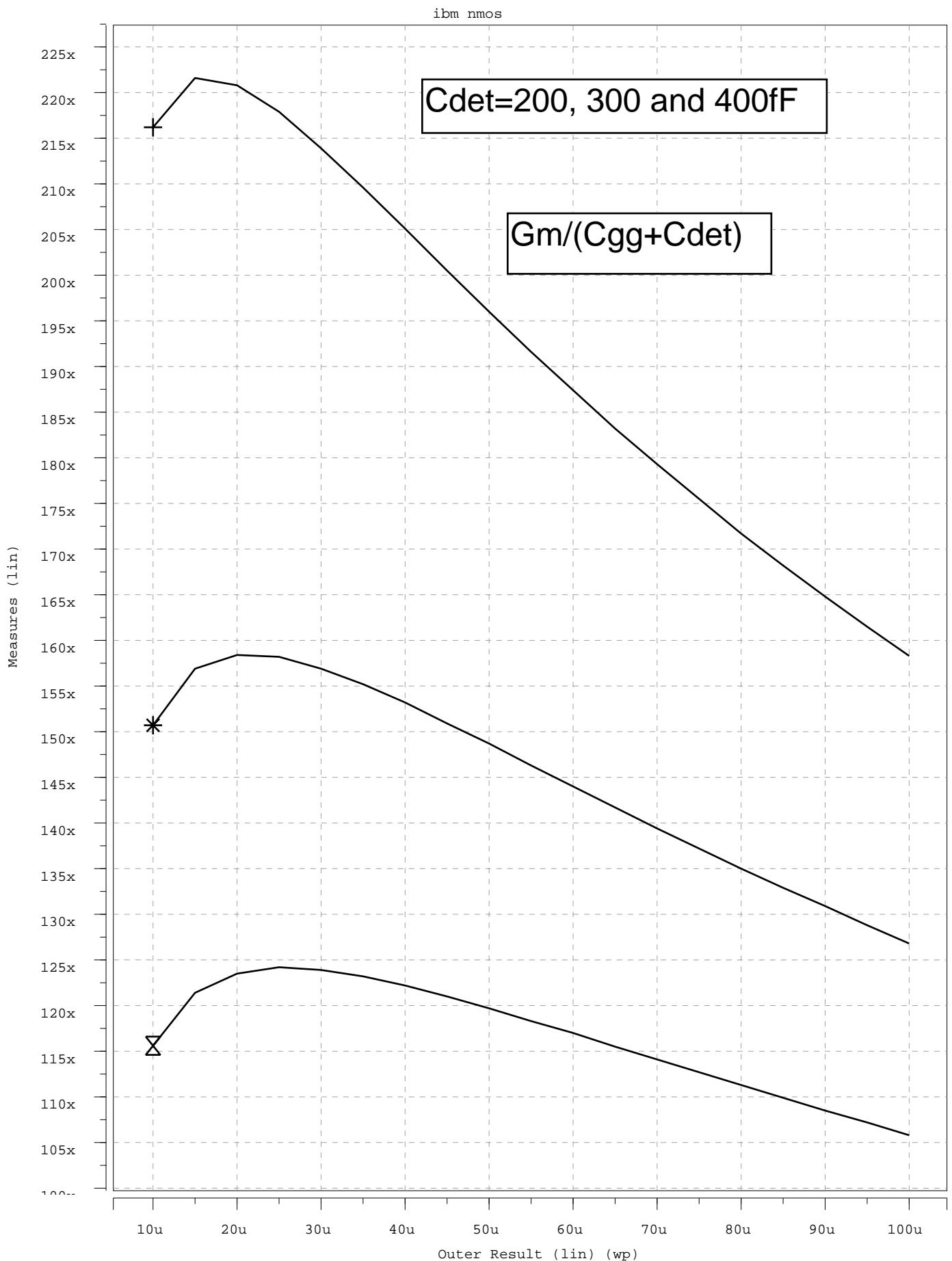




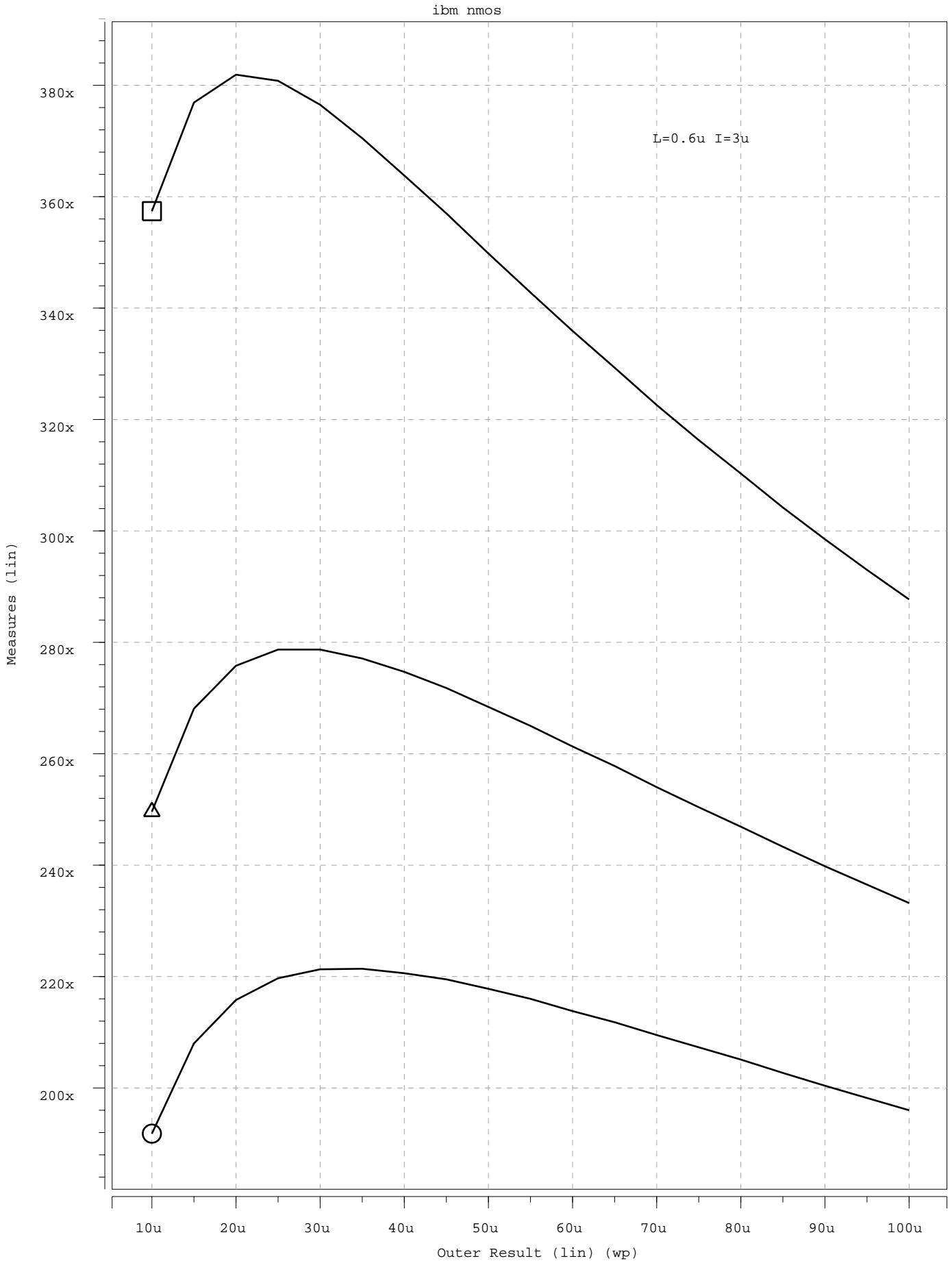


Choices

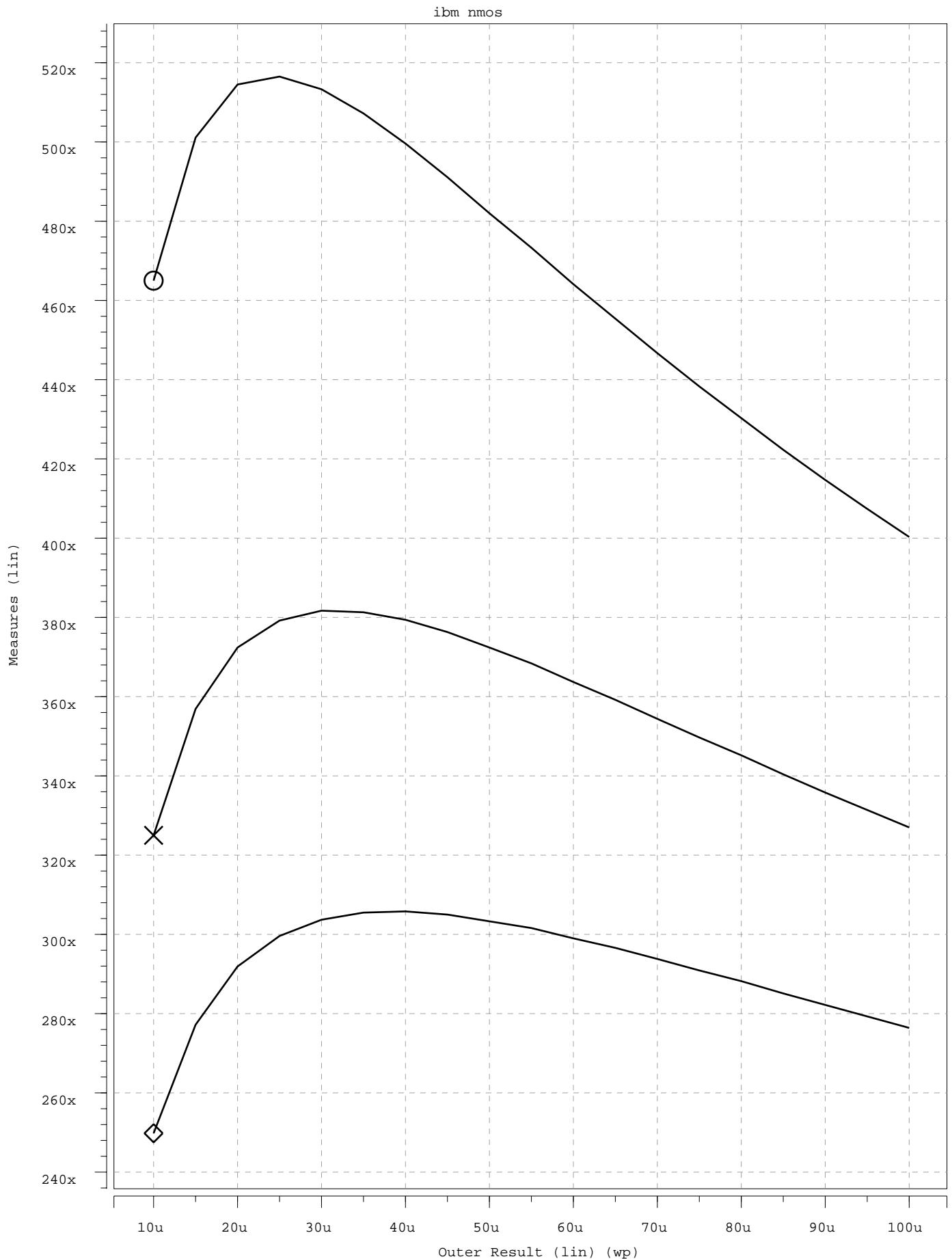
ID=3u



ID=6u



ID=9u



$C_{gg} + C_{det}$ (fF)	Voltage Gain (V)	BW (MHz)	φ_{margin} ($^{\circ}$)	t_{rise} (ns)	GBW (MHz)
75 + 0	5.36 (14.6dB)	115	42.8	3	614
75 + 100	12.5 (22dB)	57	65	6	714
75 + 200	19.6 (25.9dB)	37.5	73.9	9.3	736
75 + 300	26.8 (28.5dB)	27.7	78.5	12.6	742
75 + 400	33.9 (30.6dB)	22	81.2	16	744
75 + 500	41.1 (32.3dB)	18.1	83.1	19.3	744

pole/zero analysis

Without Cf

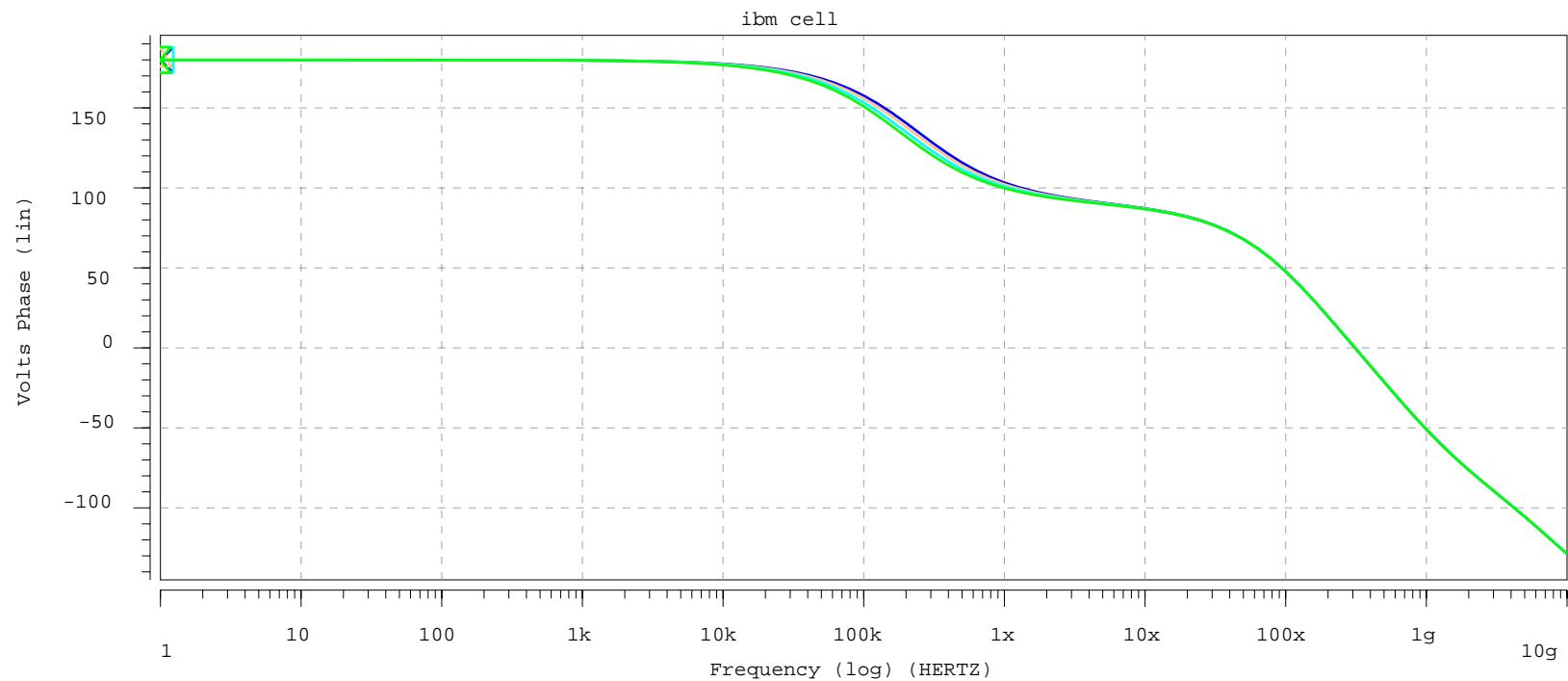
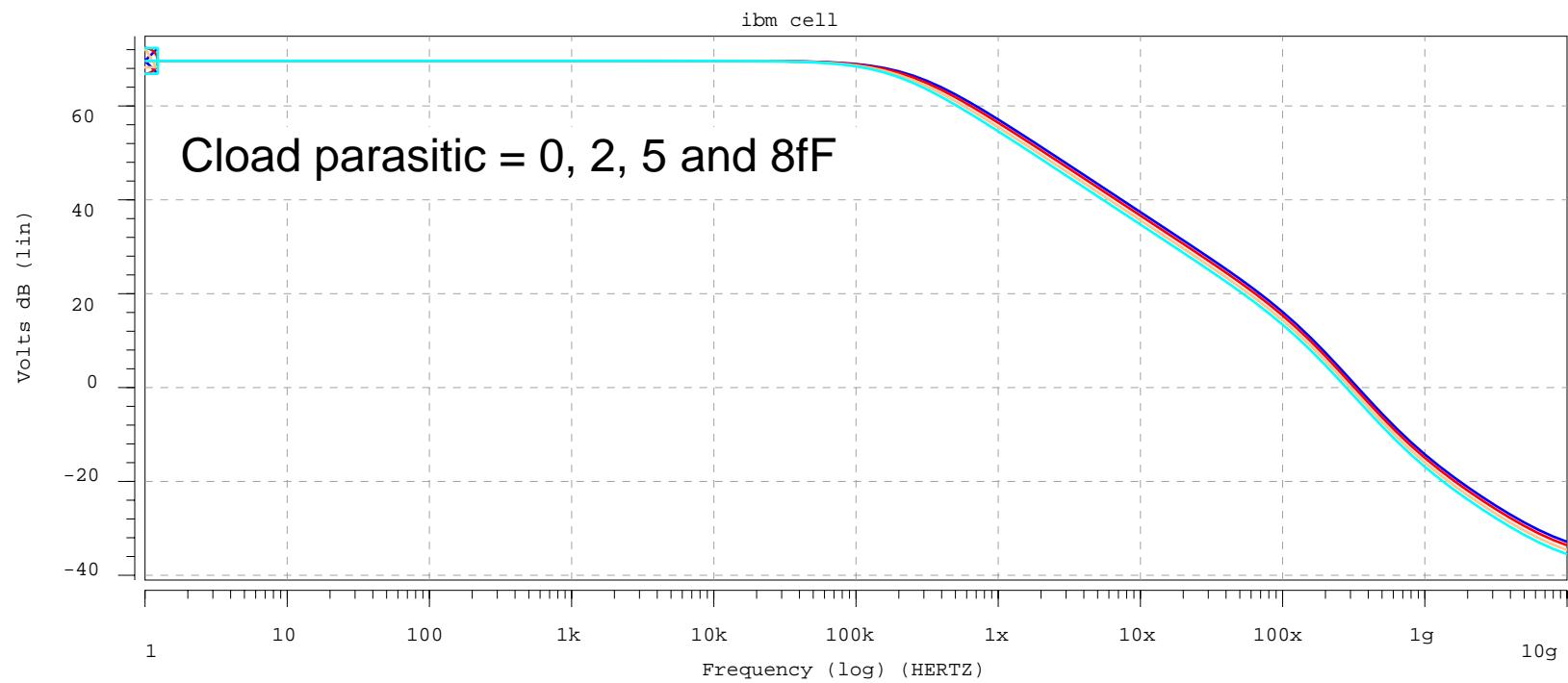
poles (rad/sec)		poles (hertz)	
real	imag	real	imag
0.	0.	0.	0.
-4.0058x	0.	-637.5472k	0.
-35.7425x	0.	-5.6886x	0.
-205.8966x	0.	-32.7695x	0.
-283.2085x	0.	-45.0740x	0.
-337.4896x	0.	-53.7131x	0.
-767.9106x	0.	-122.2168x	0.
-963.5963x	0.	-153.3611x	0.
-975.2796x	0.	-155.2206x	0.

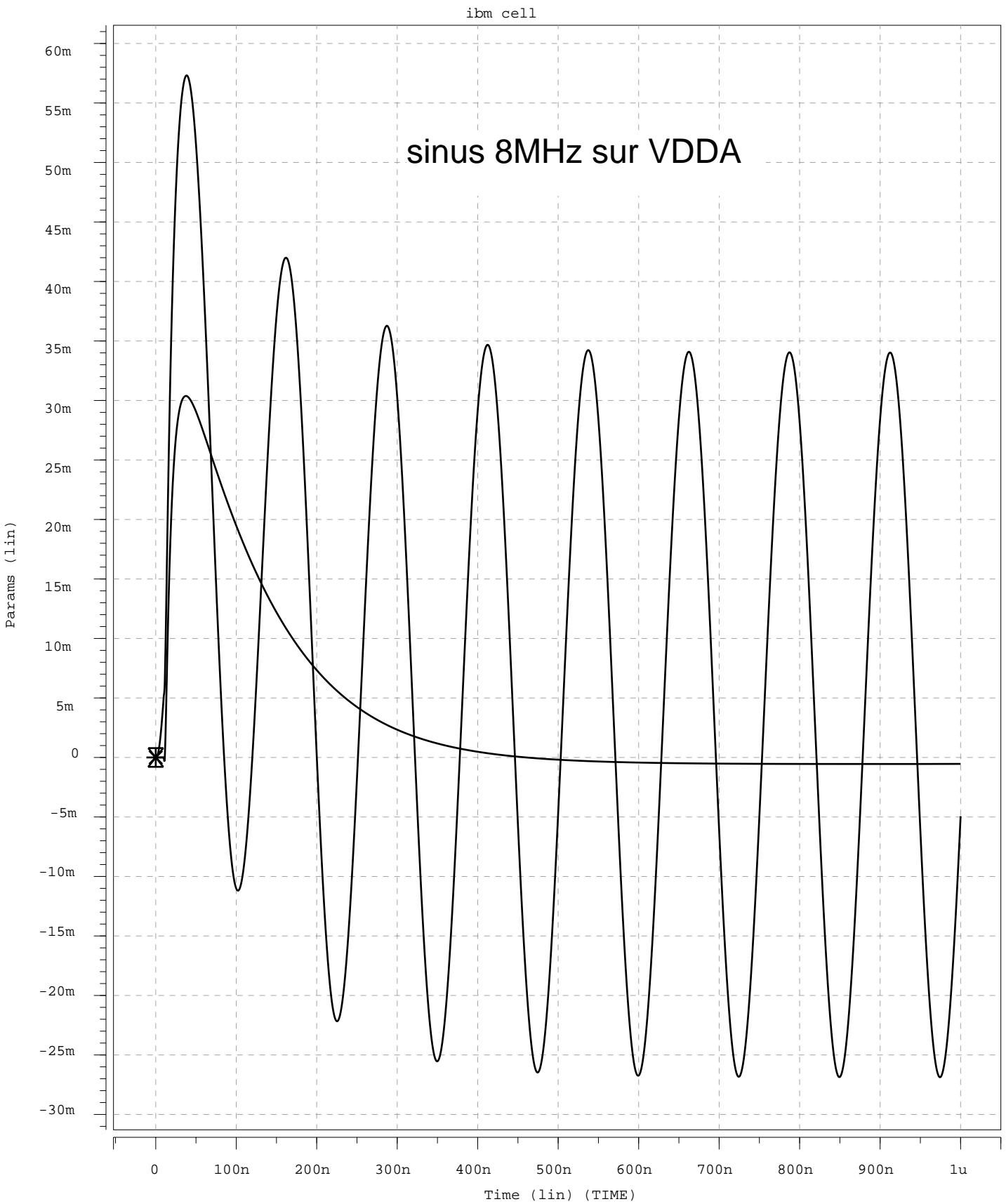
With Cf = 14fF

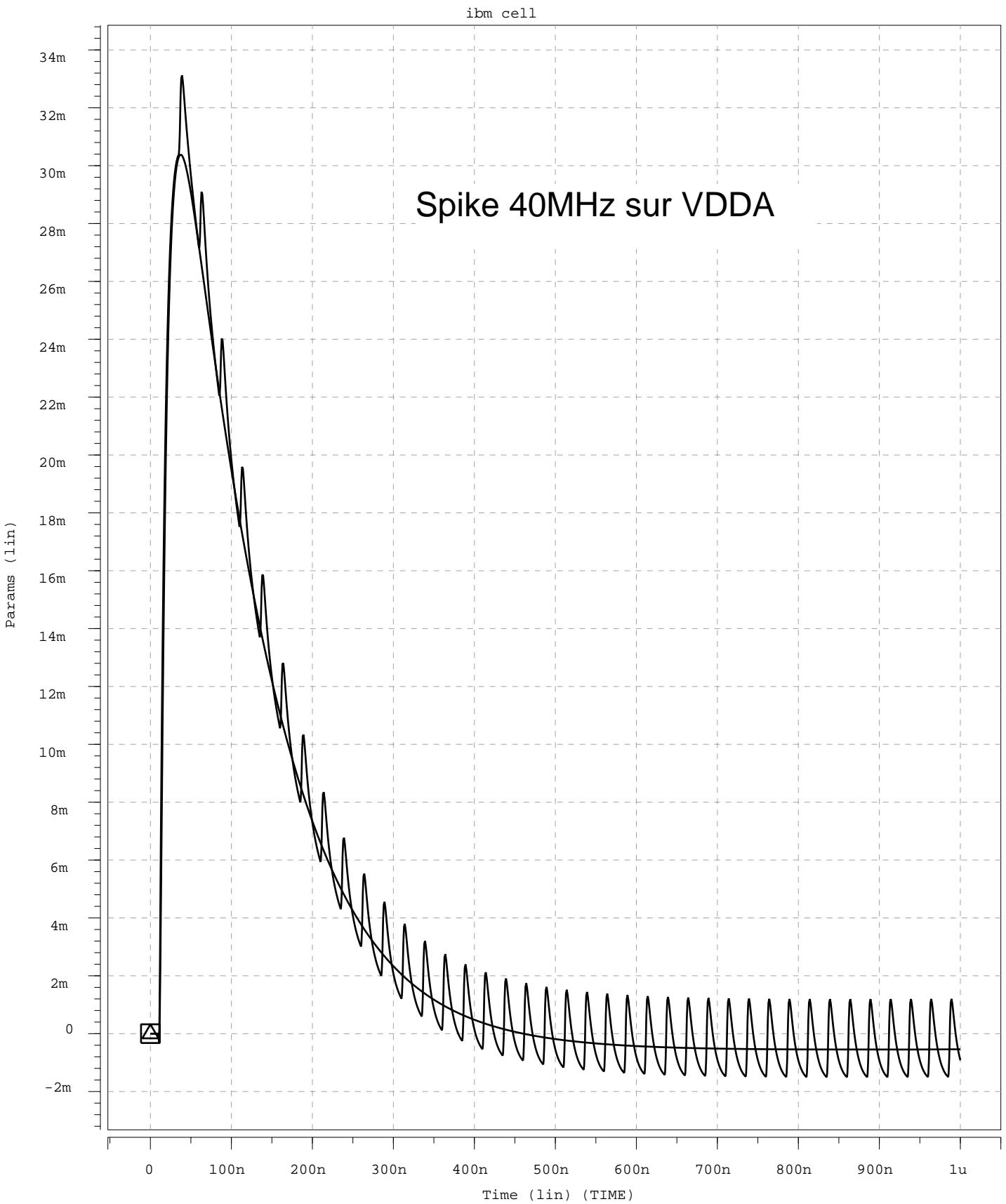
poles (rad/sec)		poles (hertz)	
real	imag	real	imag
0.	0.	0.	0.
-1.4740x	0.	-234.6018k	0.
-35.7798x	0.	-5.6945x	0.
-193.0655x	0.	-30.7273x	0.
-275.5790x	0.	-43.8598x	0.
-347.7063x	0.	-55.3392x	0.
-769.8383x	0.	-122.5236x	0.
-945.6457x	0.	-150.5042x	0.
-980.3425x	0.	-156.0264x	0.
-1.4455g	0.	-230.0550x	0.

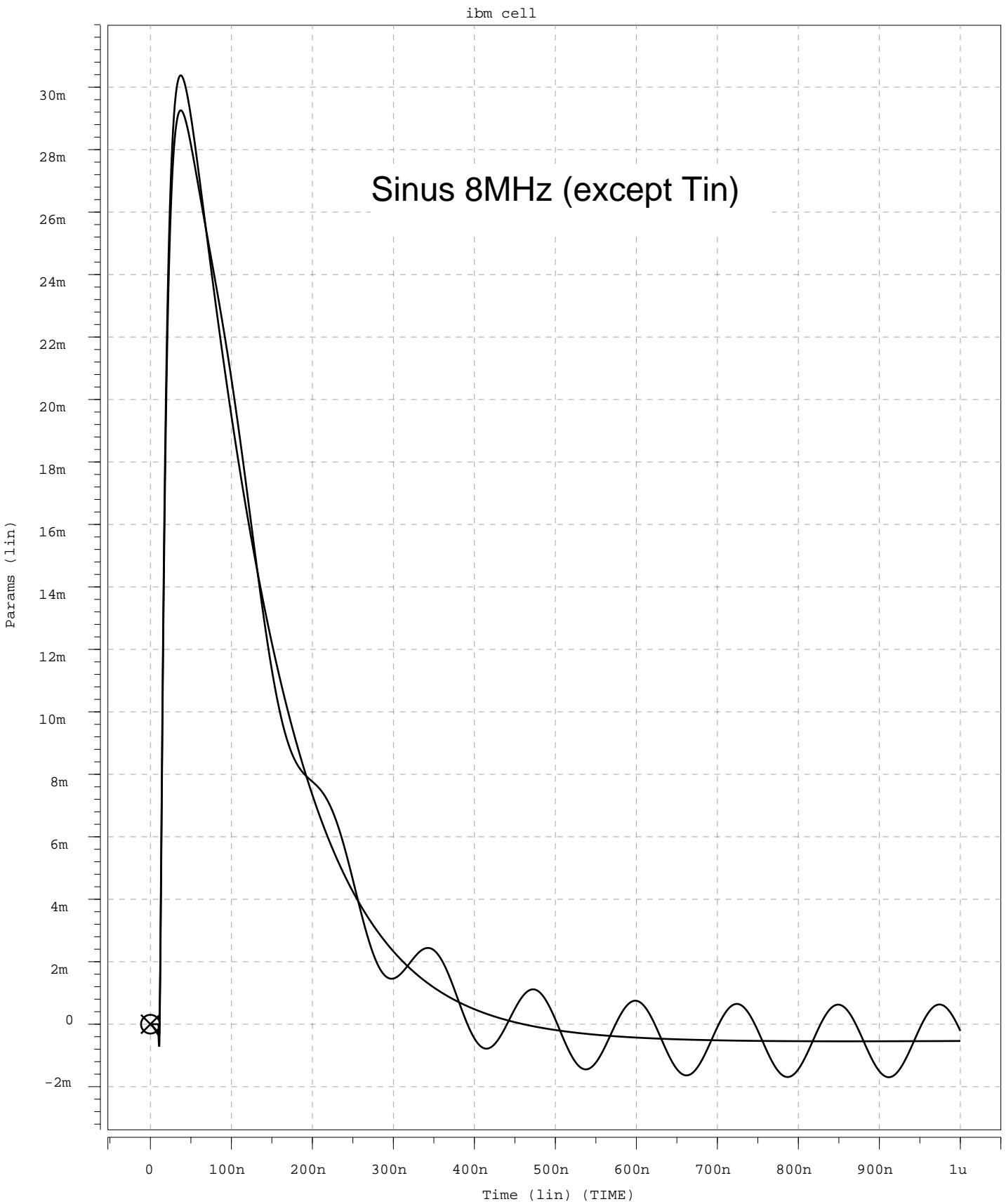
zeros (rad/sec)		zeros (hertz)	
real	imag	real	imag
0.	0.	0.	0.
-35.7996x	0.	-5.6977x	0.
-186.2815x	0.	-29.6476x	0.
-273.0753x	0.	-43.4613x	0.
-351.7196x	0.	-55.9779x	0.
-770.8642x	0.	-122.6868x	0.
-981.2933x	0.	-156.1777x	0.

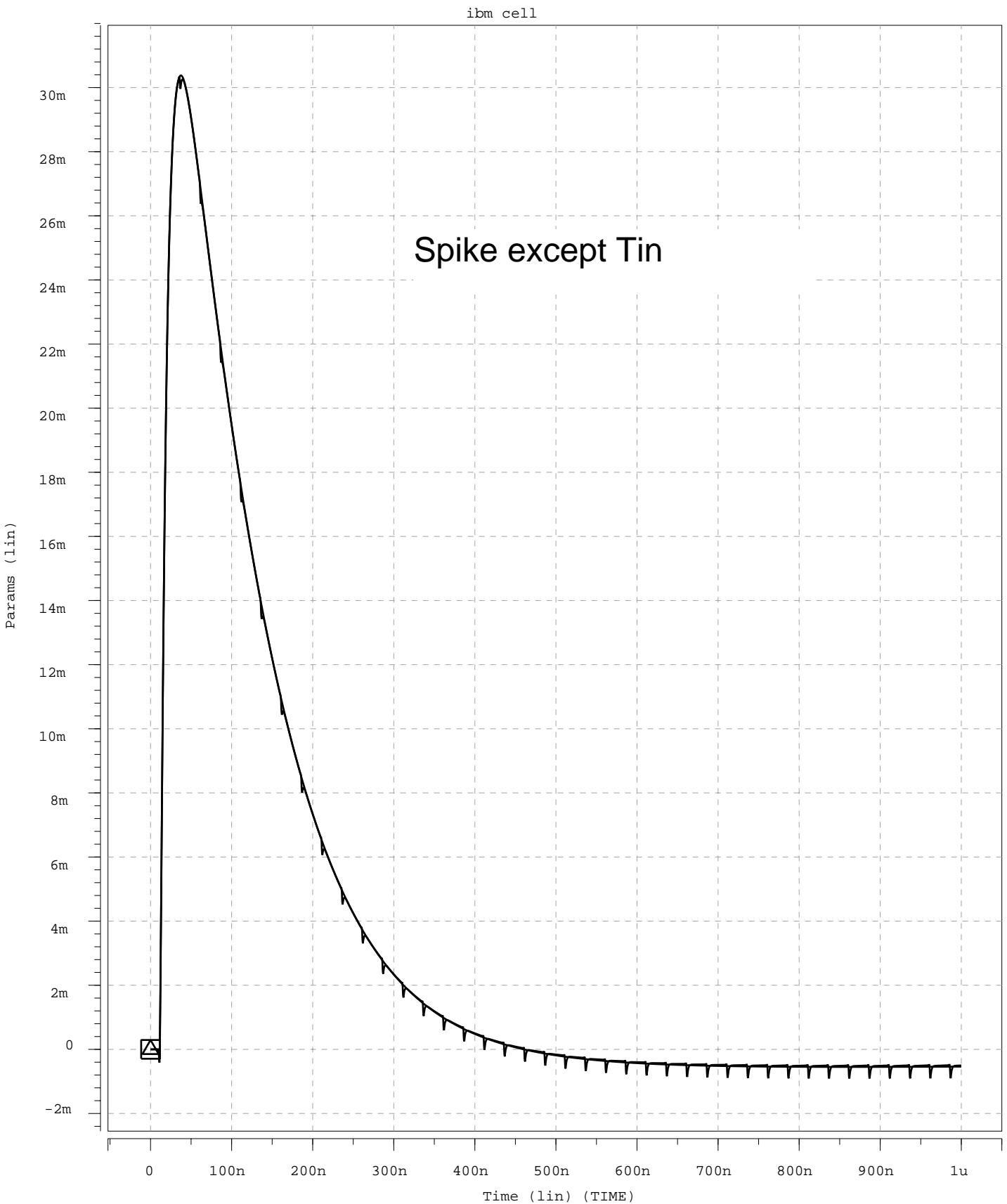
zeros (rad/sec)		zeros (hertz)	
real	imag	real	imag
0.	0.	0.	0.
-35.7996x	0.	-5.6977x	0.
-186.2815x	0.	-29.6476x	0.
-273.0753x	0.	-43.4613x	0.
-351.7196x	0.	-55.9779x	0.
-770.8642x	0.	-122.6868x	0.
-981.2933x	0.	-156.1777x	0.
-1.4679g	0.	-233.6277x	0.

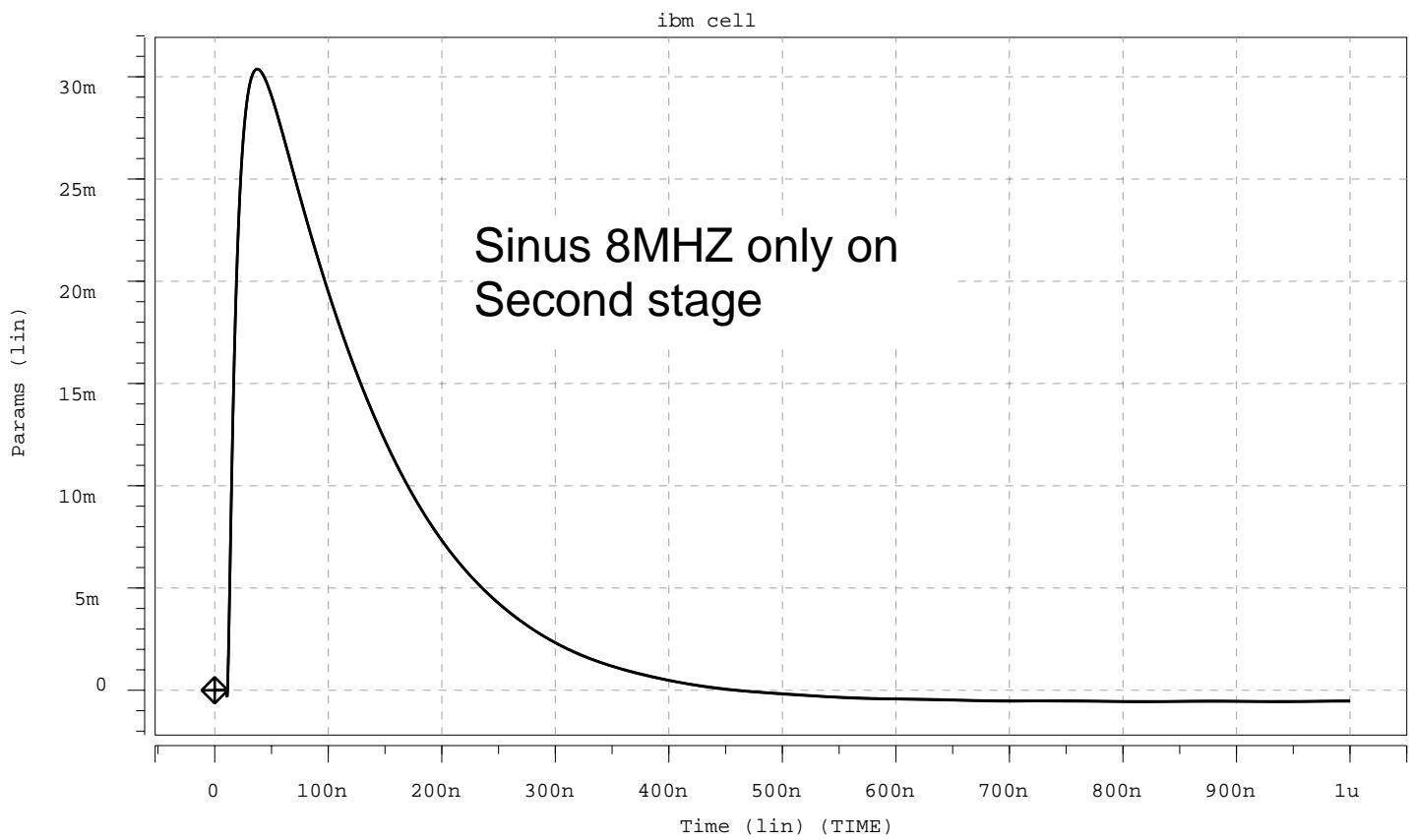


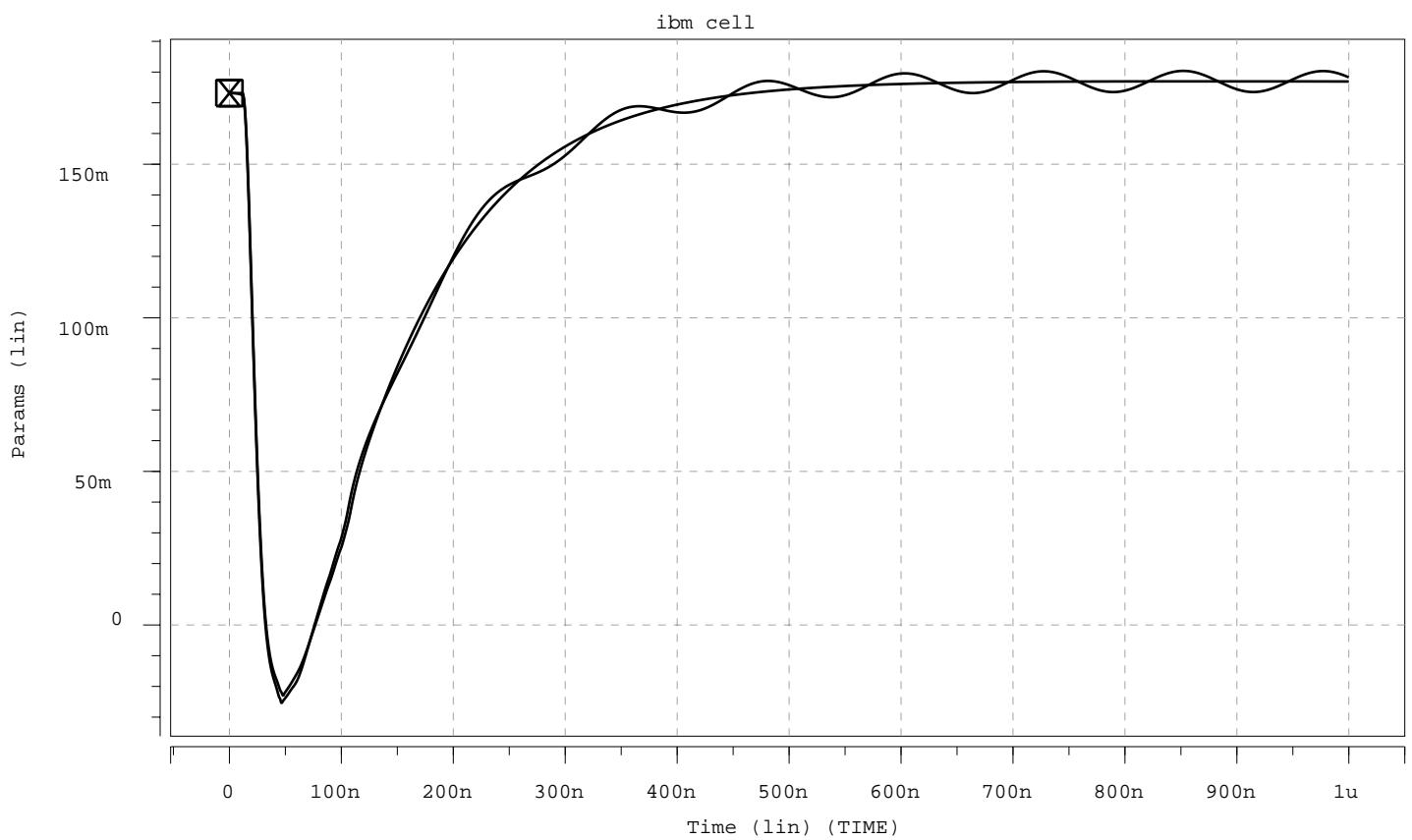
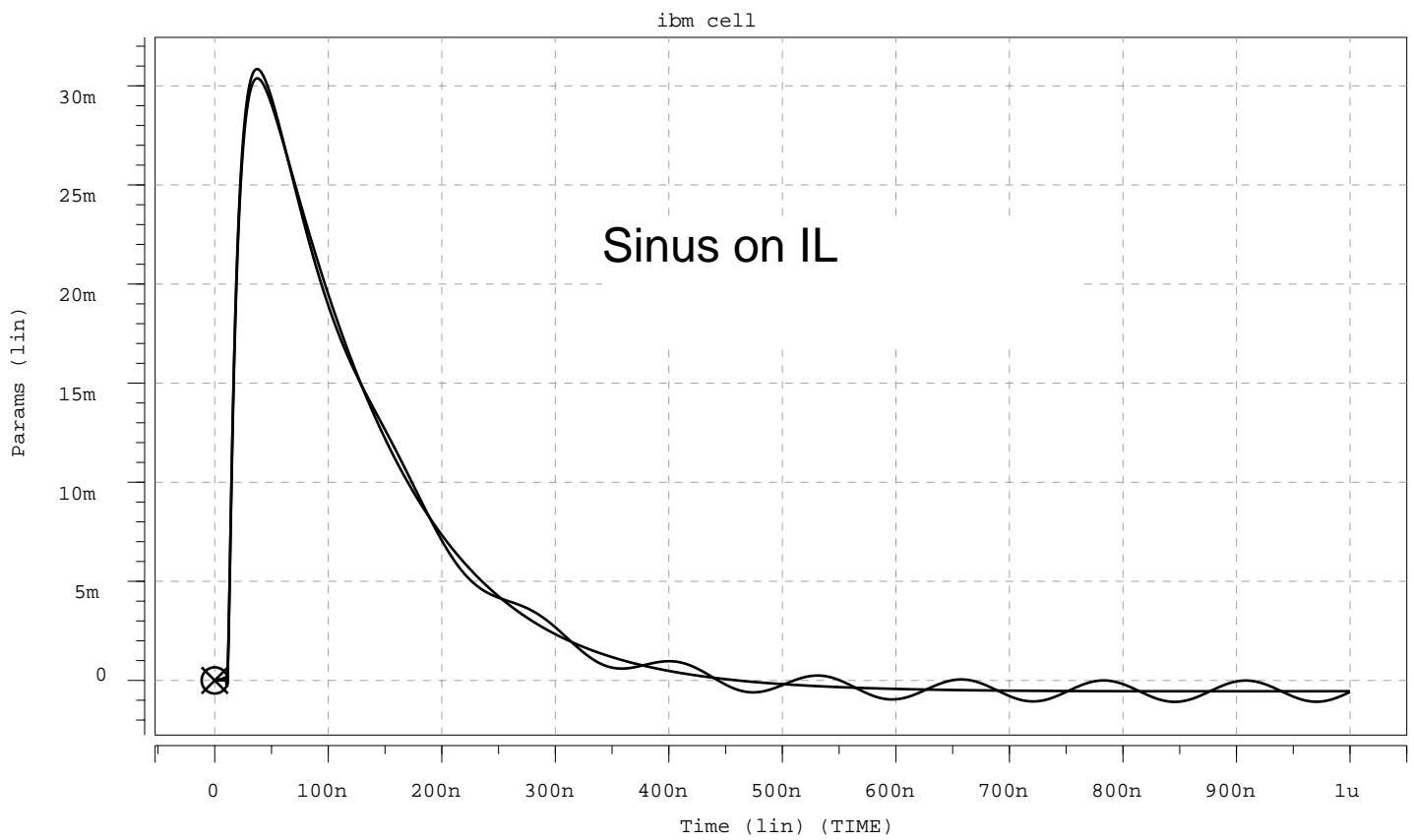


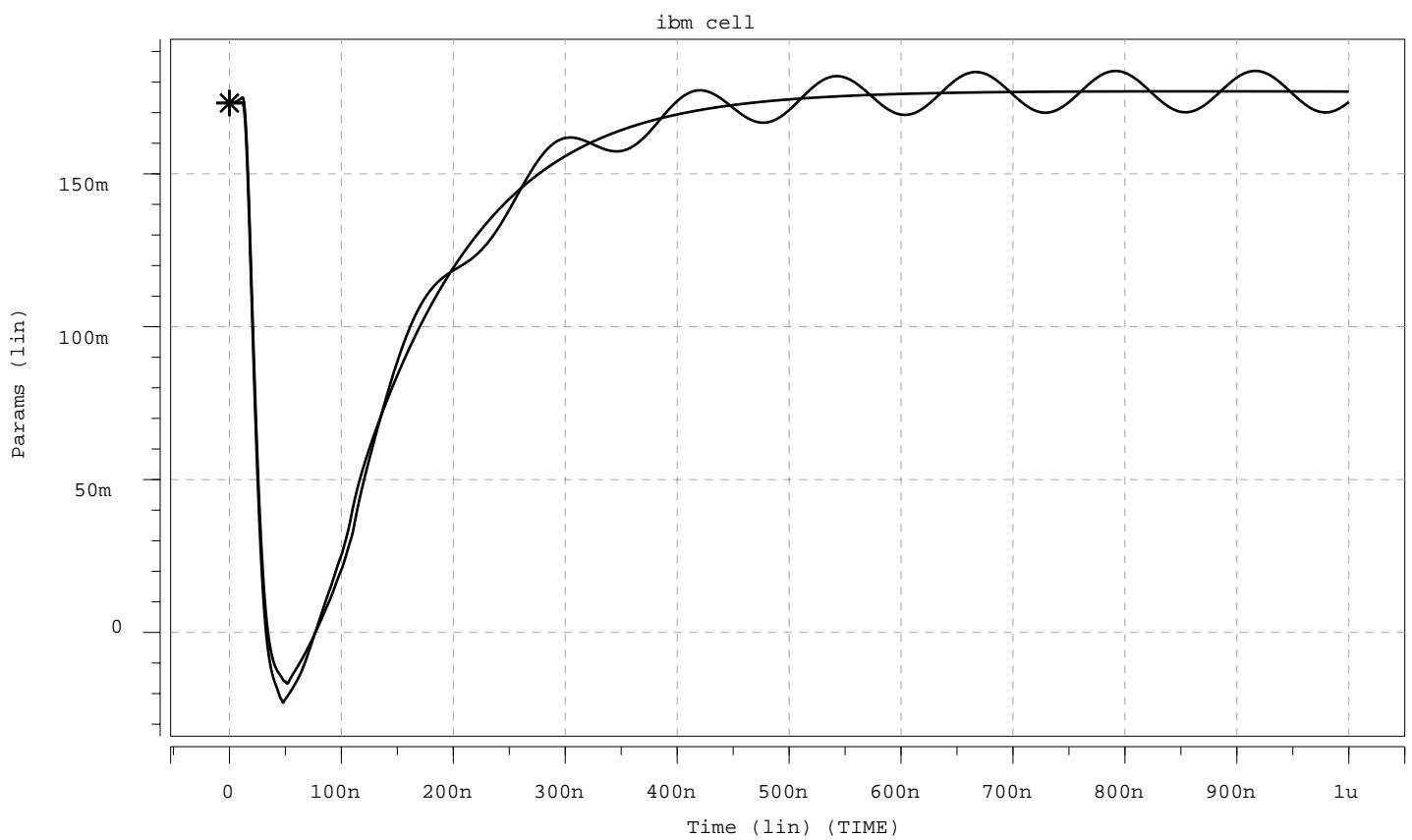
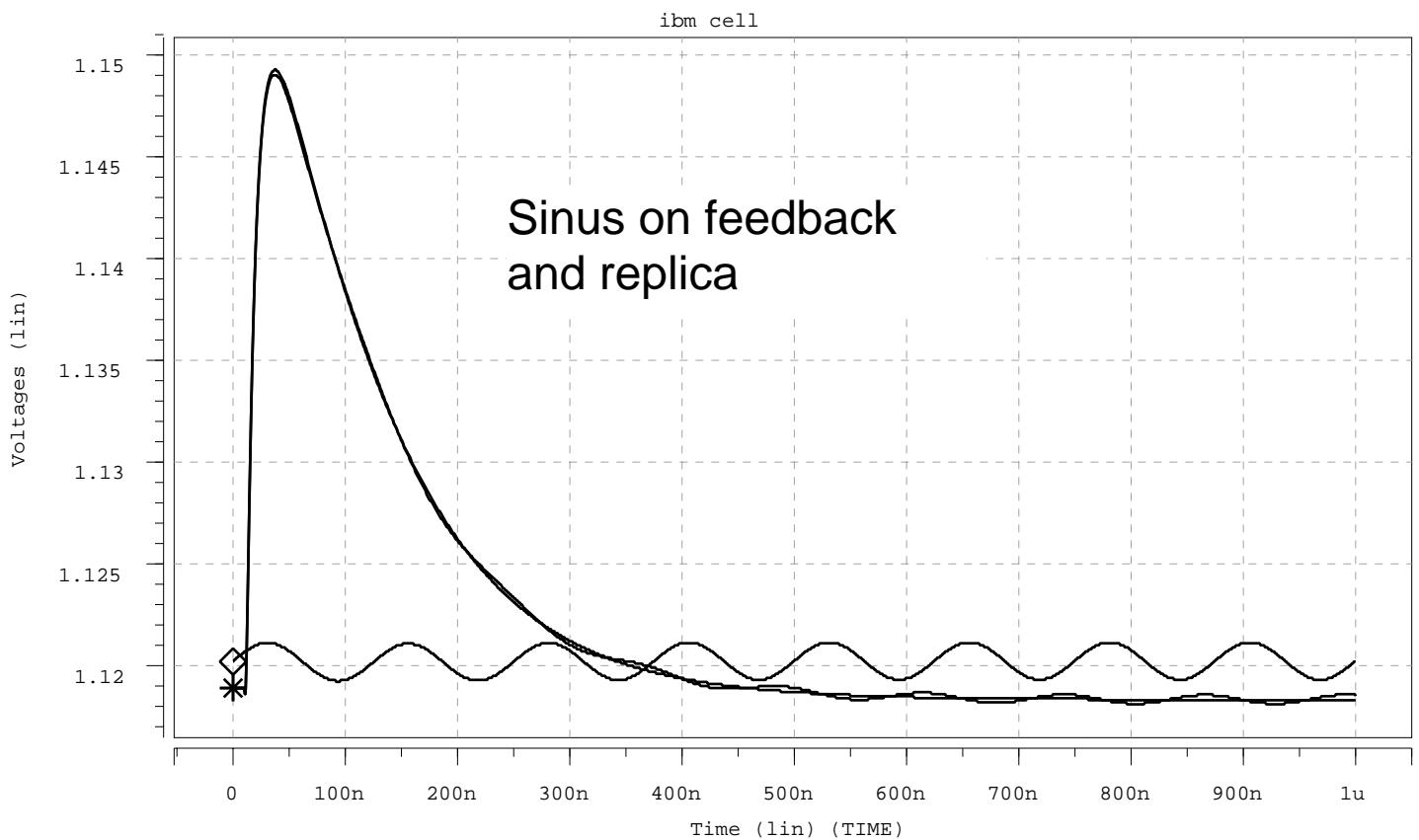


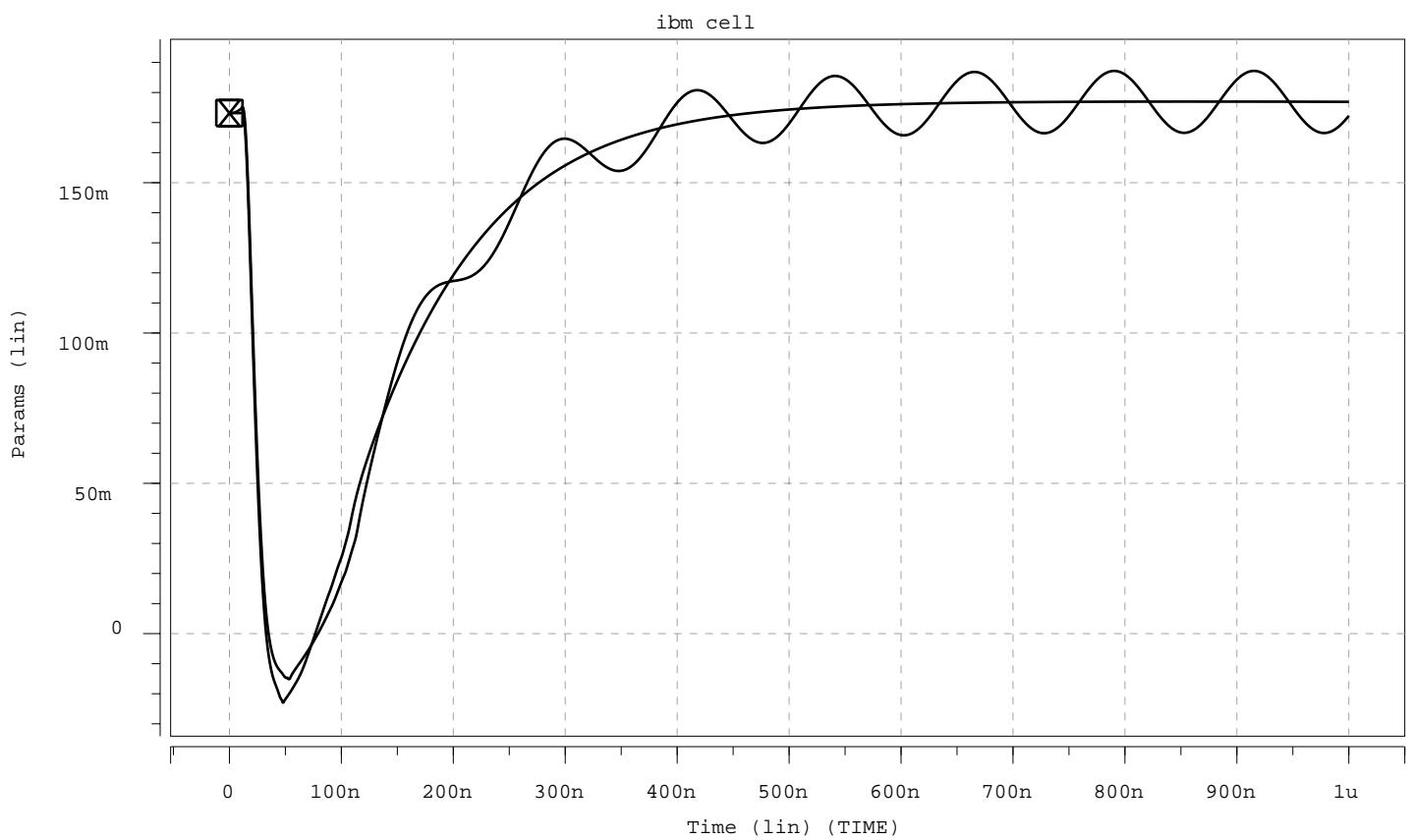
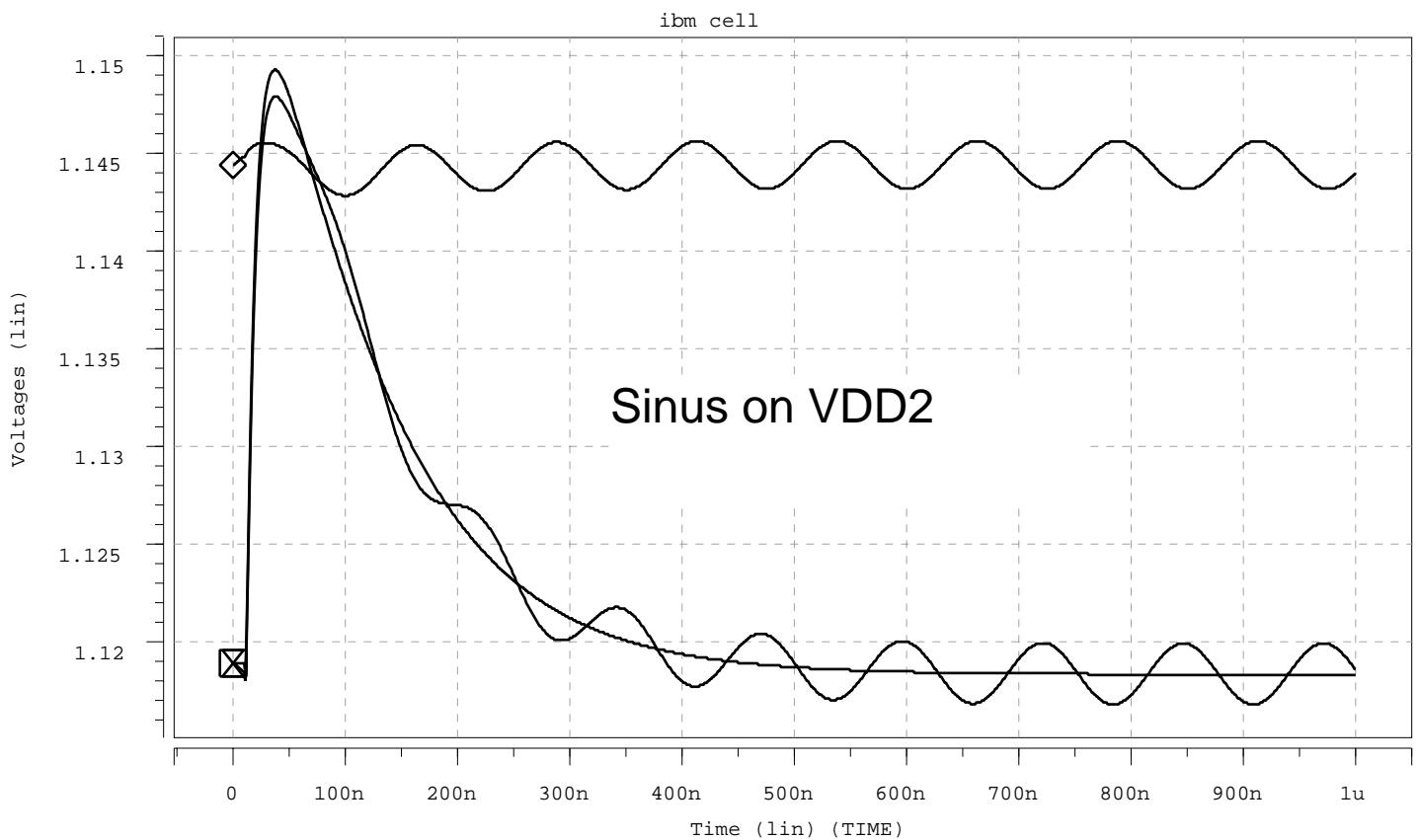


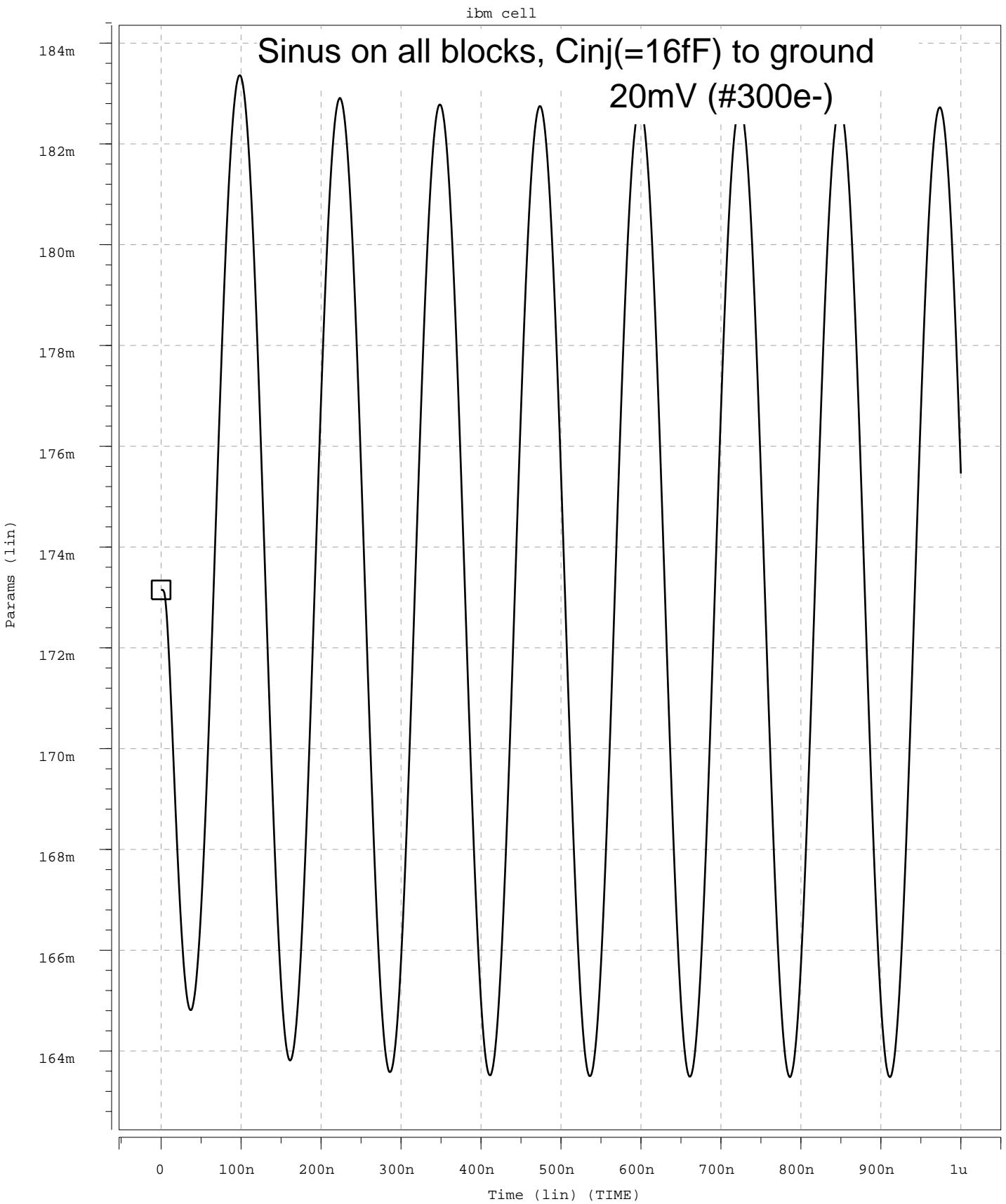


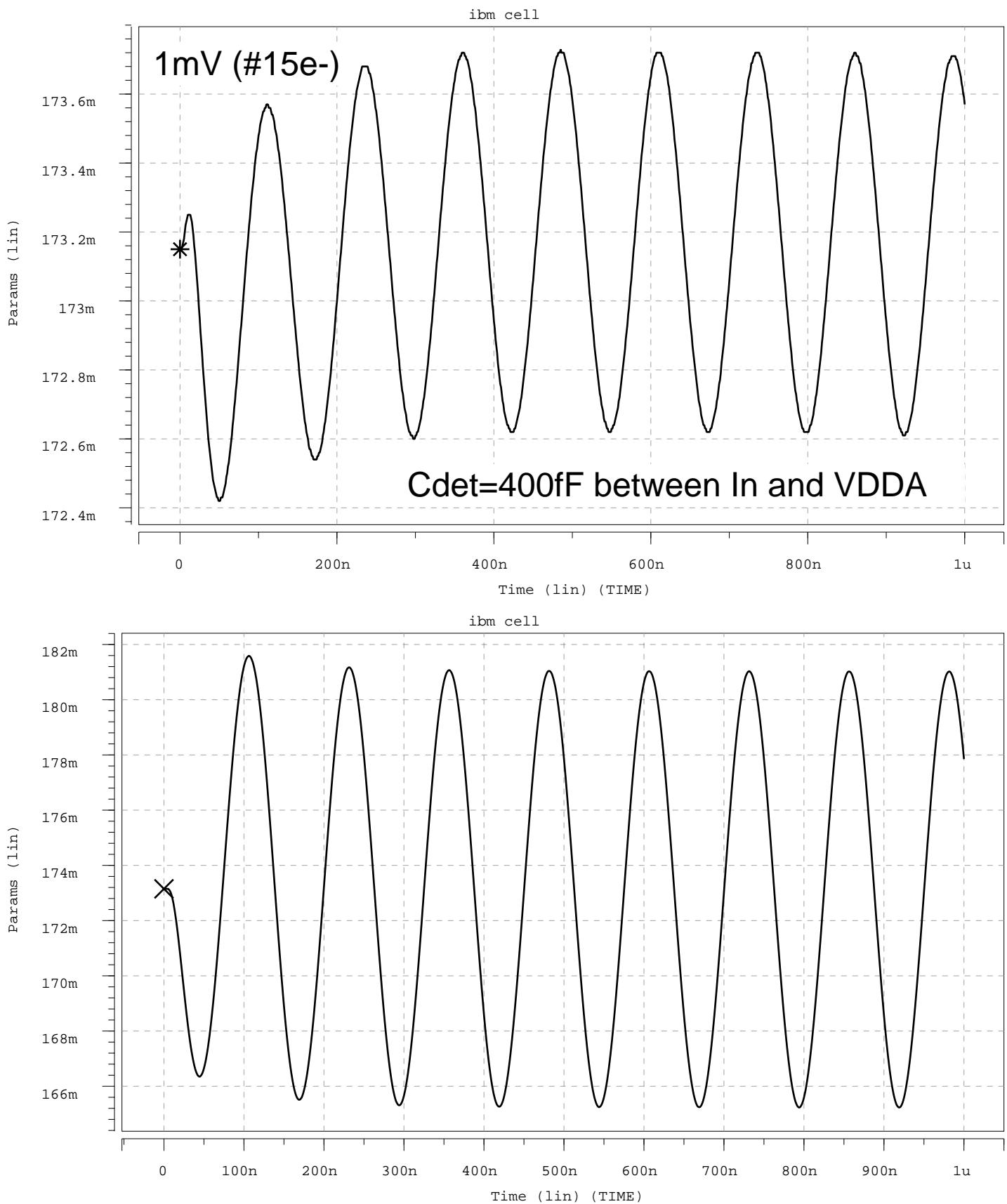


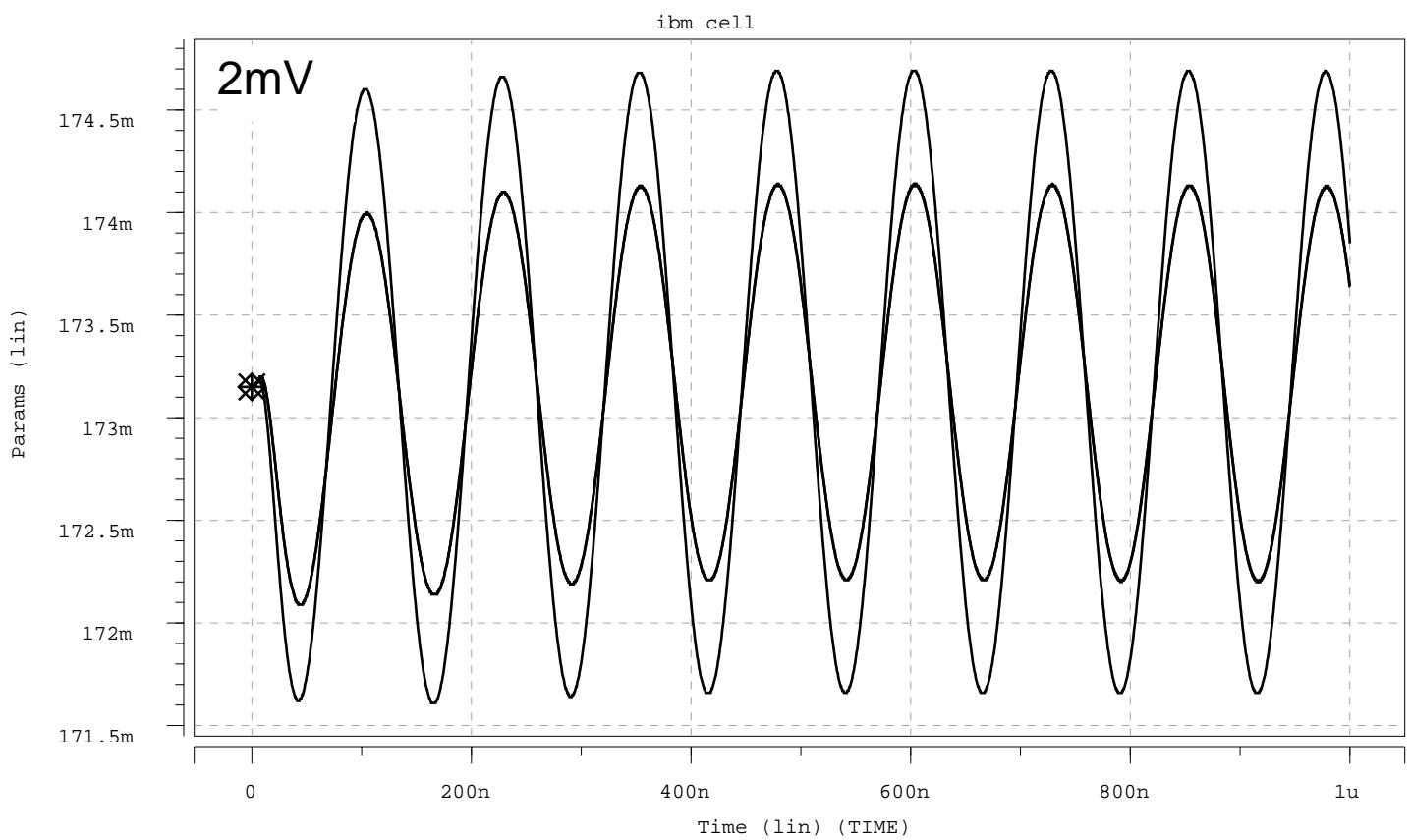
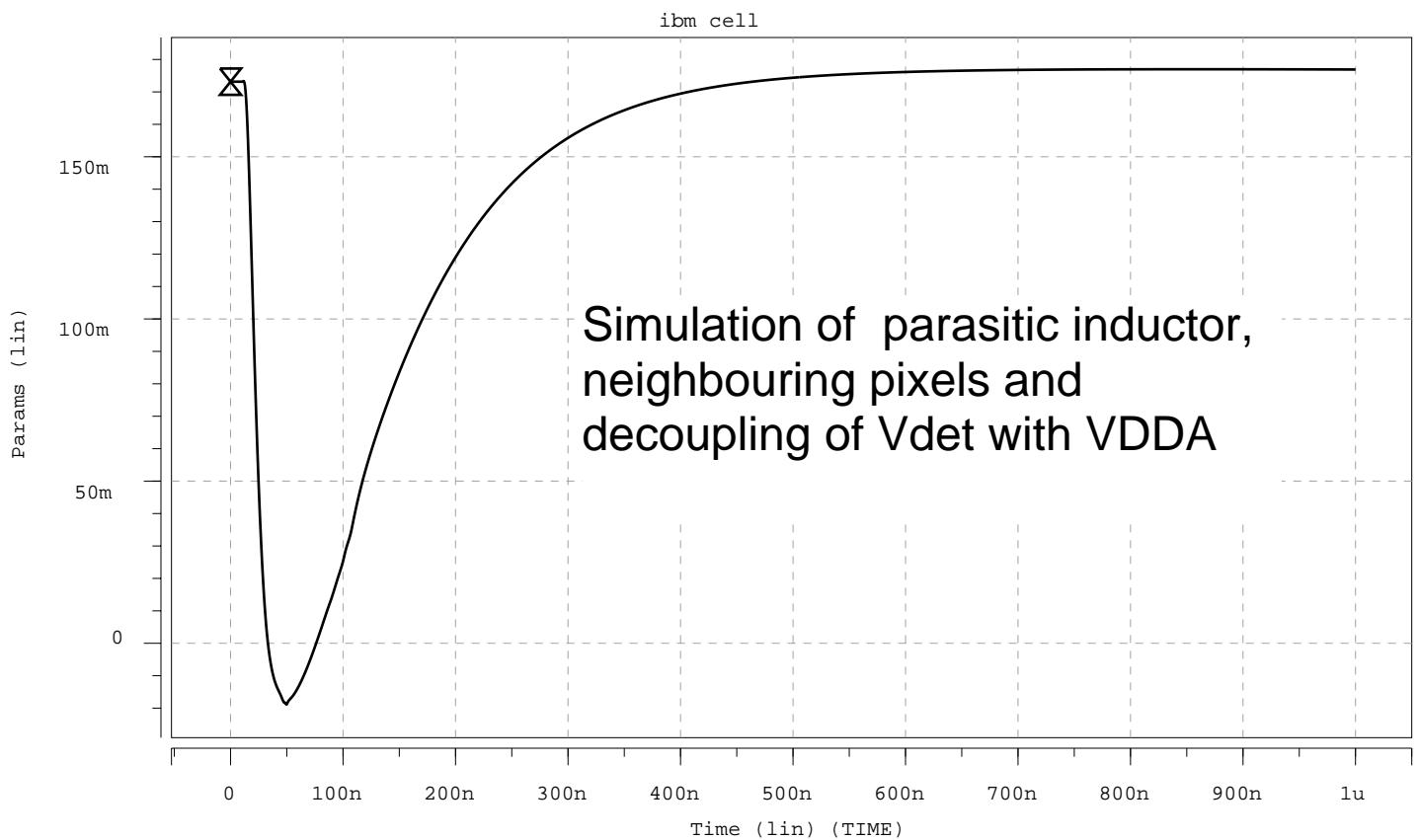




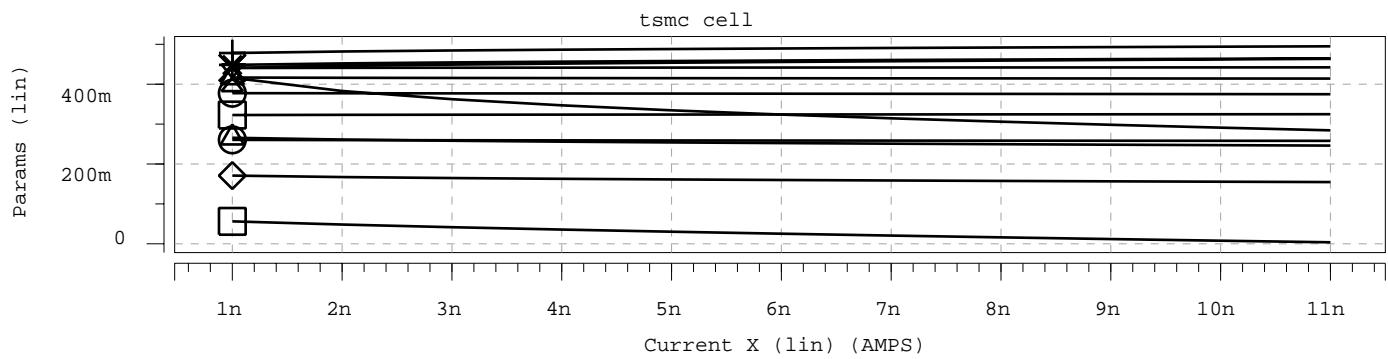
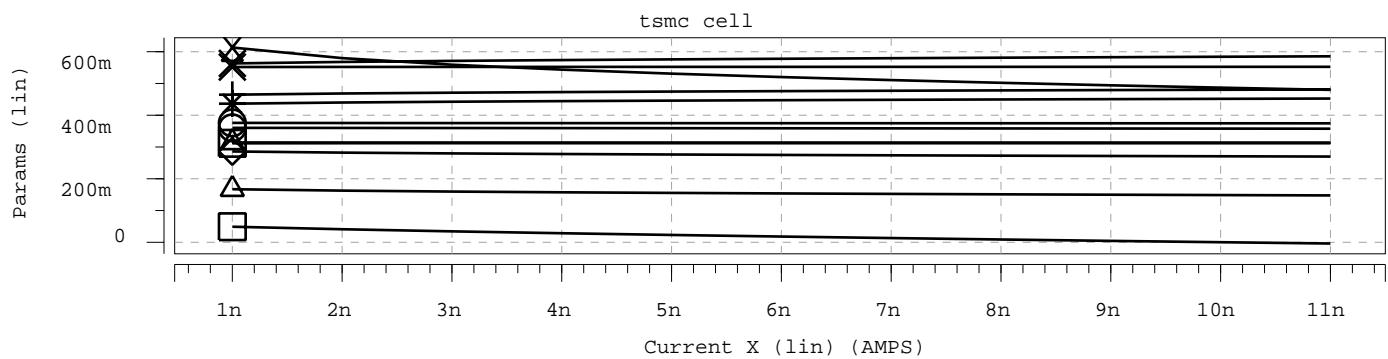
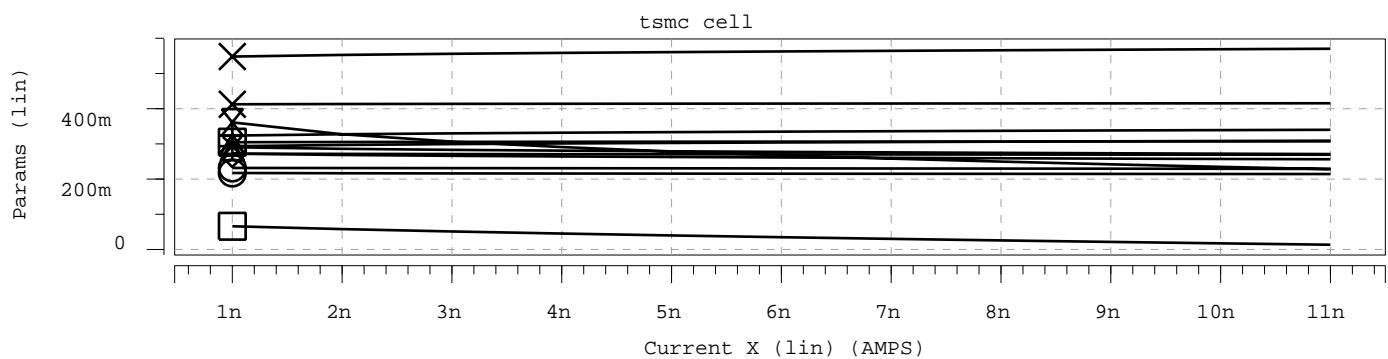
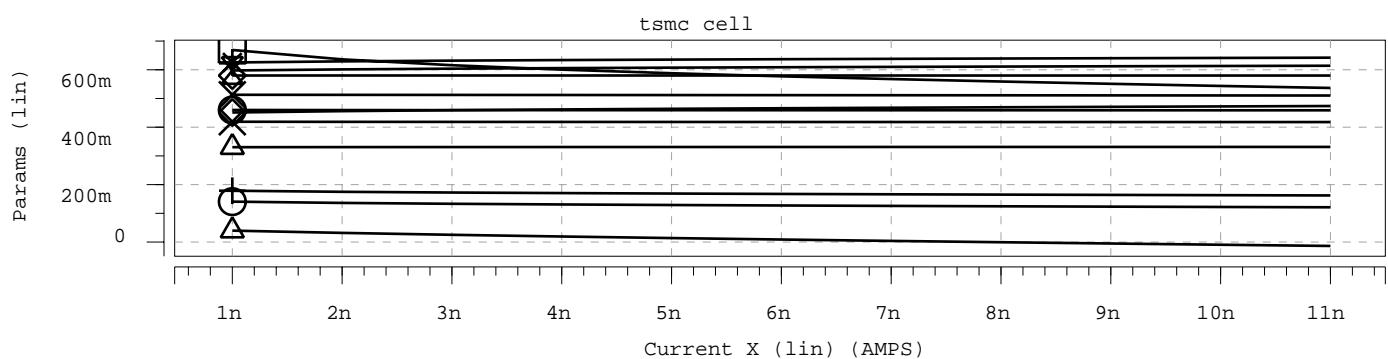
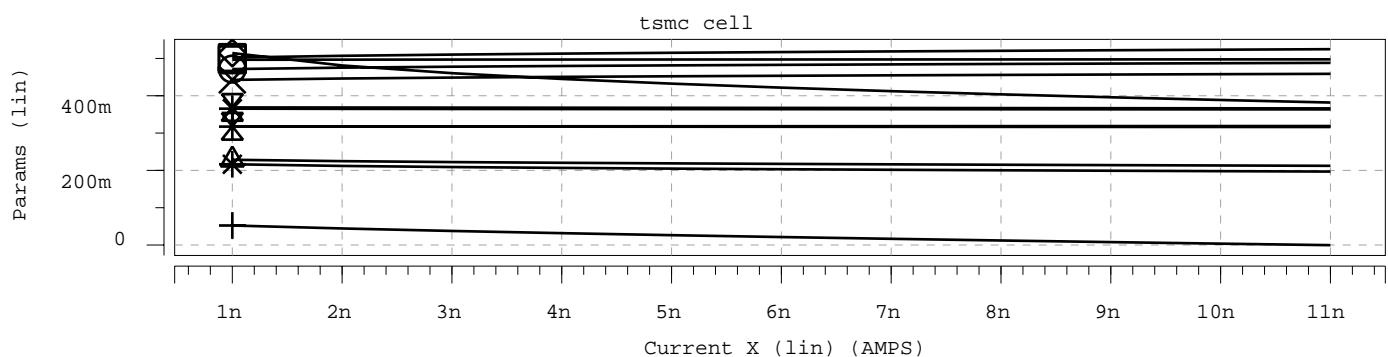


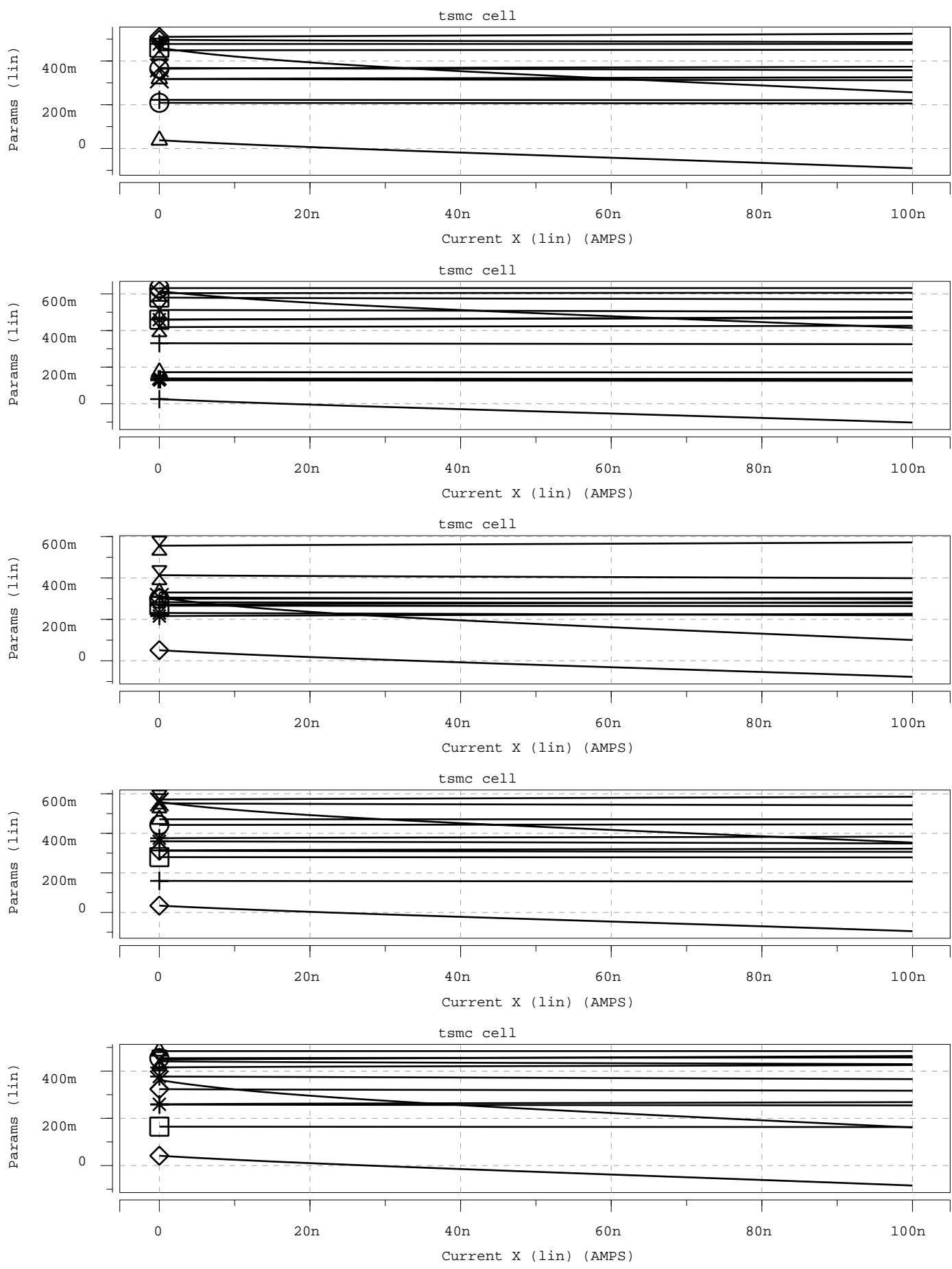


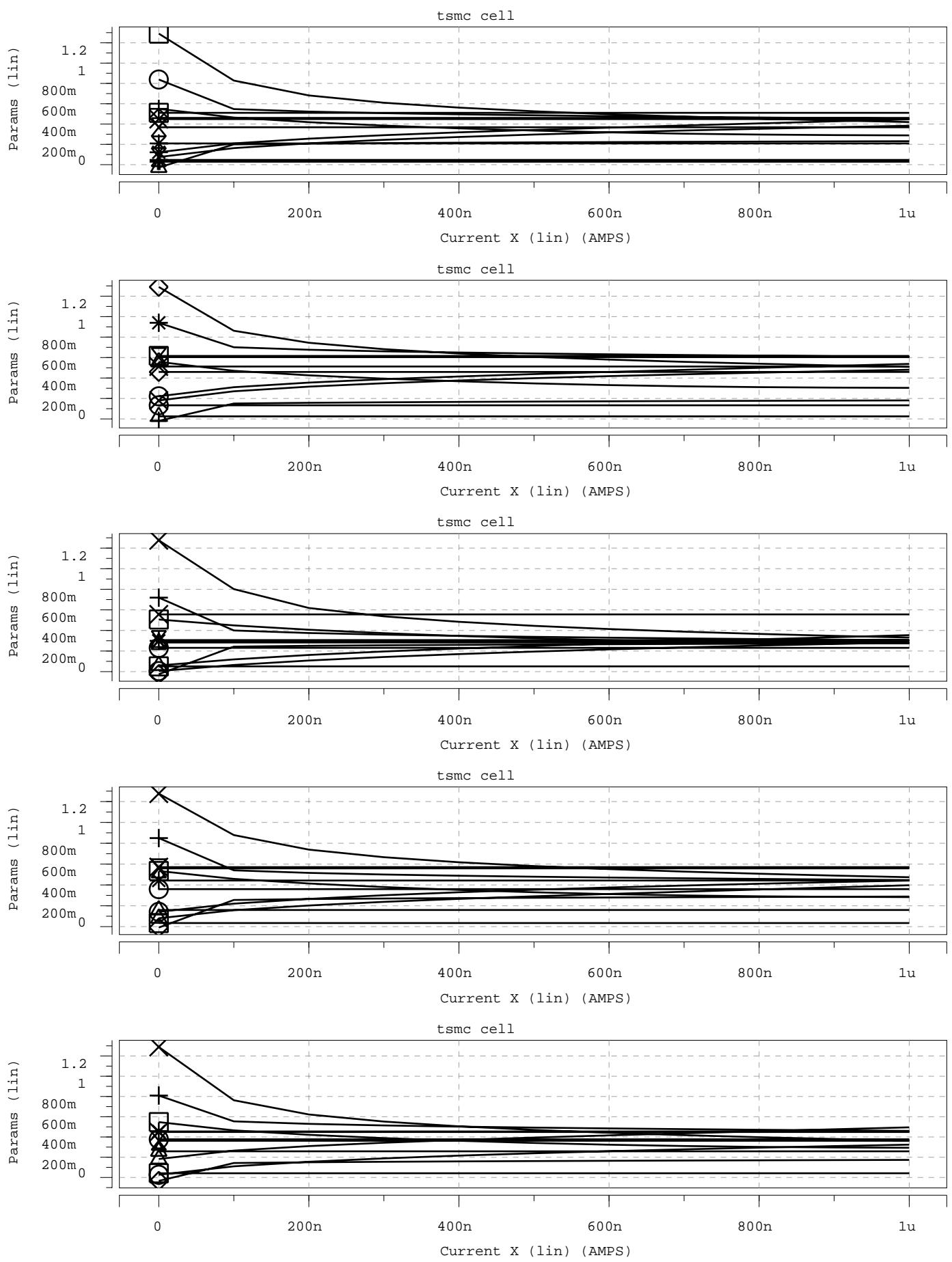




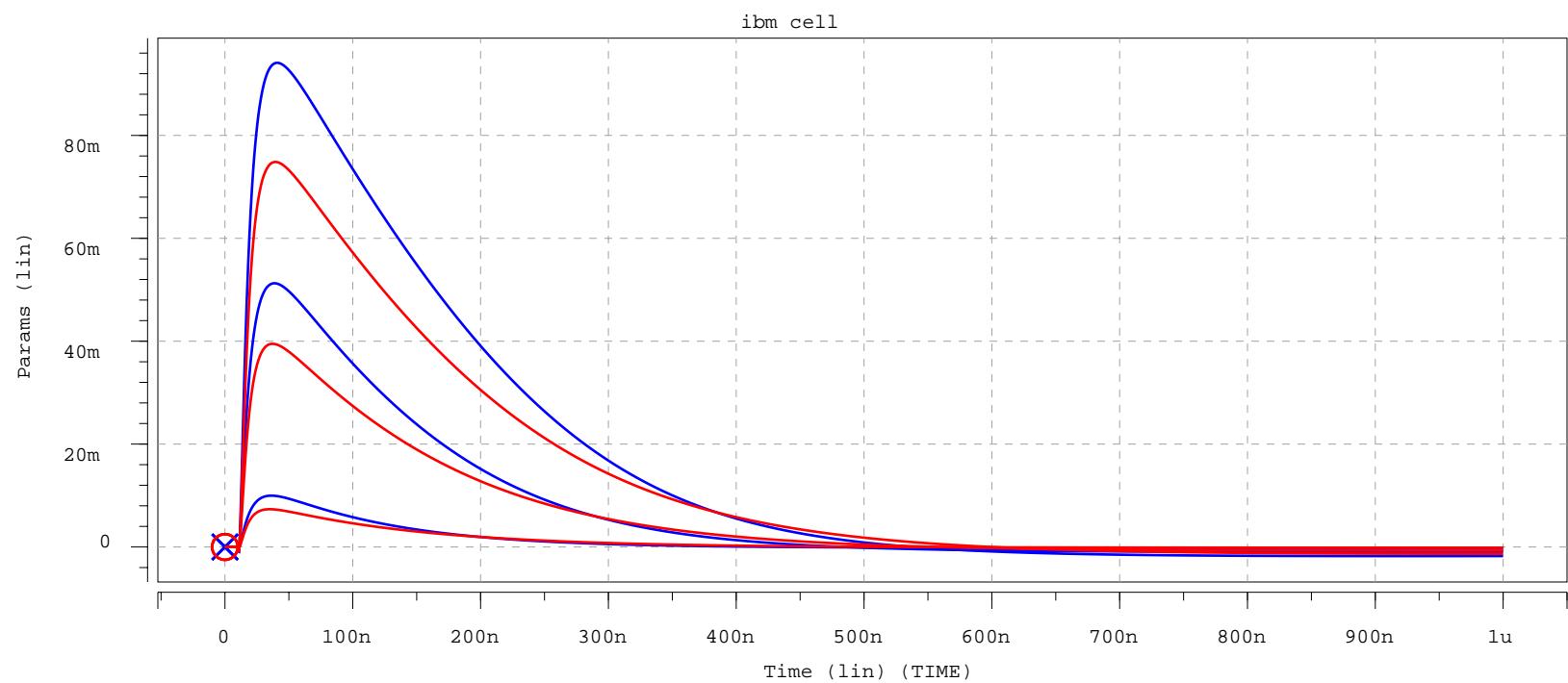
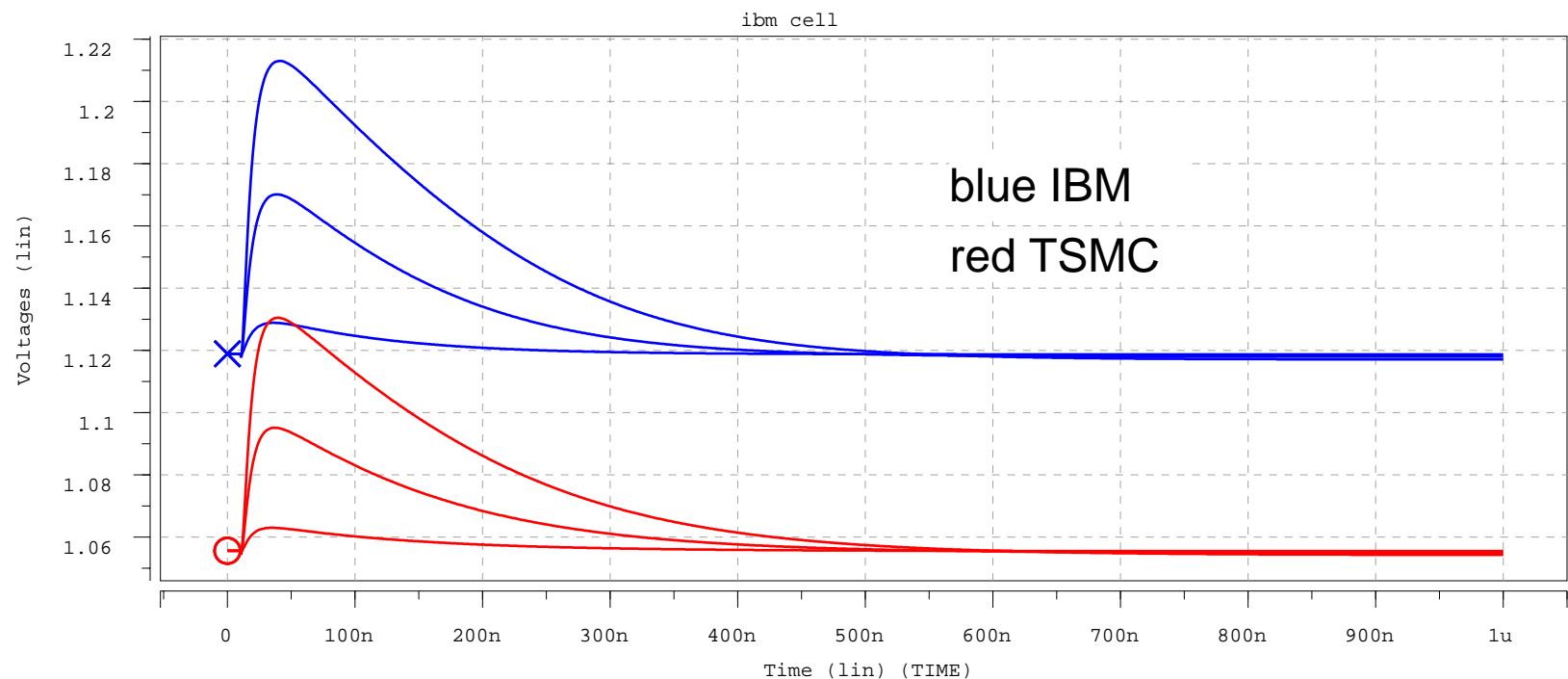
Corner Simulations

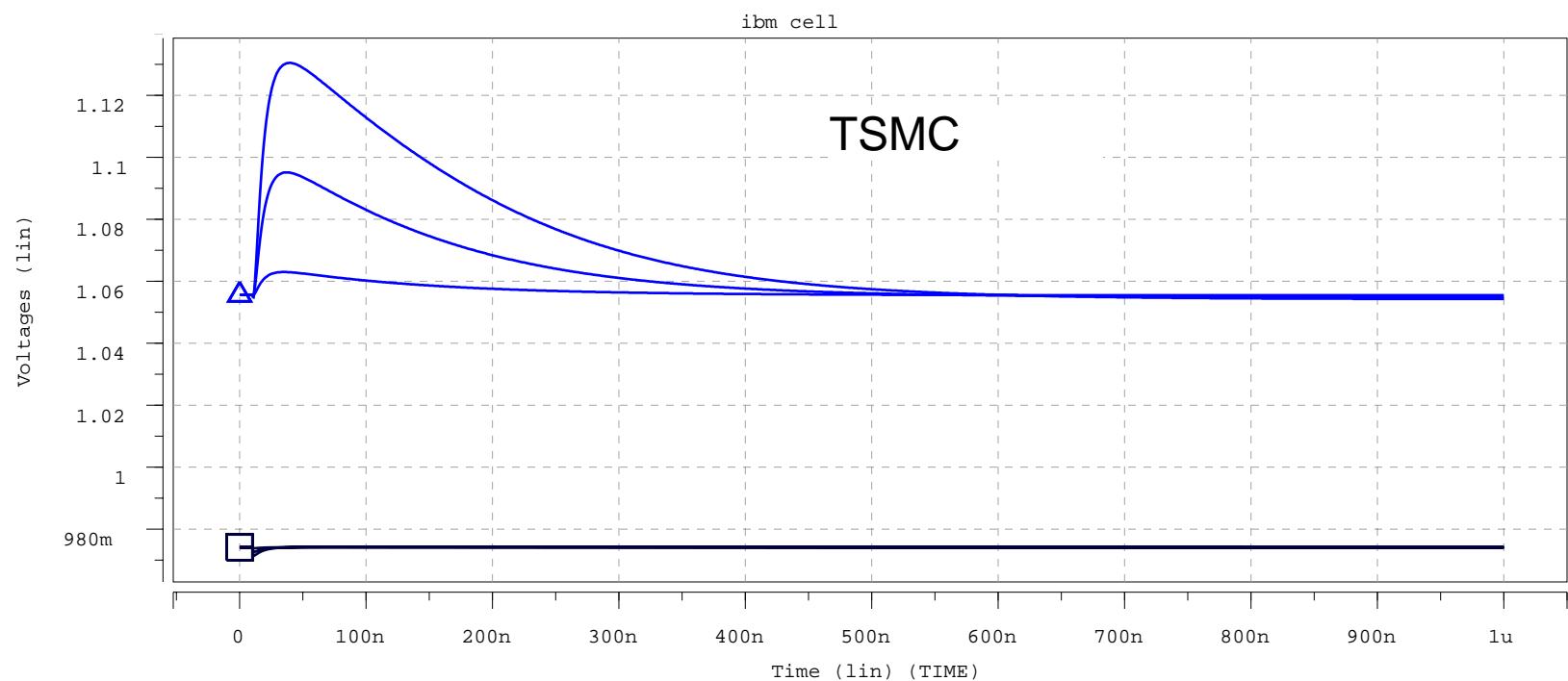
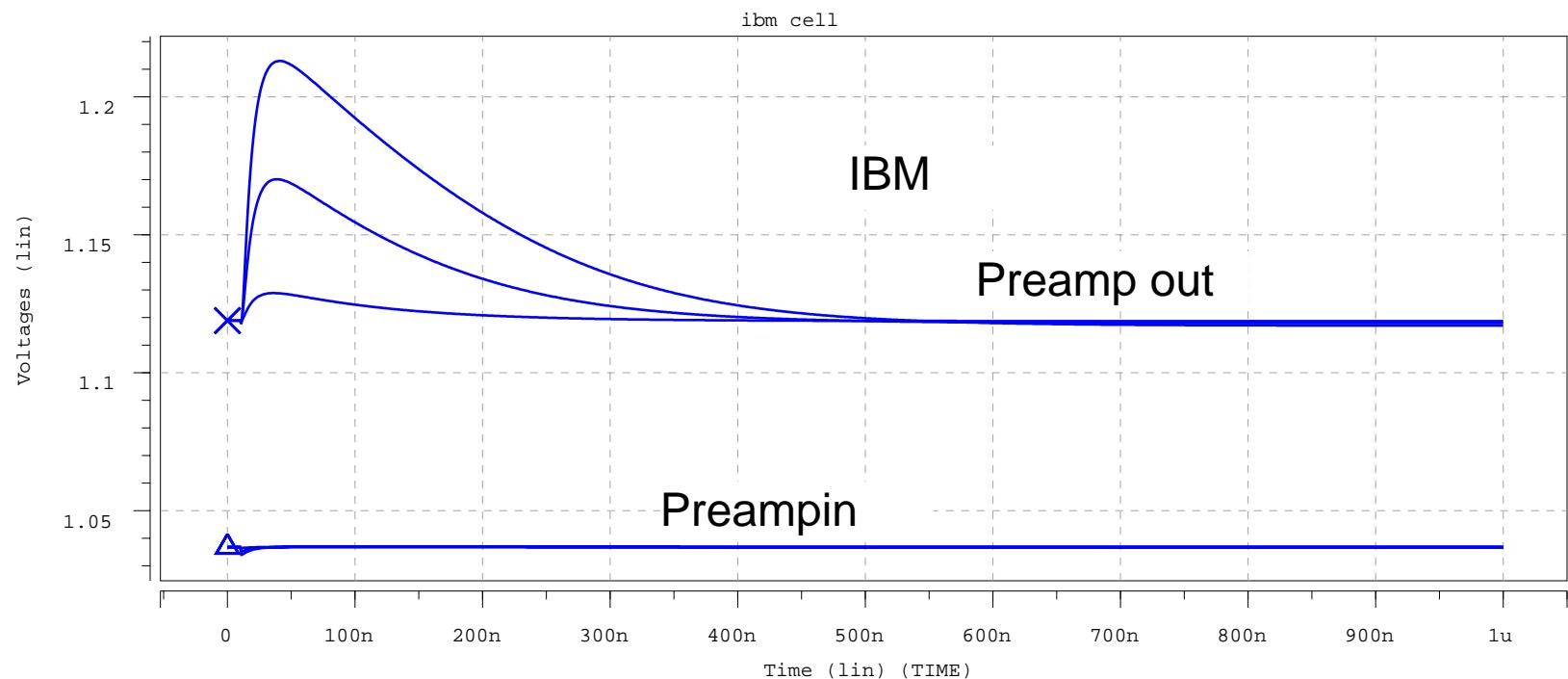


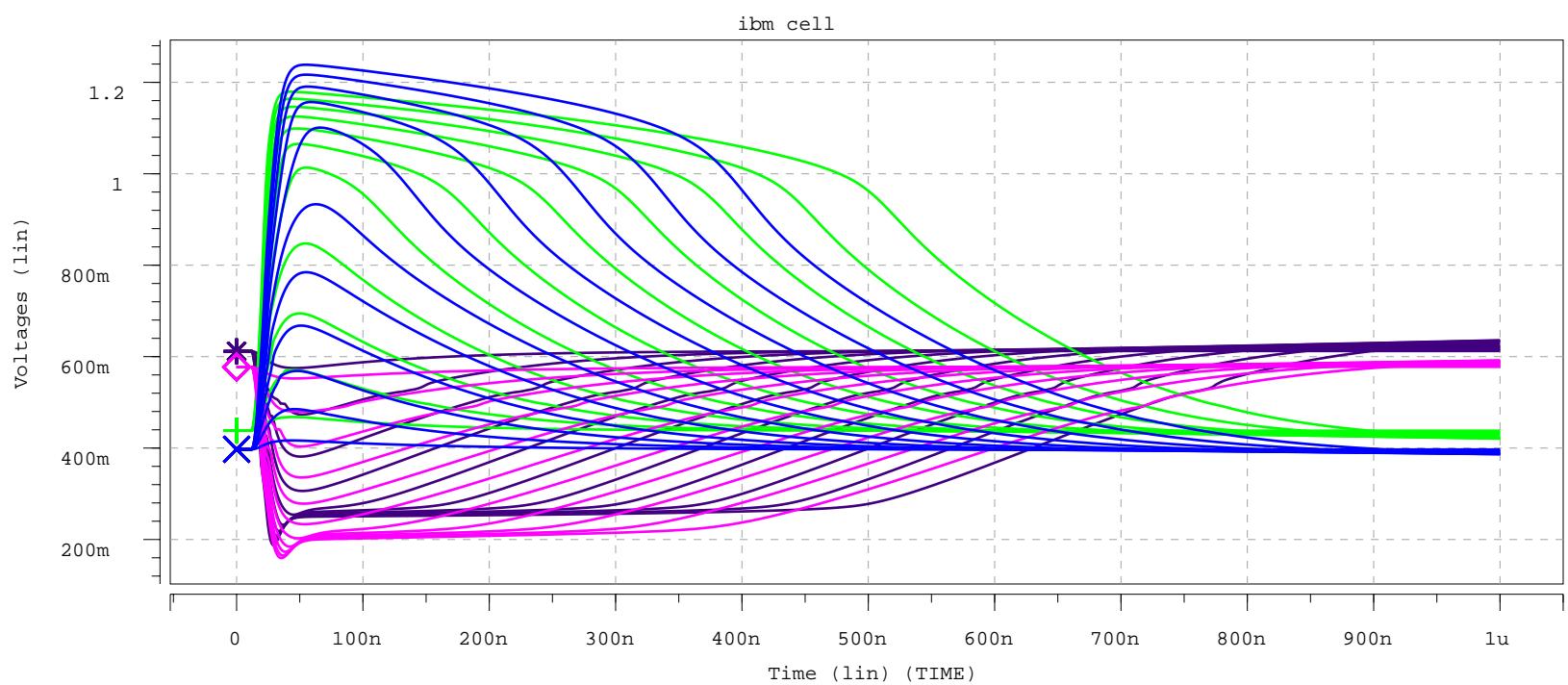
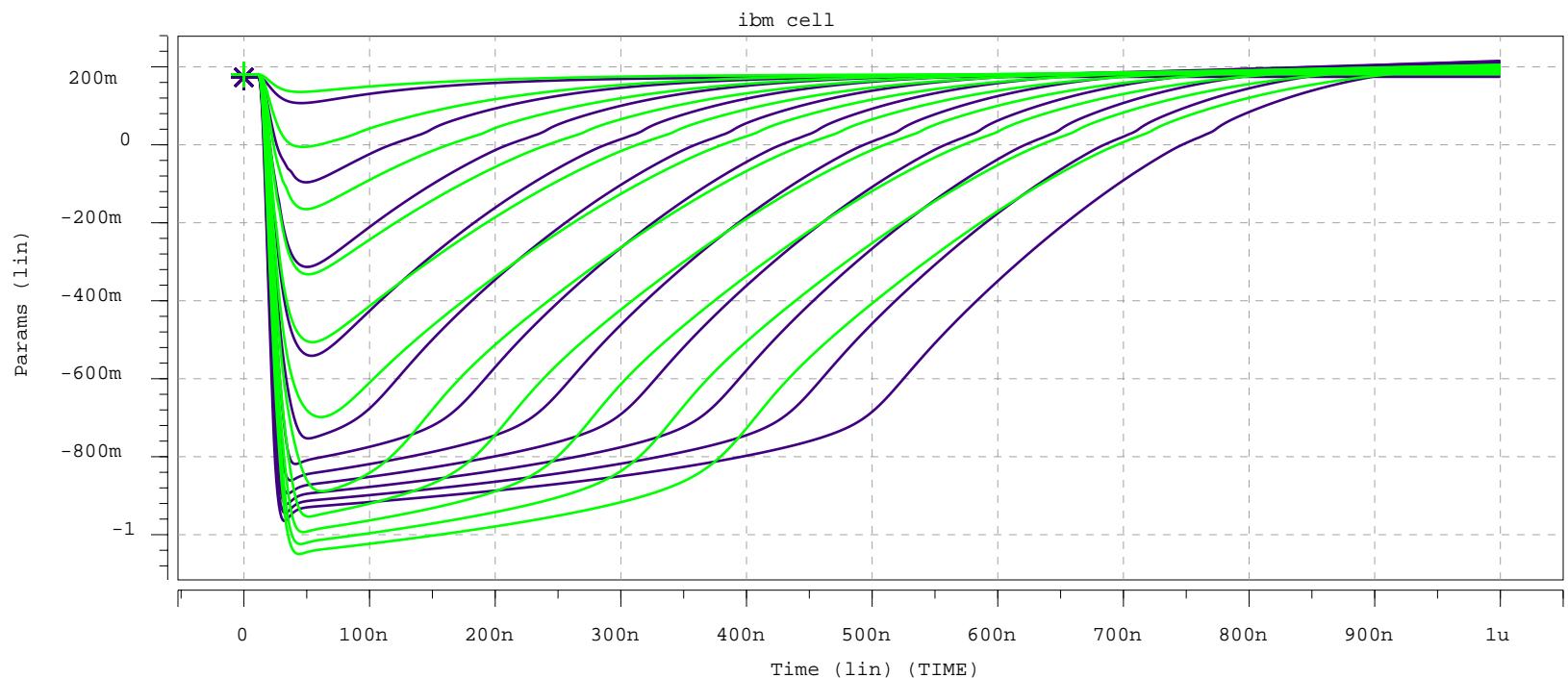


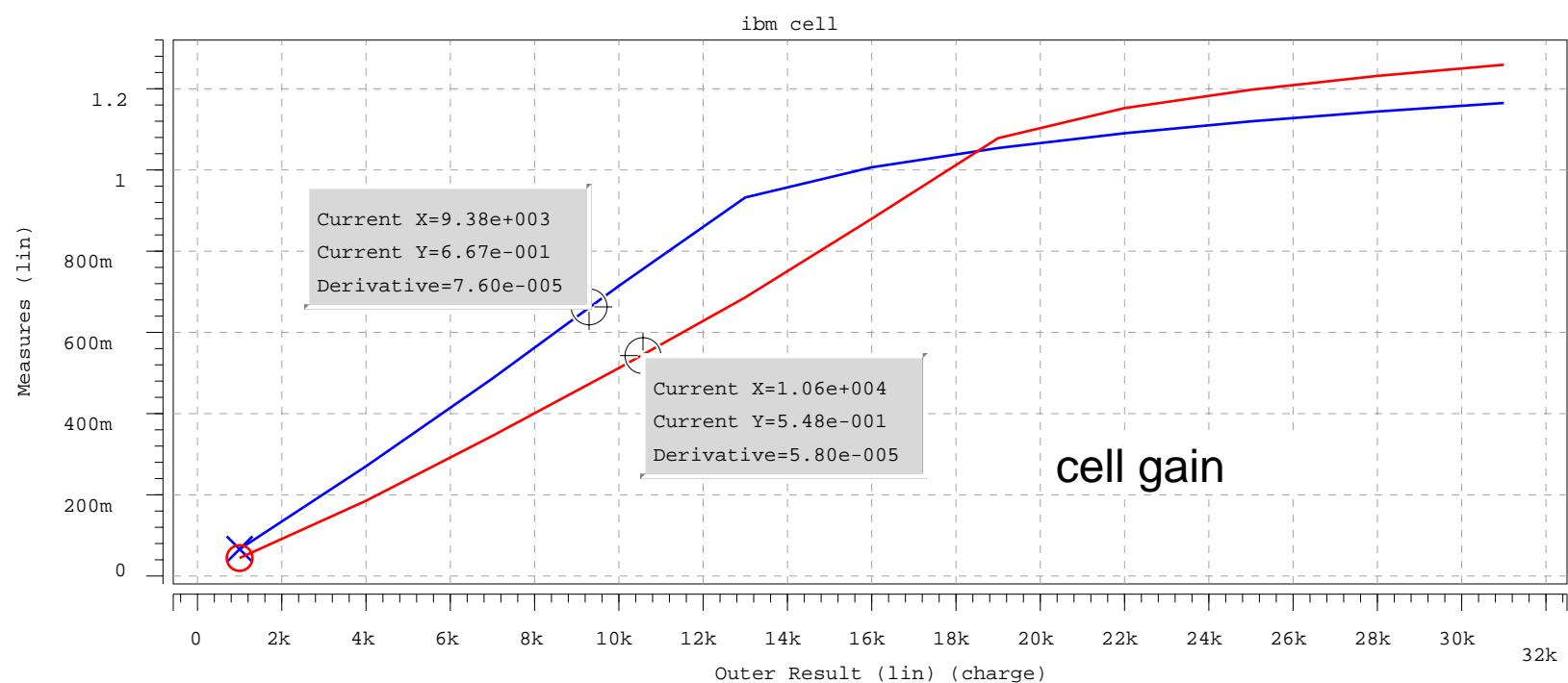
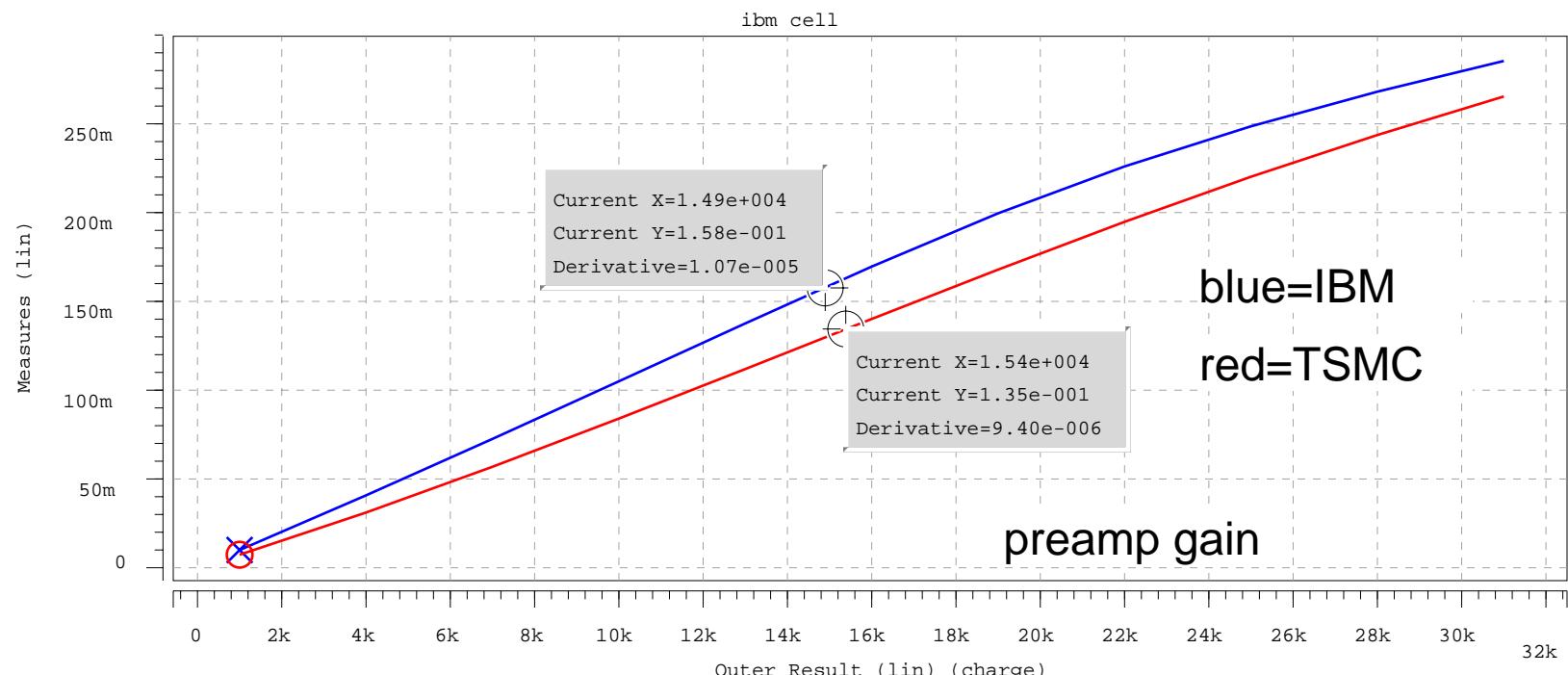


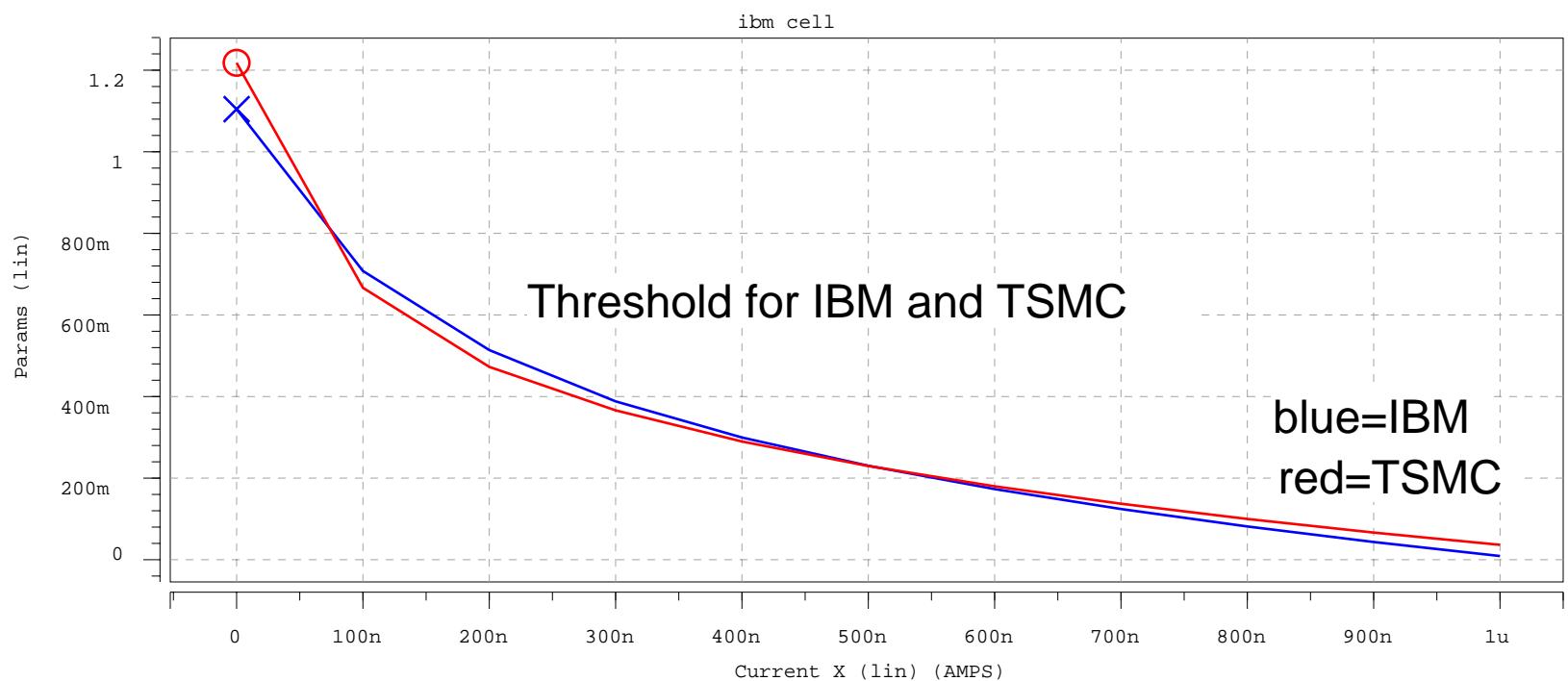
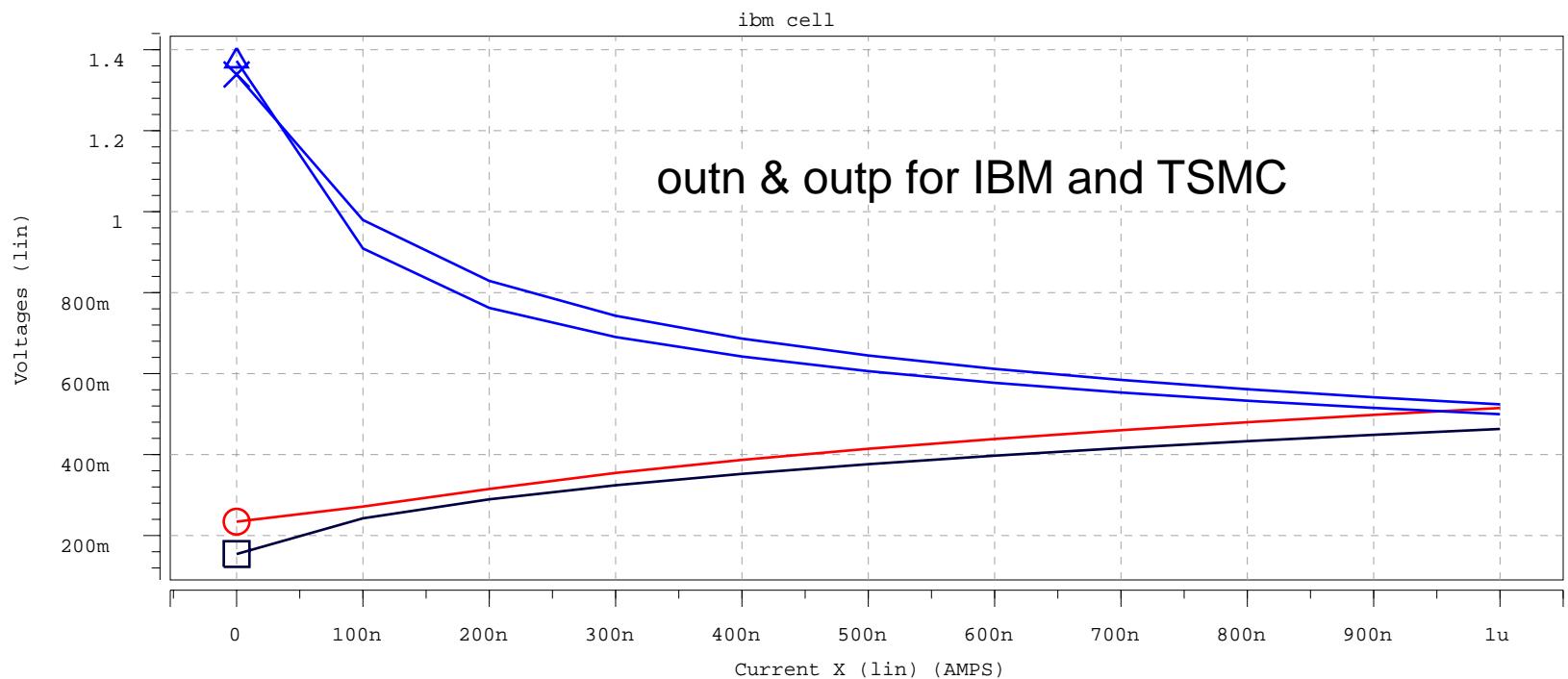
Comparison IBM/TSMC





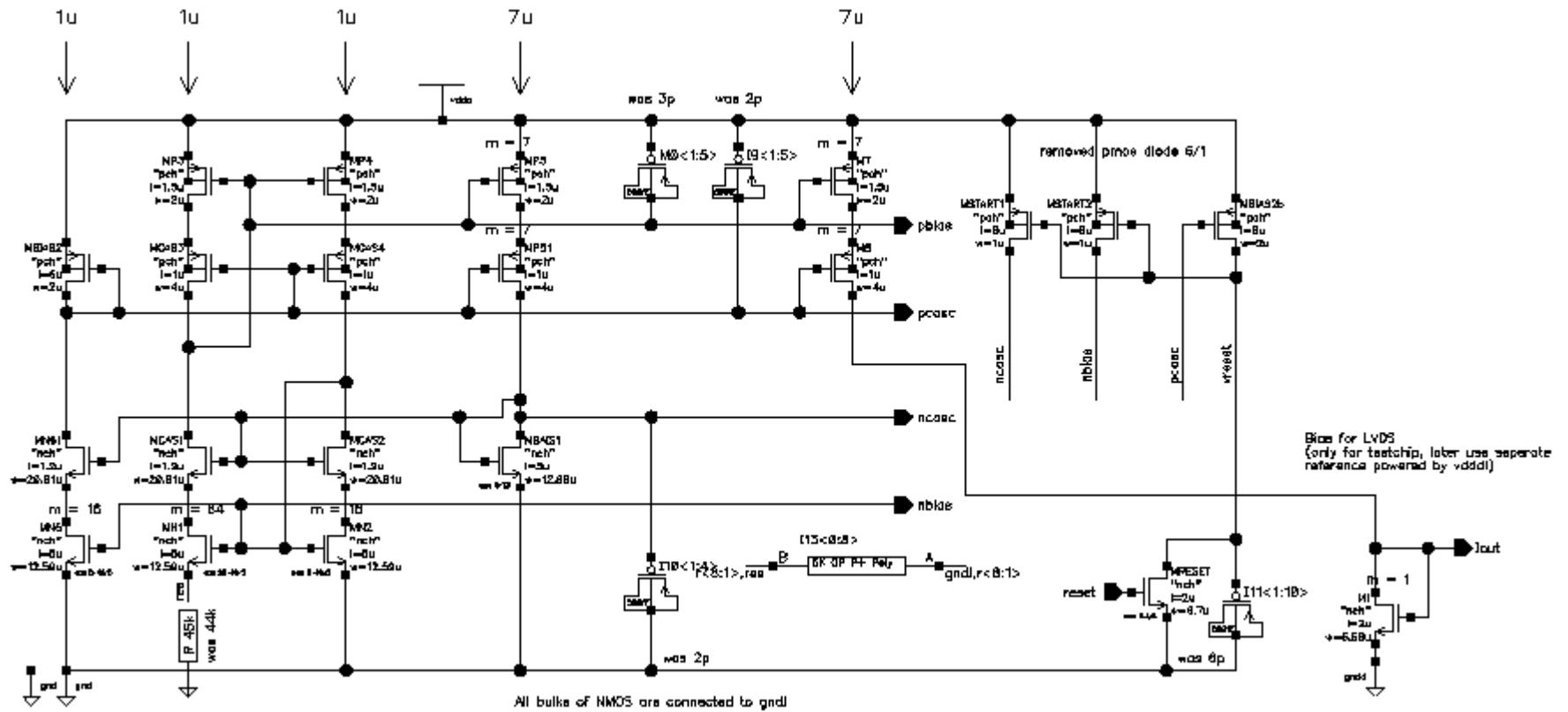




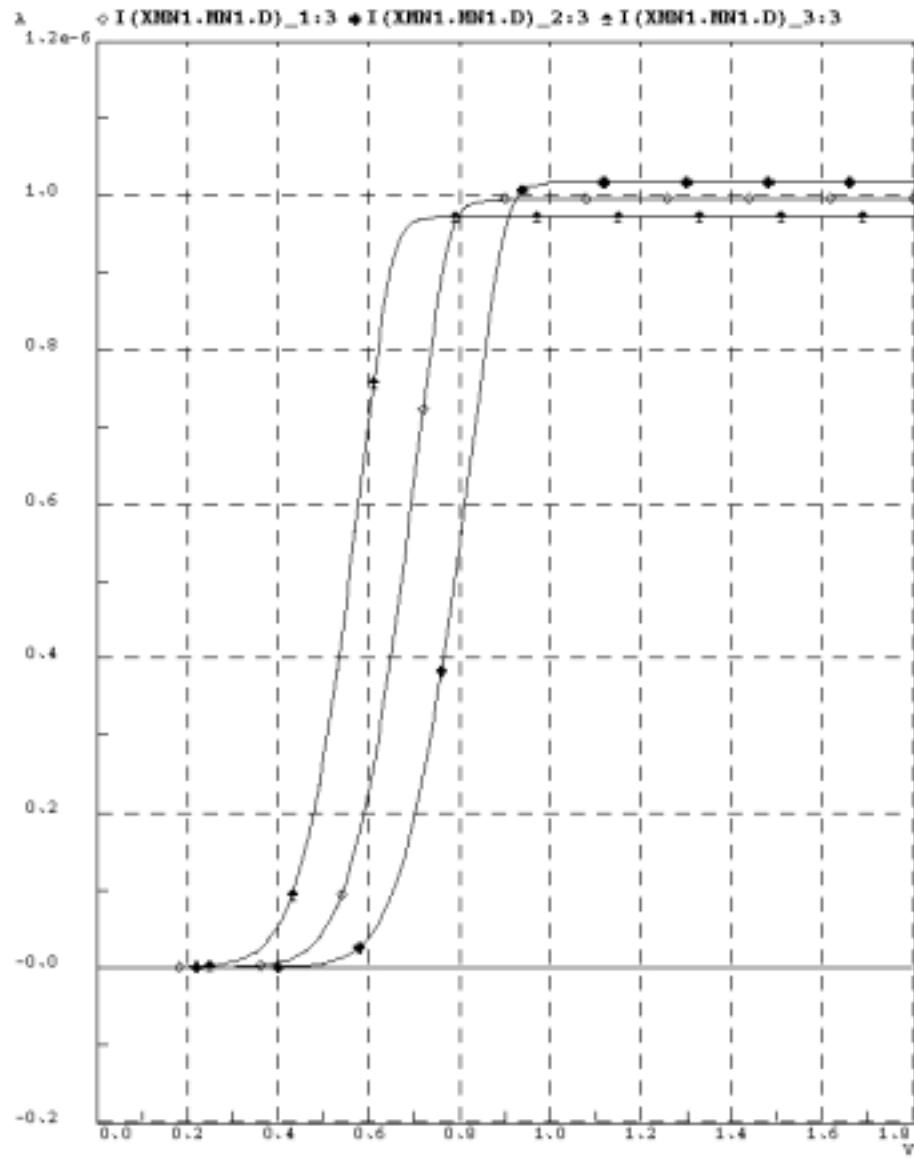


Current reference, LVDS I/O

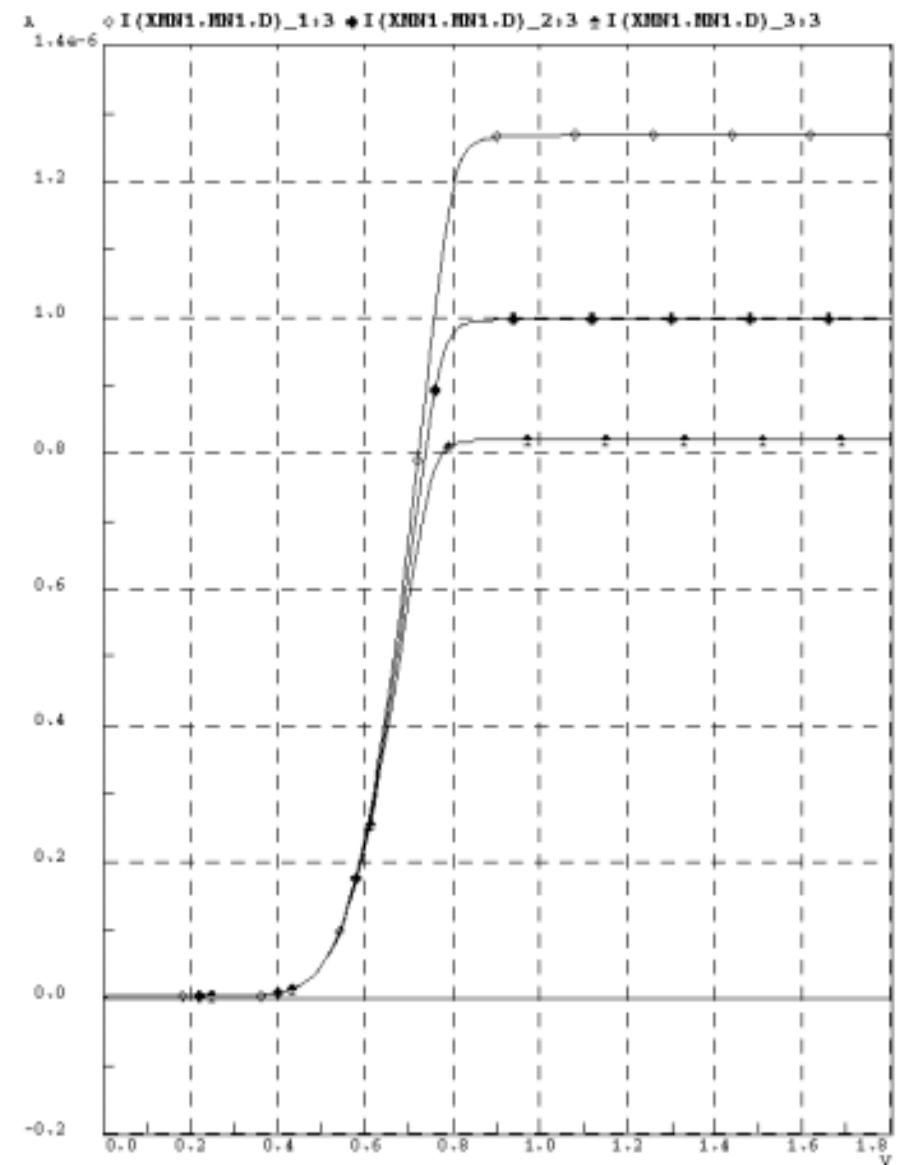
Current reference



- Start-up mechanism and external reset implemented
- Provide high output impedance and high dynamic range (wide-swing current mirror)

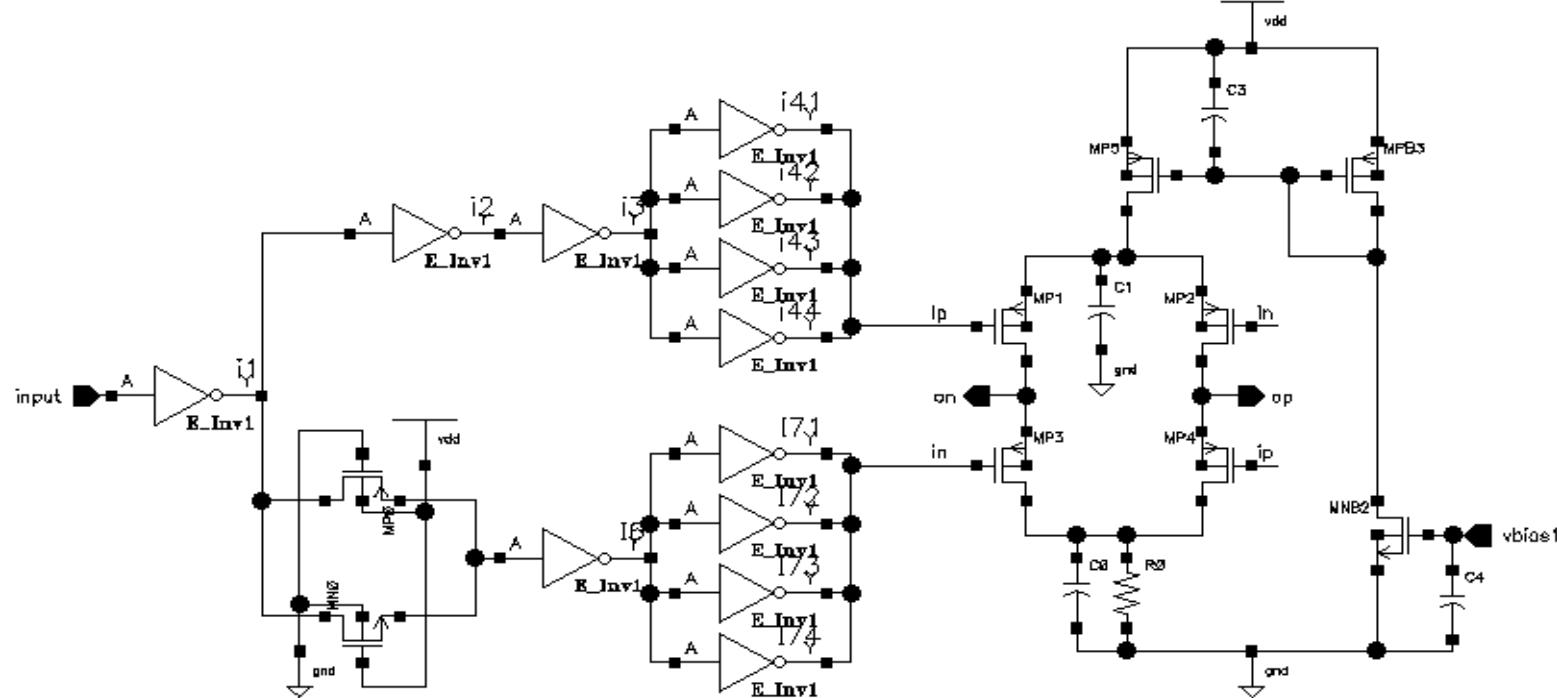


$\sigma = -3, 0$ and $+3$



$\Delta R_{\text{ref}} = \pm 20\%$

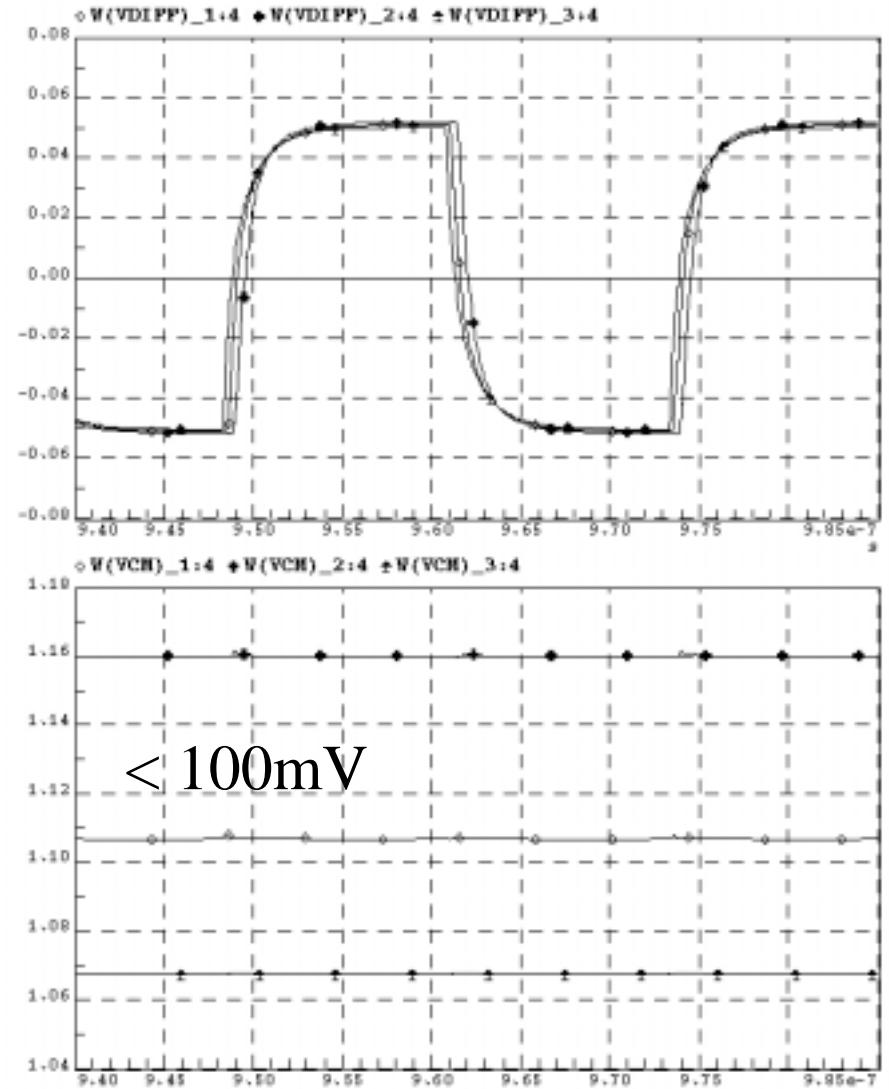
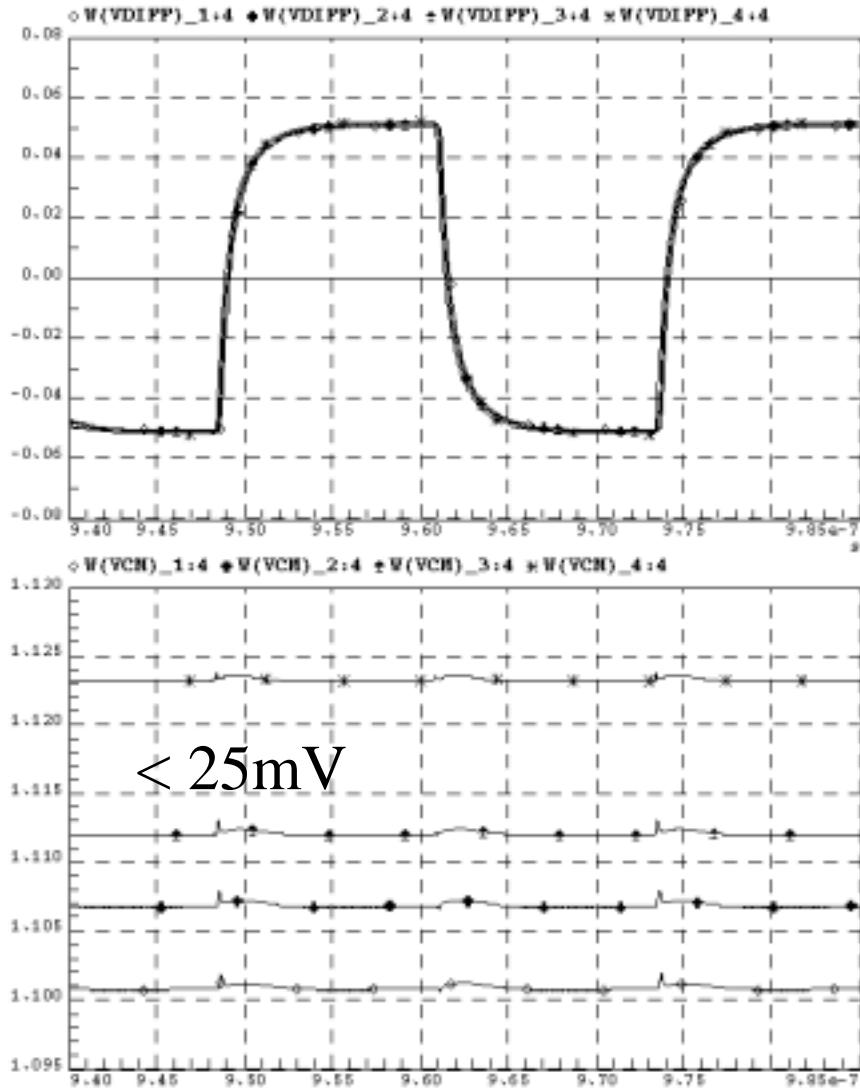
500 μ A LVDS driver



- Current controlled (LVDS I/O dedicated current reference)
- Common mode (set to 1.1V) independent of σ , power supply and reference resistor variation (same technique as for the chopper)
- 200Ω adapted

VDD=1.6, 1.8, 2 and 2.5V

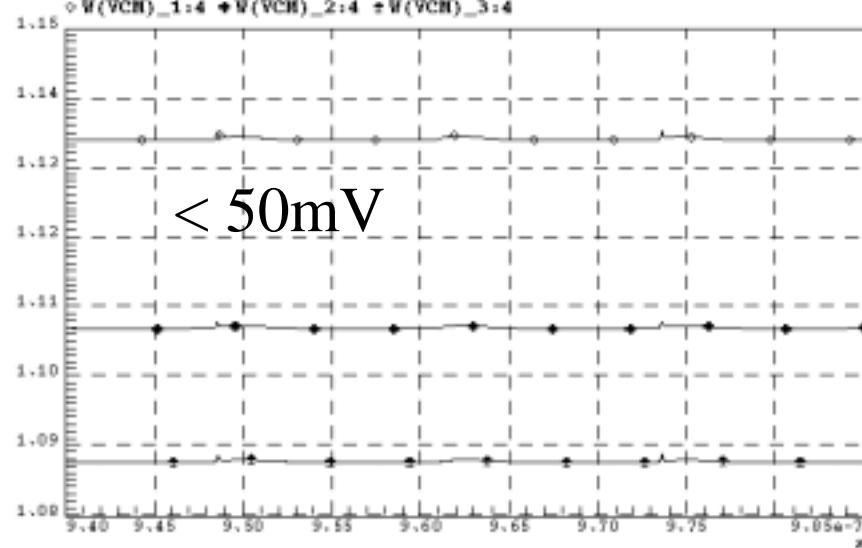
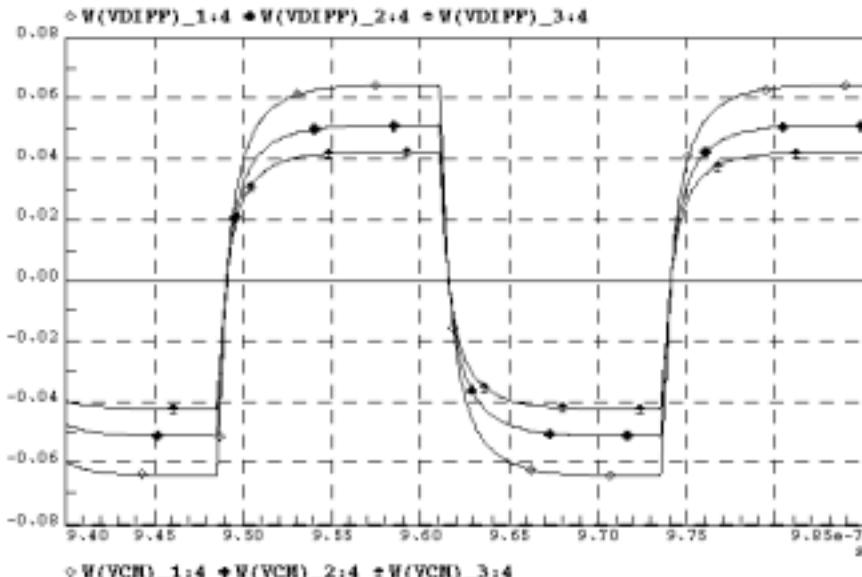
$\sigma = -3, 0$ and $+3$



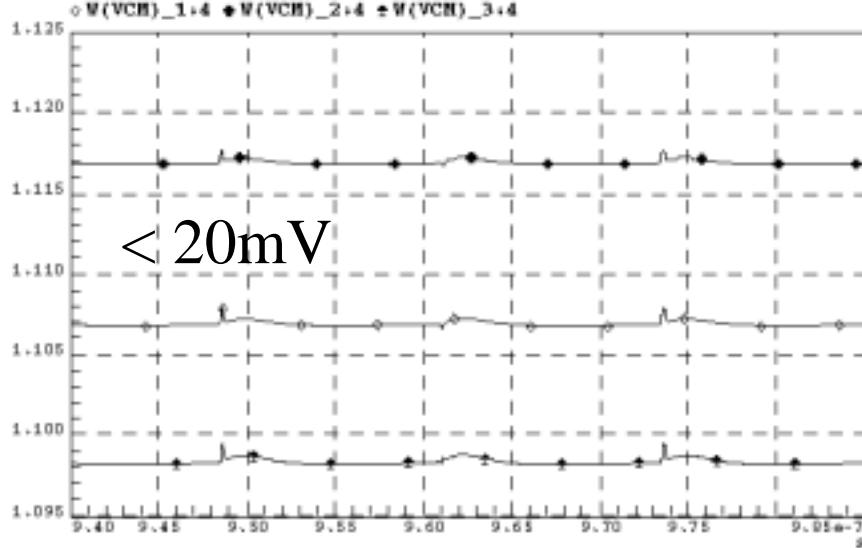
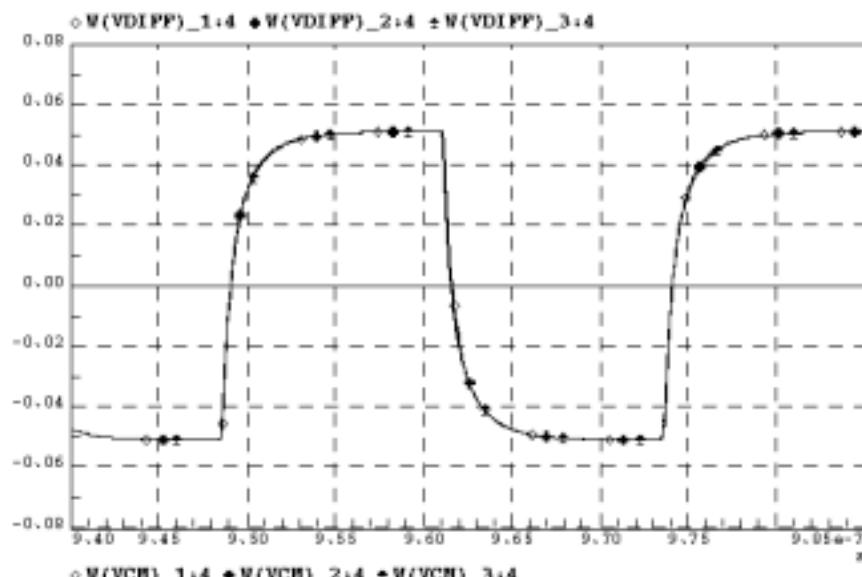
The current reference is attached

$\Delta R_{ref} = \pm 20\%$

$n_{PMIS} = -1, 0$ and $+1$

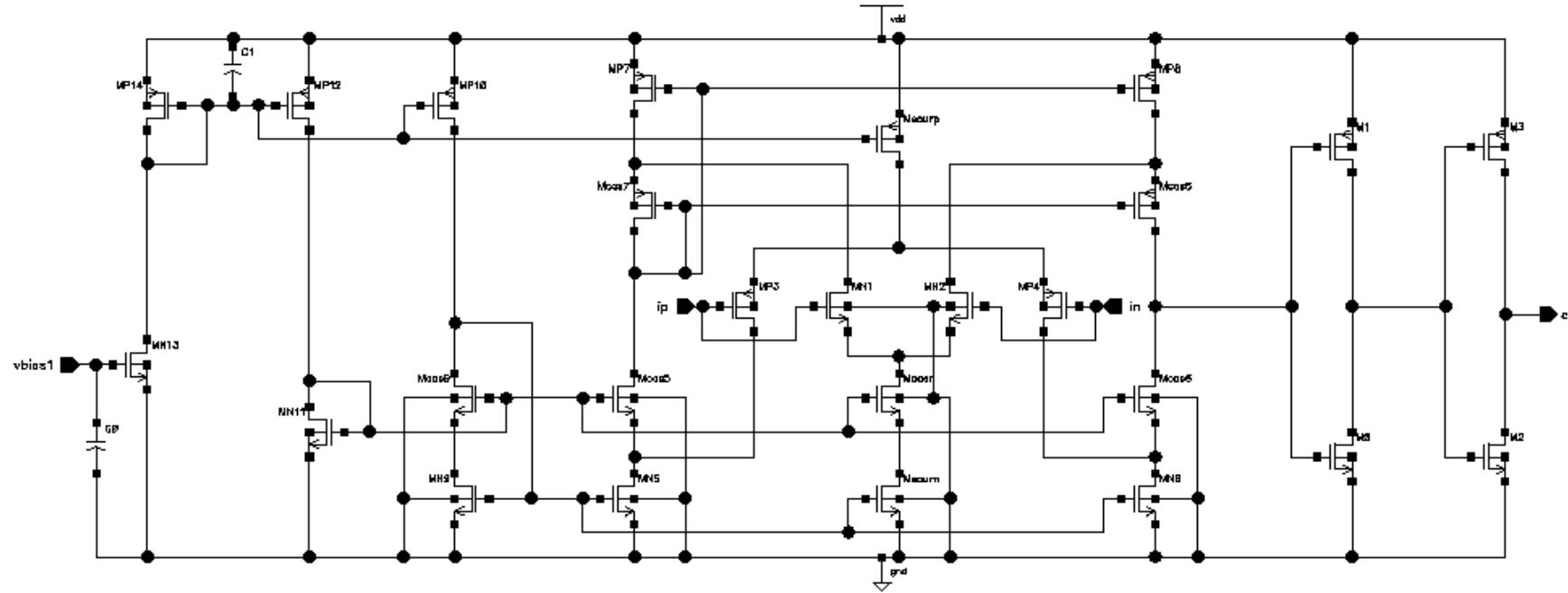


$< 50\text{mV}$



$< 20\text{mV}$

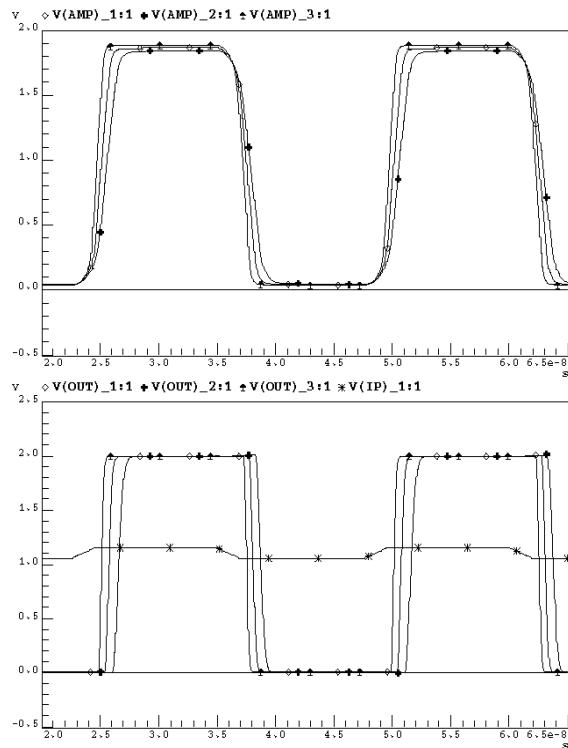
LVDS receiver



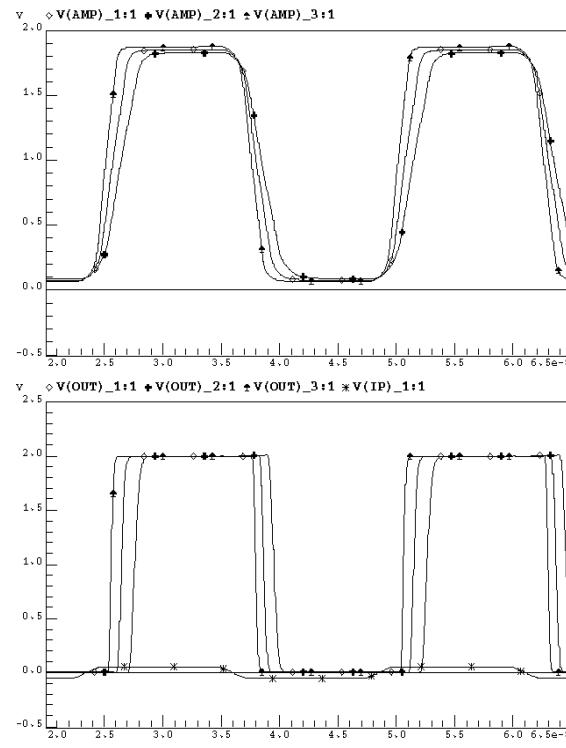
- Rail-to-rail feature
- Current controlled ($140\mu\text{A}$ per differential pair)
 - The receiver in the design kit has a quiescent current varying from 2.5mA per branch to 1.4mA at $\text{VDD}=2.5\text{V}$ (and $1.2\text{mA} <-> 0.5\text{mA}$ @ $\text{VDD}=2\text{V}$)
- Successful operation down to 1.4V

$V_{DD}=2V$

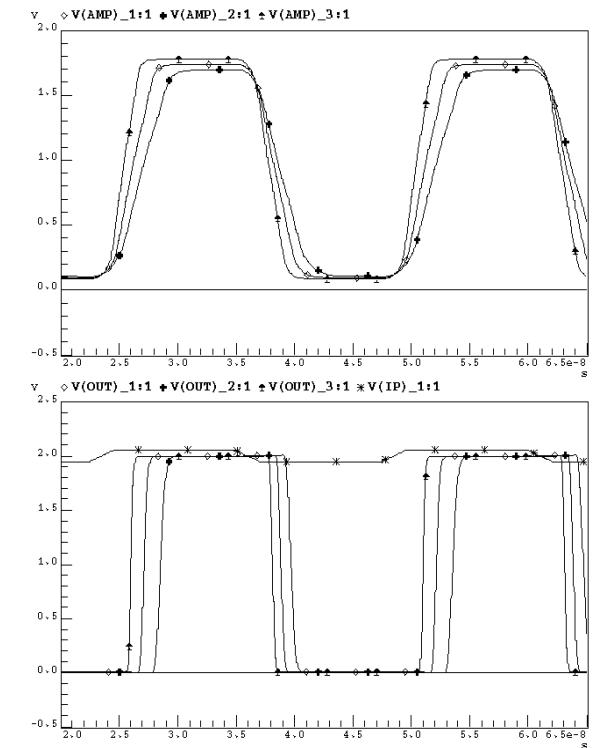
$\sigma = -3, 0$ and $+3$



$V_{cm}=1.1V$

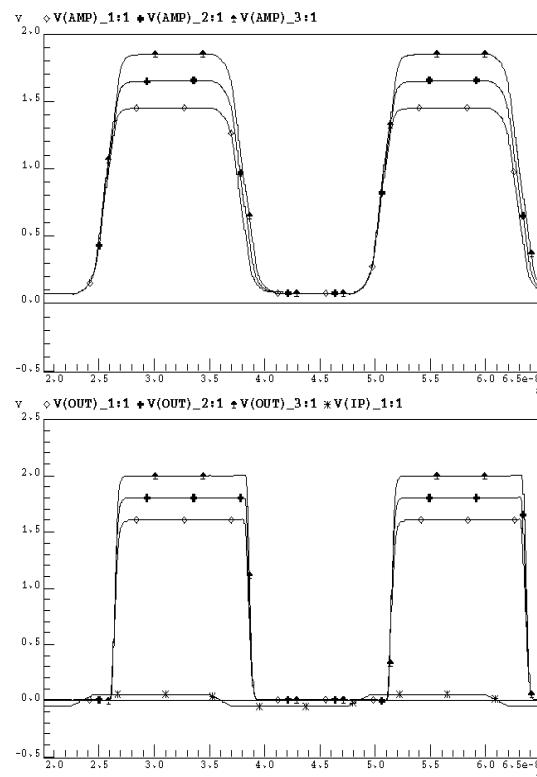
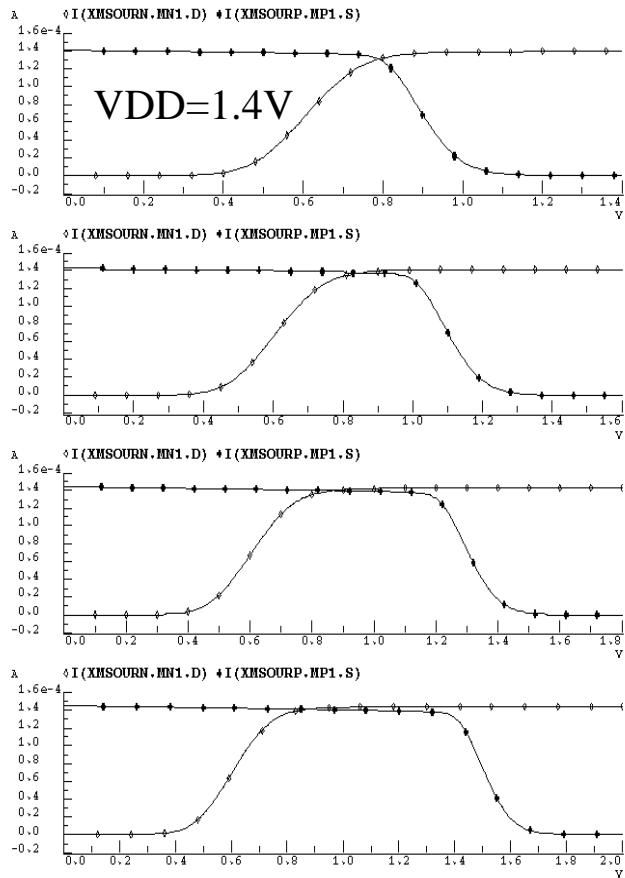


$V_{cm}=0V$

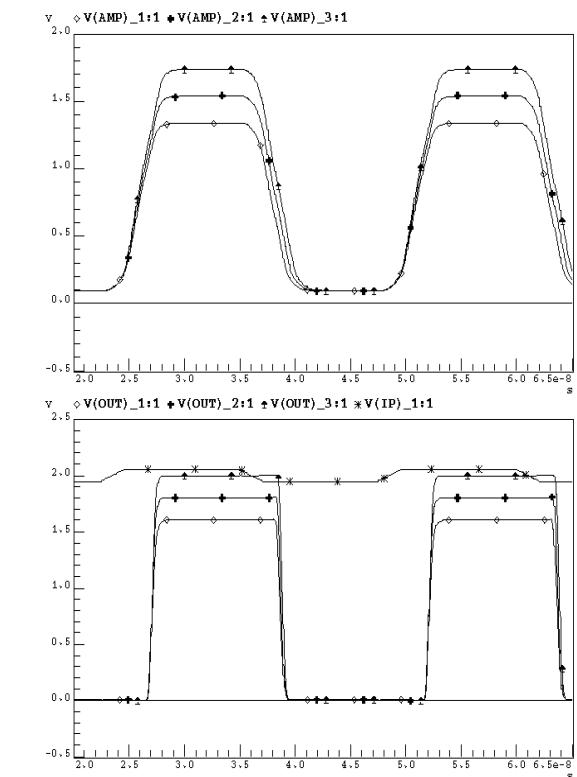


$V_{cm}=2V$

VDD=1.6, 1.8 and 2V



$V_{cm}=0V$



$V_{cm}=V_{dd}$